PRELIMINARY

NEC NEC LCD Technologies, Ltd.

TFT COLOR LCD MODULE

NL8060BC21-11C

21cm (8.4 Type) SVGA LVDS interface (1port)

PRELIMINARY DATA SHEET

DOD-PP-1046 (3rd edition)

This PRELIMINARY DATA SHEET is updated document from DOD-PP-0909(2)

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

REVISION HISTORY

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document number	Prepared date	Revision contents and	l signature
1st edition	DOD-PP- 0708	Jan. 8, 2009	Revision contents New issue	
			Writer Approved by Checked by H. FUKUYOSHI	Prepared by A. KUMANO
2nd edition	DOD-PP- 0909	Jan. 19, 2010	Revision contents P4 FEATURES • Color Conversion(Tentative name) (addition) P5 GENERAL SPECIFICATIONS • Weight: (330)g(typ.) → TBD g • Contrast ratio: (600:1)(typ.) →(900:1)(typ.) • Viewing angle: Vertical -Down side 60°(typ.) • Response time: 25ms(typ.) → (18)ms(typ.) • Luminance: At IL= 50mA → At IL= 50mA/One c • Backlight: Recommended LED driver board (0) • Power consumption: At IL= 50mA → At IL=50m (5.8)W (typ., Power dissipation of the inverted part o	ircuit Option): 104PW03F (addition) nA/One circuit er is not included.) → (5.6)W (typ.) →(300)(typ.), (450)(max.) =50mA/One circuit (Option) ned) n, MAP A O), 5(max.) typ), 21(max.) O(min.), 80(typ.) circuit, (Ambient temperature of the product)
			P29 Other • ③LED hoider. →lamp holder set. (cor • ④ Pay attention screw.(elimination) P30-P31 Outline Drawings (revised) Writer Approved by T. OGAWA ——————————————————————————————————	Prepared by T. OGAWA

INTRODUCTION

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The products are classified into three quality grades: "Standard", "Special", and "Specific" of the highest grade of a quality assurance program at the choice of a customer. Each quality grade is designed for applications described below. Any customer who intends to use a product for application other than that of Standard quality grade is required to contact an NEC sales representative in advance.

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Examples: Control systems for transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, medical equipment not specifically designed for life support, safety equipment, etc.

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Examples: Military systems, aircraft control equipment, aerospace equipment, nuclear reactor control systems, medical equipment/devices/systems for life support, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.

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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL8060BC21-11C is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

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The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

For industrial use

1.3 FEATURES

- Adoption of ST-NLT (Super-Transmissive Natural Light TFT)
- Long life LED backlight type
- High luminance
- High contrast
- Low reflection
- Wide viewing angle
- Wide temperature range
- LVDS interface
- Reversible-scan direction
- Selectable 8bit or 6bit digital signals for data of RGB
- Replaceable lamp for backlight
- Color Enhancement
- Acquisition product for UL60950-1 /CSA C22.2 No.60950-1-3 (File number: E170632)
- Compliant with the European RoHS directive (2002/95/EC)

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2. GENERAL SPECIFICATIONS

Display area	170.4 (H) × 127.8 (V) mm
Diagonal size of display	21cm (8.4 inches)
Drive system	a-Si TFT active matrix
Display color	16,777,216 colors (At 8-bit input, FRC terminal= High) 262,144 colors (At 6-bit input, FRC terminal= Low or Open)
Pixel	800 (H) × 600 (V) pixels
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe
Dot pitch	0.071 (H) × 0.213 (V) mm
Pixel pitch	0.213 (H) × 0.213 (V) mm
Module size	200.0 (W) × 152.0 (H) × 8.2 (D) mm (typ.)
Weight	260g (typ.)
Contrast ratio	800: 1(typ.)
Viewing angle	At the contrast ratio ≥10:1 • Horizontal: Right side 80° (typ.), Left side 80° (typ.) • Vertical: Up side 80° (typ.), Down side 80° (typ.)
Designed viewing direction	 At DPS terminal= Low or Open: Normal scan Viewing direction without image reversal: Up side (12 o'clock) Viewing direction with contrast peak: Down side (6 o'clock) Viewing angle with optimum grayscale (γ≒ 2.2): Normal axis (perpendicular)
Polarizer surface	Clear + Antireflection (AR)
Polarizer pencil-hardness	2H (min.) [by JIS K5600]
Color gamut	At LCD panel center 40% (typ.) [against NTSC color space]
Response time	$Ton+Toff (10\% \longleftrightarrow 90\%)$ = 18ms (typ.)
Luminance	$At IL = 50mA/One \ circuit$ $800 \ cd/m^2 \ (typ.)$
Signal system	LVDS 1port (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) [8bit/6bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)]
Power supply voltage	LCD panel signal processing board: 3.3V
Backlight	LED backlight type: (Replaceable part • Lamp holder set: Type No. 84LHS17) (Recommended LED driver board (Option) • LED driver board: Type No. 104PW03F
Power consumption	At IL= 50mA/One circuit, Checkered flag pattern 5.0W (typ.)

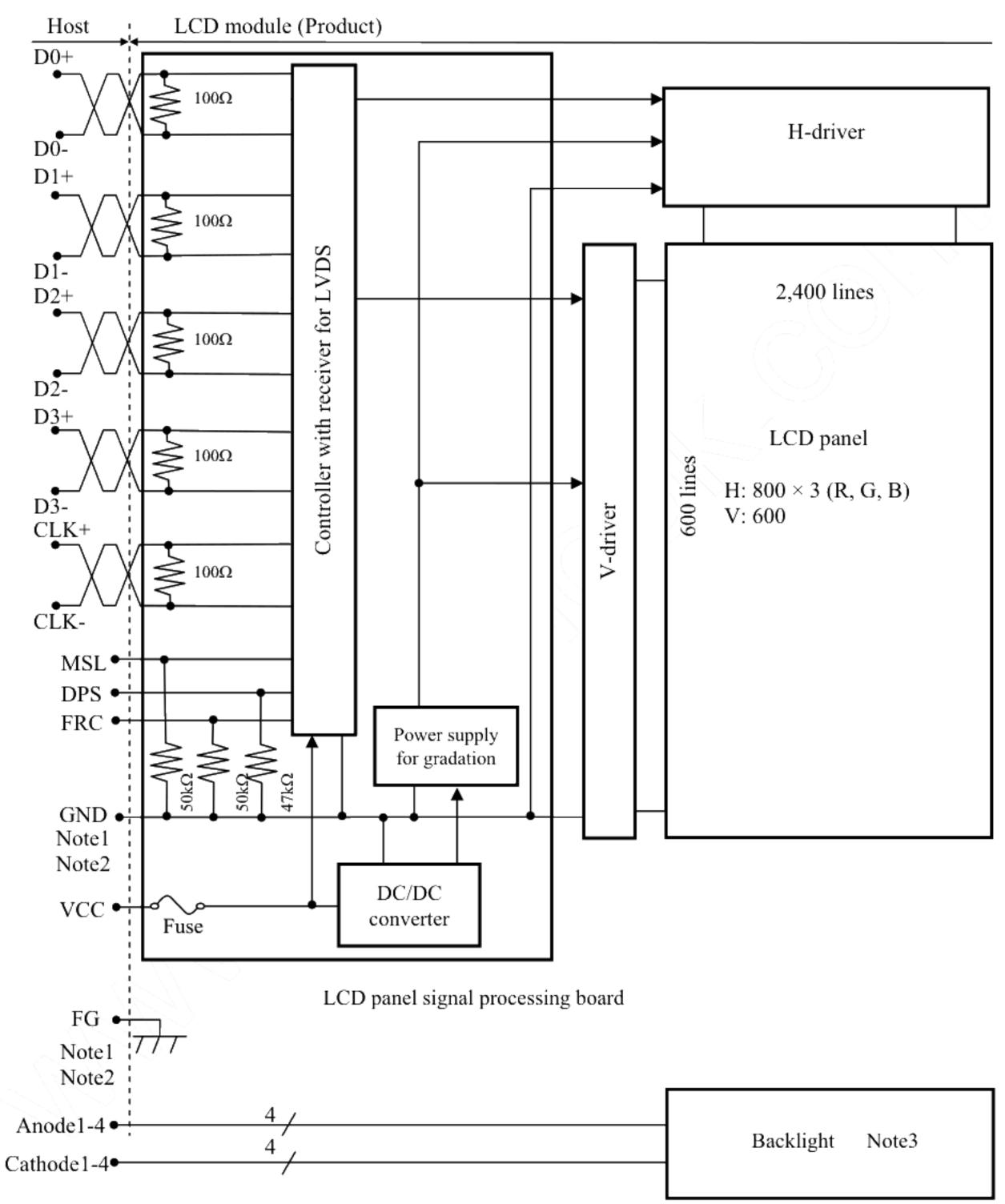




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3. BLOCK DIAGRAM



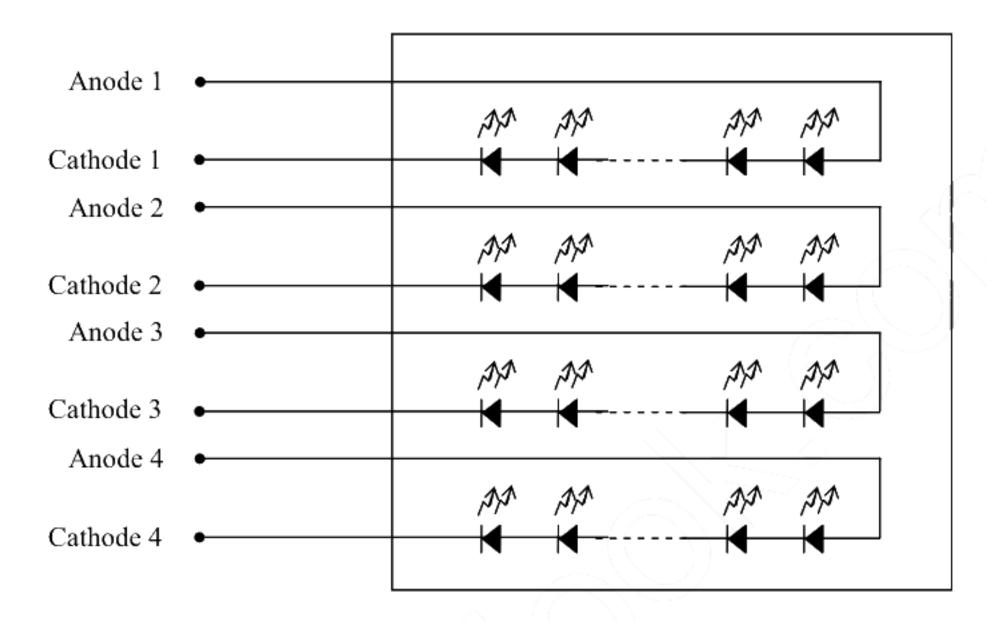
Note1: Relations between GND (Signal ground) and FG (Frame ground) in the LCD module are as follows.

GND - FG Connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds be connected together in customer equipment.

Note3: Backlight in detail

Backlight





4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter Specification			
Module size	$200.0 \pm 0.5 \text{ (W)} \times 152.0 \pm 0.5 \text{ (H)} \times 8.2 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	170.4 (H) × 127.8 (V)	Note1	mm
Weight	260 (typ.), 280 (max.)	6	g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

	Parameter	Symbol	Rating	Unit	Remarks	
Power supply voltage	1 LCD panel signal processing poard 1			-0.3 to +4.0	V	
Input voltage	Display Not	-	VD	-0.3 to VCC+0.3	V	-
for signals	Function Not	_	VF	-0.5 to VCC+0.5	v	
Backlight	Forward	current	IL	60	mA	per one circuit
Ir	ncident light intensit	y	II	150,000	lx	Note3
	Storage temperature		Tst	-30 to +80	°C	-
Operating	Operating temperature Front surface		TopF	-30 to +80	°C	Note4
Operating	temperature	Rear surface	TopR	-30 to +80	°C	Note5
	J(0	<u> </u>		≤ 95	%	Ta ≤ 40°C
				≤ 85	%	40°C < Ta ≤ 50°C
	Relative humidity Note6		RH	≤ 55	%	50°C < Ta ≤ 60°C
				≤ 36	%	60°C < Ta ≤ 70°C
				≤ 24	%	70°C < Ta ≤ 80°C
	Absolute humidity Note6			≤ 70 Note7	g/m ³	-

Note1: D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-

Note2: DPS, FRC and MSL.

Note3: If the product surface (polarizer) is exposed to an ultraviolet ray, the polarizer may discolor (Surface treatment may be damaged.). Use a filter to protect the polarizer from the ultraviolet ray.

Note4: Measured at LCD panel surface (including self-heat)

Note5: Measured at LCD module's rear shield surface (including self-heat)

Note6: No condensation

Note7: Water amount at Ta= 80°C and RH= 24%

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4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

 $(Ta=25^{\circ}C)$

Parameter		Symbol	min.	typ.	max.	Unit	Remarks	
Power supply voltage	VCC	3.0	3.3	3.6	V	-		
Power supply current		ICC	-	230 Note1	340 Note2	mA	at VCC= 3.3V	
Permissible ripple voltage		VRP	-	-	100	mVp-p	for VCC	
Differential input threshold	High	VTH	-	-	+100	mV	at VCM= 1.2V	
voltage	Low	VTL	-100	-	-	mV	Note3	
Terminating resistance	Terminating resistance			100	. <u> </u>	Ω	-	
Input voltage for	High	VFH	0.7VCC	-	VCC	, v	CMOS laval	
DPS, FRC and MSL signals	Low	VFL	0	-((0.3VCC	V	CMOS level	
Input current for EBC circus	High	IFH	-		300	μΑ		
Input current for FRC signal	Low	IFL	-300		-	μΑ	-	

Note1: Checkered flag pattern [by EIAJ ED-2522]

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver

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4.3.2 Backlight lamp

(Ta= 25°C, Note1, Note2)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	50.0	55.0	mA	-
		18.6	21.0	23.8		Ta= +25°C at IL= 50 mA /One circuit
Forward Voltage	VL	17.1	-	-	v	Ta= +80°C at IL= 50 mA /One circuit
Forward Voltage	VL	1	-	26.2	•	Ta= -30°C at IL= 50 mA /One circuit
		-	-	26.4		Ta= -30°C at IL= 55 mA /One circuit

Note1: Please drive with constant current.

Note2: The Luminance uniformity may be changed depending on the current variation between 4 circuits. It is recommended that the current value difference between each circuit be less than 5%.

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4.3.3 Power supply voltage ripple

This product works, even if the ripple voltage levels are beyond the permissible values as sated in the following table, but there might be noise on the display image.

Power sup	ply voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

4.3.4 Fuse

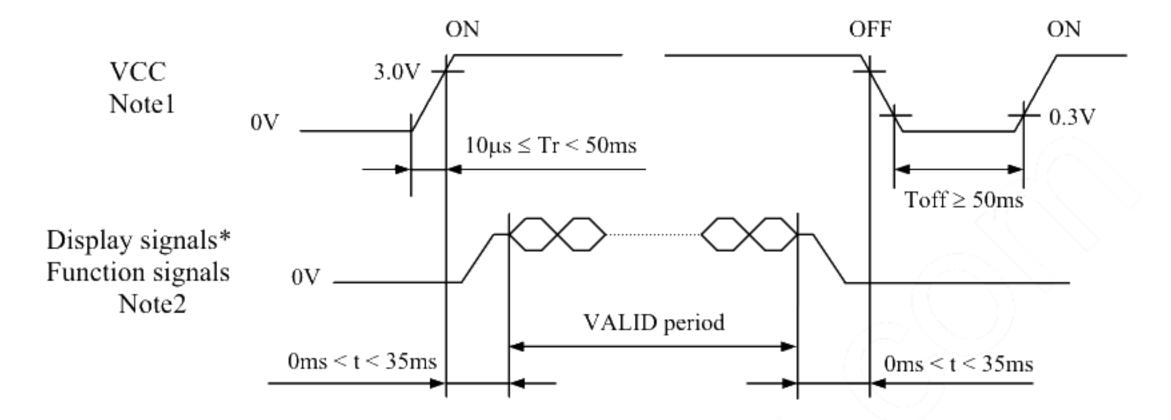
Daramatar		Fuse	Dating	Ei.	D	
Parameter	Туре	Supplier	Rating	Fusing current	Remarks	
VCC	ECC16202 A B	KAMAYA ELECTRIC	2.0A	4.04	Note1	
VCC	FCC16202AB	Co., Ltd.	36V	4.0A		

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Note1: The power supply capacity should be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



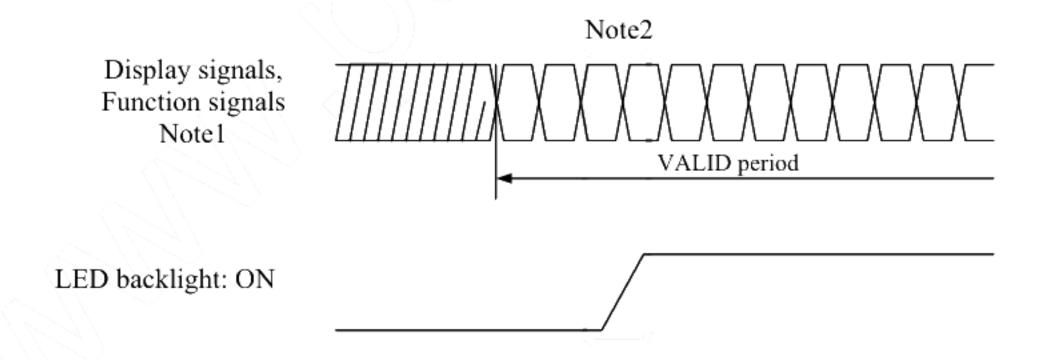
^{*} These signals should be measured at the terminal of 100Ω resistance.

Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-) and function signals (DPS, FRC and MSL) must be se to Low or High-impedance, exclude the VALID period (See above sequence diagram), in order to avoid the internal circuitry damage.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. VCC should be cut when the display and function signals are stopped.

4.4.2 LED driver board (Option)



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE))
Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JAE))

Adaptable plug:		-S20S (Japan	madstry Linna	ou (JAL))			
No.	Symbol	Signal	Input data	Input data	Remarks		
NO.	Syllibol	Signai	MAP A	MAP B	signal: 6bit	Kemarks	
А	D3+	Pixel data	R0-R1,G0-G1,B0-B1 R6-R7,G6-G7,B6-B7		6	Note1, Note2	
В	GND	Ground		-	Ground	Note3	
A	D3-	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7		Note1, Note2	
В	GND	Ground		- /	Ground	Note3	
3	DPS	Selection of scan direction				Note4	
1	FRC	Selection of the number of colors	Hi	gh	Low or Open	Note1 Note5	
5	GND	Ground		Ground		Note3	
5	CLK+	5					
7	CLK-	Pixel clock		Note2			
3	GND	Ground		Note3			
)	D2+	Divel data	R4-R7 DE	Note2			
0	D2-	1 ixei data	D4-B7,DE	DZ-B3,DE			
1	GND	Ground		Ground		Note3	
2	D1+	Pivel data	G3-G7 B2-B3	G1-G5 R0.	-R1	Note2	
3	D1-	1 ixer data	G3-G7,B2-B3	G1-G5,B0	-101	Notez	
4	GND	Ground		Ground		Note3	
5	D0+	Pivel data	R2-R7 G2	R0-R5 G	.0	Note2	
6	D0-)	172-177,02	110102			
7	GND	Ground		Note3			
8	MSL	Selection of LVDS input map	Low	High	Low	Note5	
9	VCC	Power cumby		Power supply		Note3	
0	VCC	1 Ower suppry		Tower suppry		Notes	
	No. A B A	No. Symbol A D3+ B GND A D3- B GND B GND GND CLK+ GND CLK- GND D2+ GND D1+ GND D1- GND GND GND MSL MSL WCC	No. Symbol Signal A D3+ Pixel data B GND Ground A D3- Pixel data B GND Ground B GND Ground B GND Ground G CLK+ Pixel clock G CLK- Pixel data D D2+ Pixel data D D2- Pixel data D D1+ Pixel data D D1+ Pixel data D0- Ground Pixel data D0- Ground Pixel data Selection of LVDS input map Power supply	No. Symbol Signal Input data MAP A	No. Symbol Signal Input data signal: 8bit MAP A	No. Symbol Signal Input data signal: 8bit MAP A	

Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: All GND and VCC terminals should be used without any non-connected lines.

Note4: See "4.8 SCANNING DIRECTIONS".

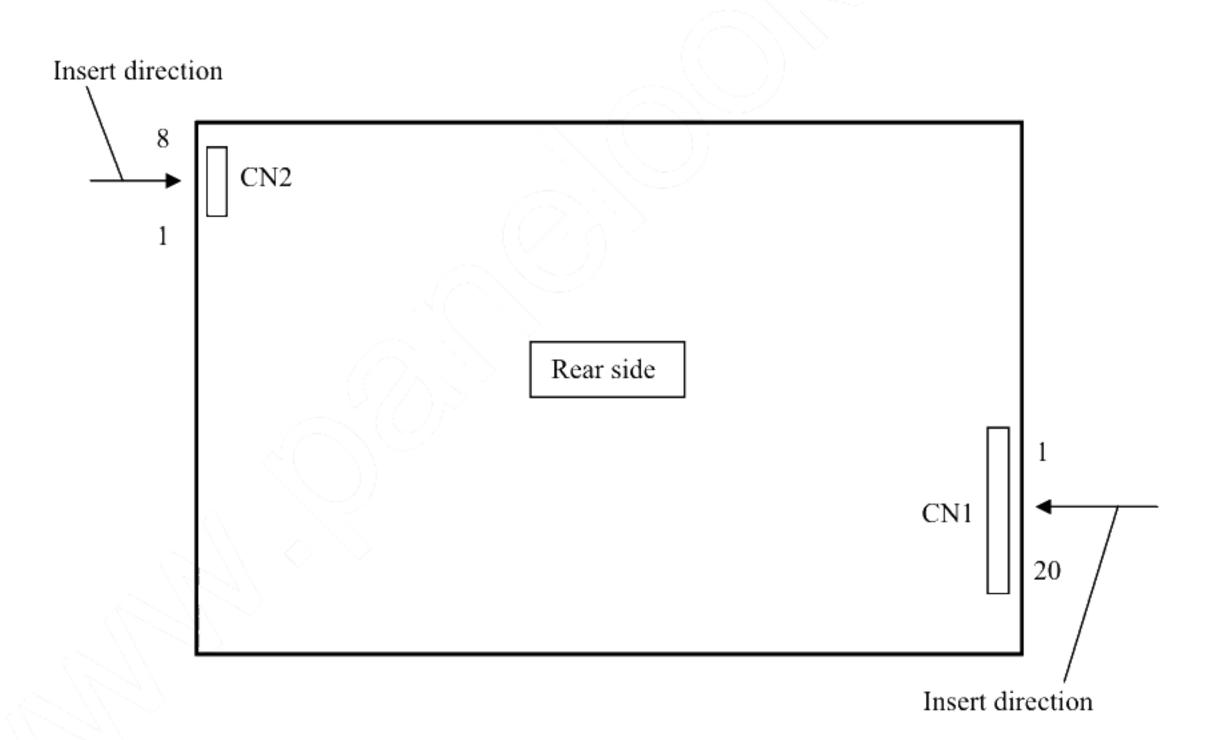
Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".

4.5.2 Backlight lamp

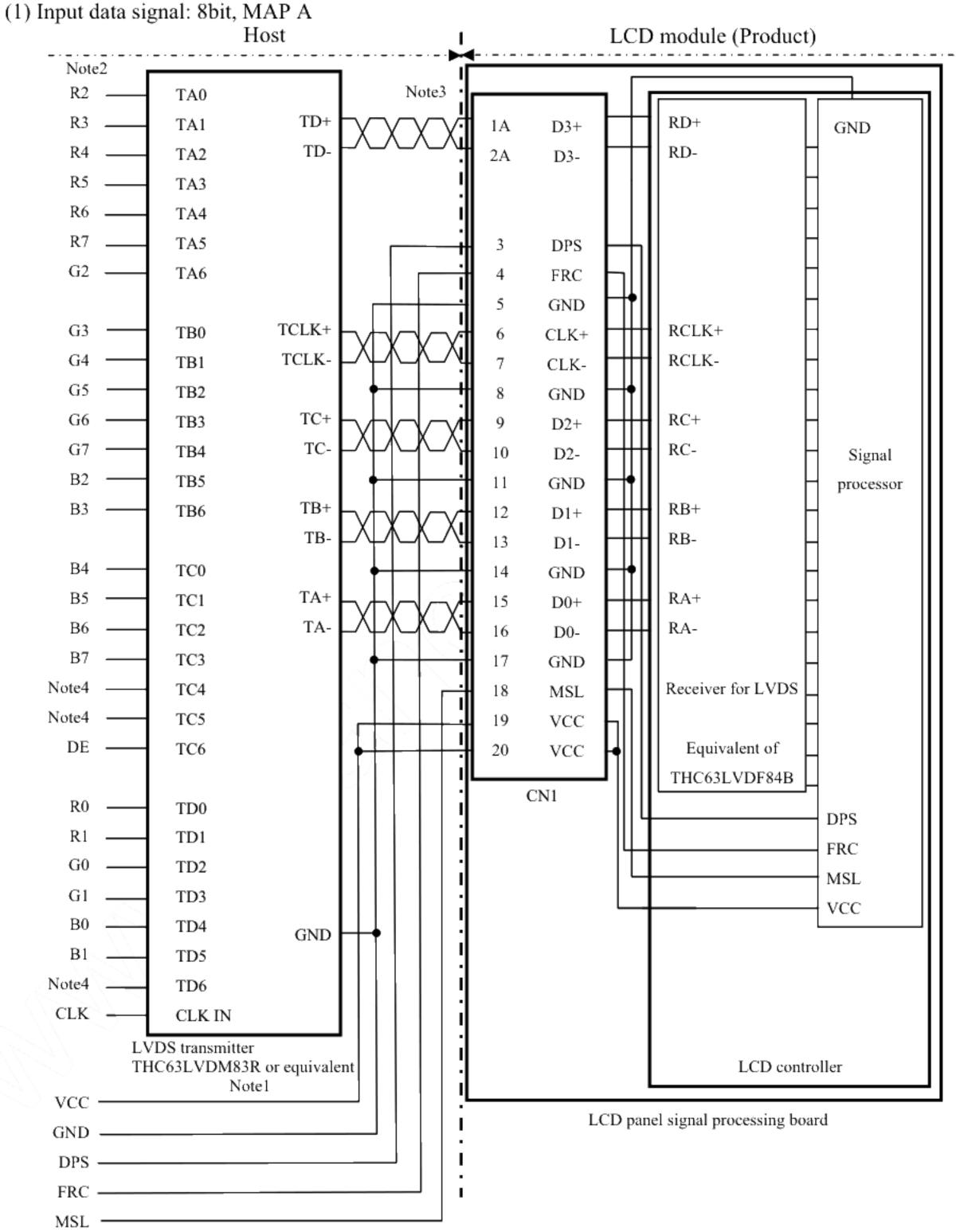
CN2 plug (LCD module side): SM08B-SRSS-TB (J.S.T. Mfg. Co., Ltd.)
Adaptable socket: SHR-08V-S (J.S.T. Mfg. Co., Ltd.)

			-,,		
Pin No.	Symbol	Signal	Remarks		
1	Al	Anode1	-		
2	K1	Cathode1	-		
3	A2	Anode2	-		
4	K2	Cathode2	- 21		
5	A3	Anode3	- <		
6	K3	Cathode3	∠- -\\\		
7	A4	Anode4	//- \\\\\		
8	K4	Cathode4	2-5\\ -		

4.5.3 Positions of plug and socket



4.5.4 Connection between receiver and transmitter for LVDS



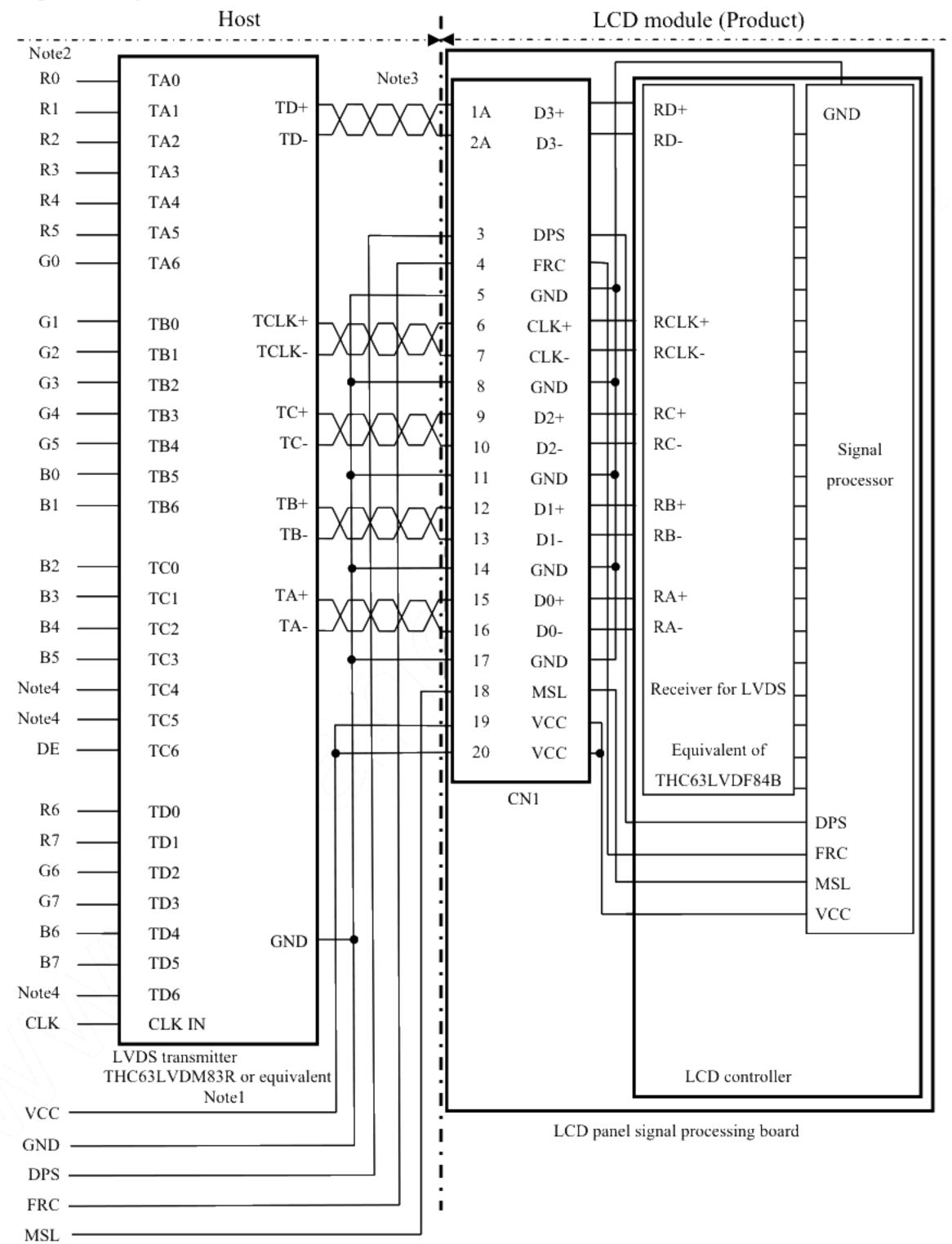
Note1: Recommended transmitter: THC63LVDM83R (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep TC4, TC5 and TD6 open to avoid noise problem.

(2) Input data signal: 8bit, MAP B



Note1: Recommended transmitter: THC63LVDM83R (THine Electronics Inc.) or equivalent

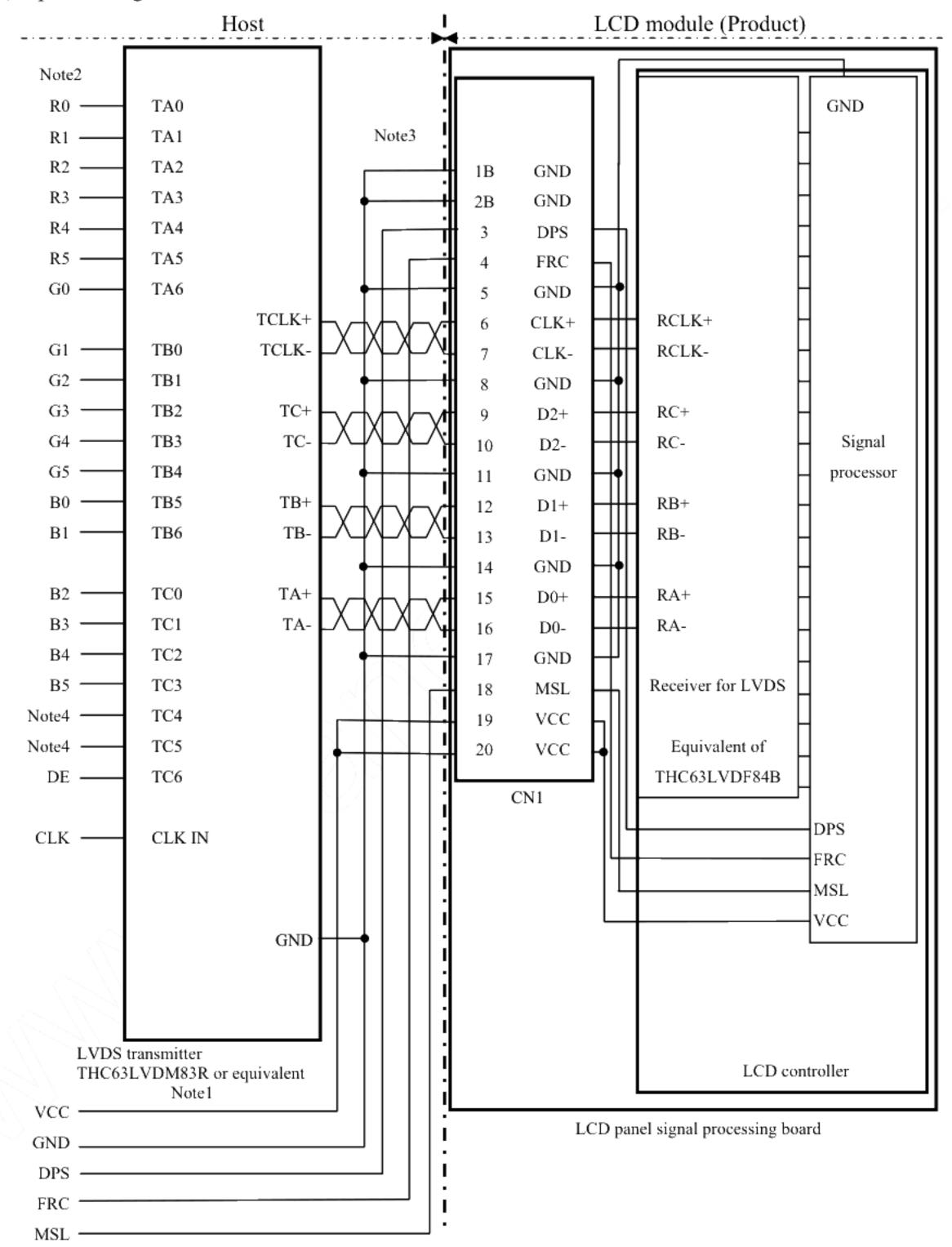
Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel

signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep TC4, TC5 and TD6 open to avoid noise problem.

(3) Input data signal: 6bit



Note1: Recommended transmitter THC63LVDM83R (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R5, G5, B5

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4 and TC5 are not used inside the product, but do not keep TC4 and TC5 open to avoid noise problem.

4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations between input data signals and FRC signal

This product can display 16,777,216 colors equivalent in 256 gray scales and 262,144 colors equivalent in 64 gray scales by combination of input data, FRC and MSL signals. See the following table.

Combination	Input data signals	Input Data mapping	CN1- Pin No.1 and 2	FRC terminal	MSL terminal	Display colors	Remarks
1	8 bit	MAP A	D3+/-	High	Low	16,777,216	Note1
2	8 bit	MAP B	D3+/-	High	High	16,777,216	Note1
3	6 bit	-	GND	Low or Open	Low	262,144	Note2

Note1: See "**4.6.2 16,777,216 colors**". Note2: See "**4.6.3 262,144 colors**".



4.6.2 16,777,216 colors

This product can display 16,777,216 colors equivalent in 256 gray scales by combination ① or ②. (See "4.6.1 Combinations between input data signals, FRC signal and MSL signal".) Also the relation between display colors and input data signals is as follows.

Display	/ colors								Data	a sig	nal	(0: I	Low	leve	el, 1	: Hi	gh le	evel)							
Display	COIOIS	R7	R6	R5	R4	R3	R2	Rl	R0	G7	7 G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	В4	В3	B2	Βl	Β0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	4	1	1	1
lors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ြိ	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Basic Colors	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-1-	1	1	/1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
υ		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	↑					:							1	:								:			
l gr	\downarrow					:																:			
Rec	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
scale	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
gray	↑					:								:								:			
Green gray	\downarrow					:								:								:			
Jre(bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
1 ~		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	1					:								:								:			
50 50	+	1	_	-	-	:	_	_	_	_	-	-		:							-	Ι.	_	_	
Blu	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1 .	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	l	l	1	l	1	l



4.6.3 262,144 colors

This product can display 262,144 colors in 64 gray scales by combination ③. (See "4.6.1 Combinations between input data signals, FRC signal and MSL signal".) Also the relation between display colors and input data signals is as follows.

Display						Data	ı signa	al (0:	Low	level	, 1: H	ligh le	vel)						
Display	COIOIS	R.5	R 4	R 3	R 2	R 1	R 0	G5	G4	G3	G2	G1	G0	В5	B4	В3	В2	B 1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	/1-	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0 <	0	0	0
ပ	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	. 1	1
Basic colors	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
B	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1,-	- 1	_1_	/ /1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
o l		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
ay s	↑			:						/	;\					2	:		
l gr	\downarrow			:									~				:		
Rec	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	/ 0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	-0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
SCS	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
ray	↑			:	. /						:					2	:		
Green gray scale	\downarrow			:							:						:		
irec	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
~		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u>9</u>		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	1	. <		♦ :							:						:		
50 50	1		~	:				_	_	-	:		_		_		:		
Blu	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0,	0) B					
C(0, 0)	C(1, 0)		C(X, 0)		C(798, 0)	C(799, 0)
C(0, 1)	C(1, 1)		C(X, 1)		C(798, 1)	C(799, 1)
•	•	•	•	•	•	$\langle \cdot \rangle$.
	•	• • •			•//	\ · · ·
•	•	•	•	•	• \ \)) •
C(0, Y)	C(1, Y)		C(X, Y)		C(798, Y)	C(799, Y)
•	•	•	•	•	((•))	•
	•	• • •		• • •	\ <u>.</u> •//	•
•	•	•	•	○•	/s. •	•
C(0, 598)	C(1, 598)		C(X, 598)	• \• \•	C(798, 598)	C(799, 598)
C(0, 599)	C(1, 599)		C(X, 599)		C(798, 599)	C(799, 599)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view. Also the arrow shows the direction of scan.

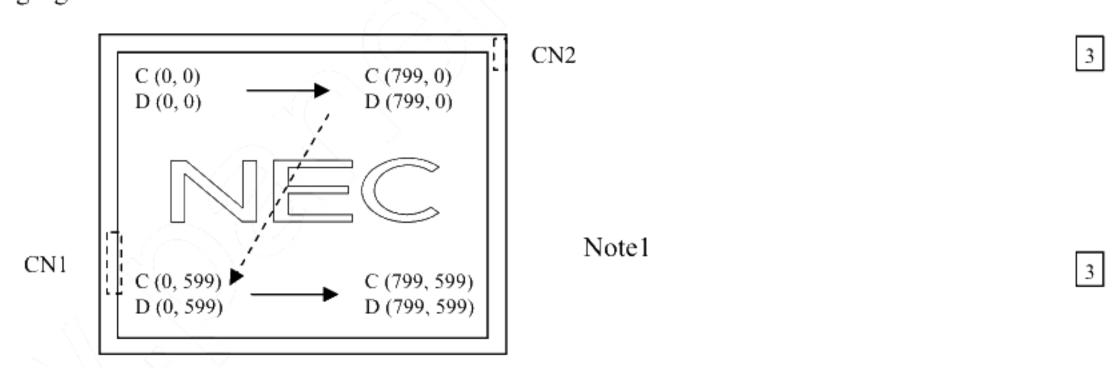


Figure 1. Normal scan (DPS: Low or Open)

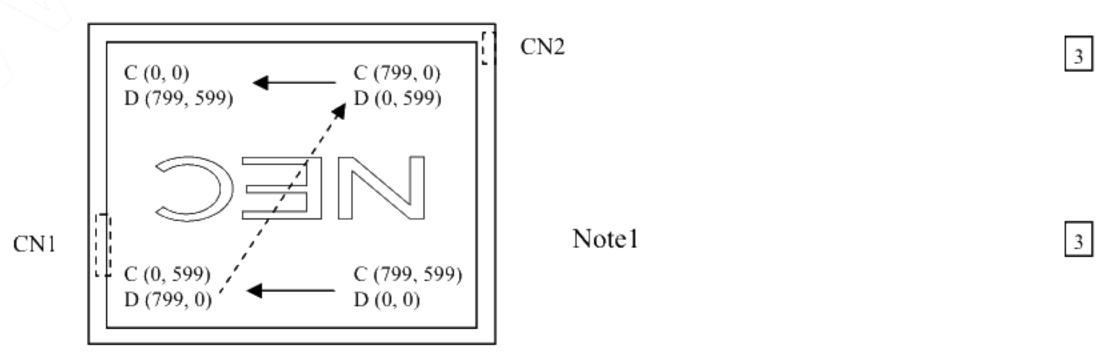


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

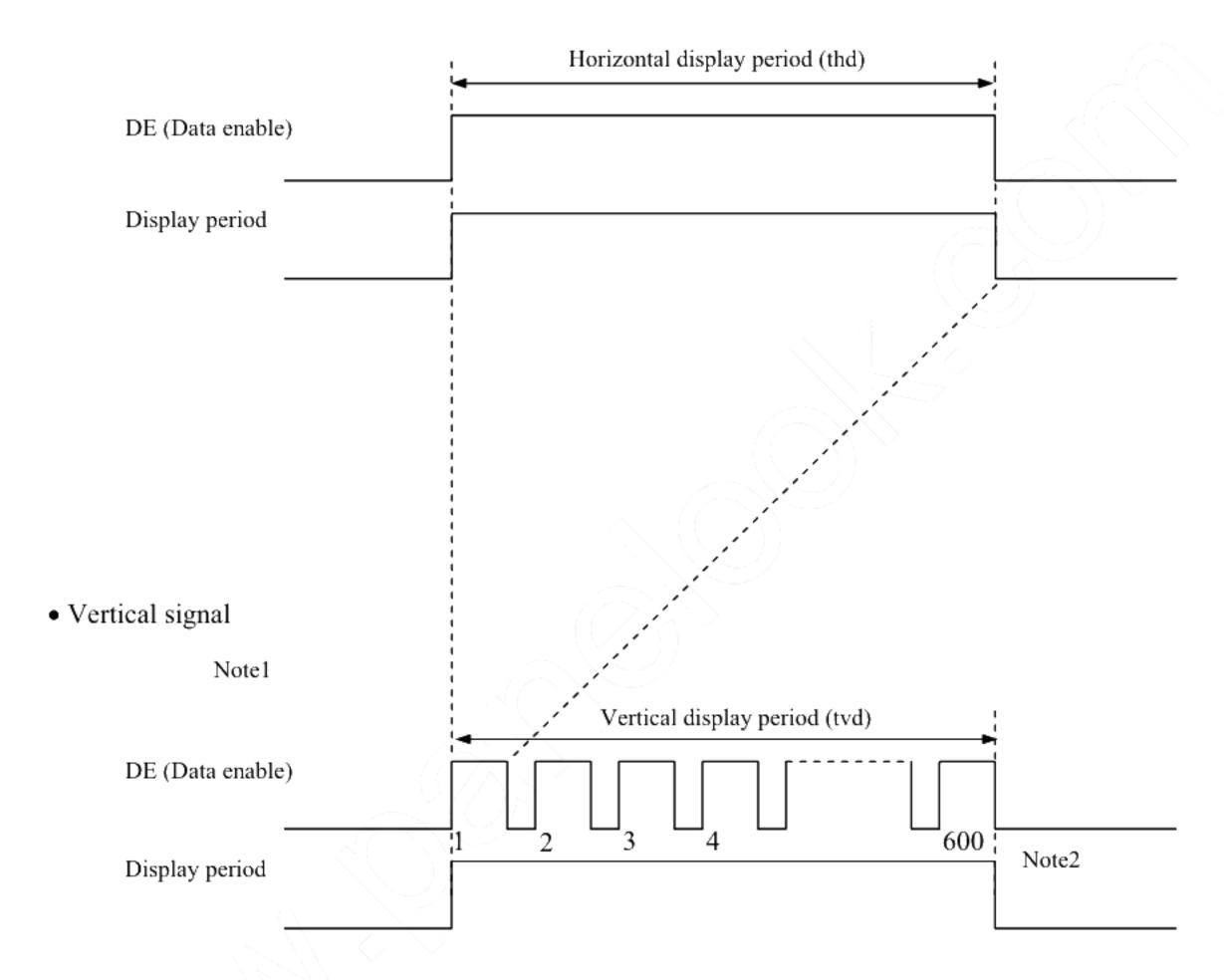
D (X, Y): The data number of input signal for LCD panel signal processing board

4.9 INPUT SIGNAL TIMINGS

4.9.1 Outline of input signal timings

• Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing.

Note2: See "4.9.3 Input signal timing chart" for numeration of pulse.



NL8060BC21-11C

4.9.2 Timing characteristics

(Note1, Note2, Note3)

	Parameter		Symbol	min.	typ.	max.	Unit	Remarks
	Fre	quency	1/tc	34.0	38.362	42.0	MHz	26.067ns (typ.)
CLK	I	Outy	-				-	
	Rise time, Fall time		-		-		ns	-
	CLK-DATA Setup time		-				ns	/~
DATA	CLK-DATA Hold time		-		-		ns	_(e\\
	Rise time, Fall time		-				ns	
		Cycle	th	24.0	26.693	30.1	μs	
	Horizontal	Cycle	ui	-	1,024	-	CLK	37.463kHz (typ.)
		Display period	thd	800			CLK))
	77	Cycle	tv	16.1	16.683	17.2 /	ms_	
DE	Vertical (One frame)	Cycle	ιν	-	625	-	Н	59.94Hz (typ.)
	(one name)	Display period	tvd		600	4	Н	
	CLK-DE	Setup time	-				ns	
	CLK-DE	Hold time	-	-		\	ns	-
	Rise tim	e, Fall time	-				ns	

Note1: Definition of parameters is as follows.

tc=1CLK, th=1H

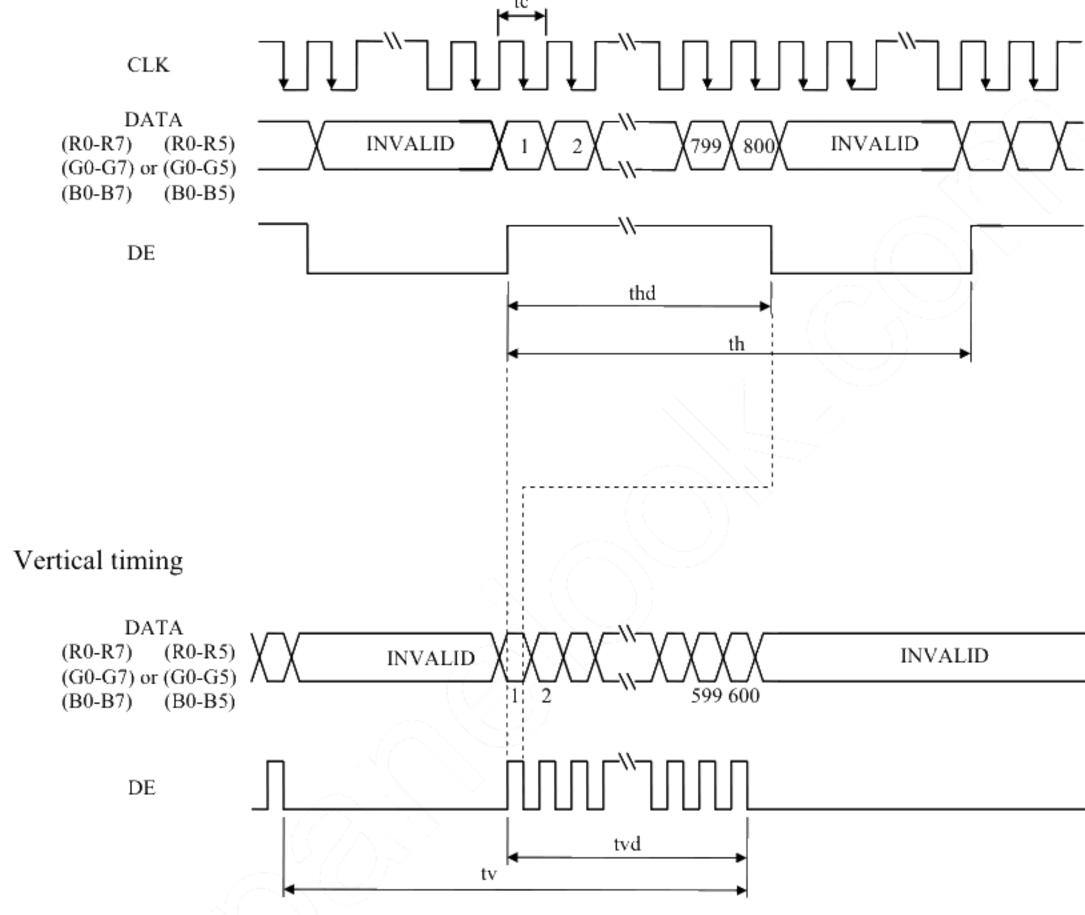
Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

3

4.9.3 Input signal timing chart

Horizontal timing





4.10 OPTICS

4.10.1 Optical characteristics

(Note1, Note2)

3

3

Paramete	r	Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminano	e	White at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	L	480	800	-	cd/m ²	BM-5A	-
Contrast ra	tio	White/Black at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	CR	400	800	-	-	BM-5A	Note3
Luminance uni	formity	White $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	LU	-	1.25	1.4	-	BM-5A	Note4
	White	x coordinate	Wx	0.263	0.313	0.363	1		
	White	y coordinate	Wy	0.279	0.329	0.379	-/-		
	D 1	x coordinate	Rx	-	0.568	- ((-1		
CI	Red	y coordinate	Ry	-	0.363	/	-2)		
Chromaticity	Graan	x coordinate	Gx	-	0.354	- 🔿	-	SR-3	Note5
	Green	y coordinate	Gy	-	0.530		-		
	Dlus	x coordinate	Bx	-//	0.157	-	-		
	Blue	y coordinate	Ву	/	0.131	-	-		
Color gam	ut	θ R= 0°, θ L= 0°, θ U= 0°, θ D= 0° at center, against NTSC color space	C	35	40	-	%		
Dagnanga tima		White to Black	_ Ton		3	5	ms	BM-5A	Note6
Response time		Black to White	Toff	- ->	15	20	ms	DIVI-3A	Note7
	Right	θU= 0°, θD= 0°, CR≥ 10	θR	70	80	-	٥		
Viewing angle	Left	θU= 0°, θD= 0°, CR≥ 10	-θL	70	80	-	0	EZ	Note8
	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	٥	Contrast	
	Down	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θD	70	80	-	٥		

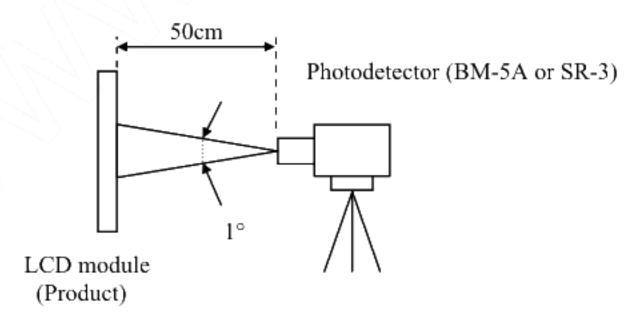
Note1: These are initial characteristics.

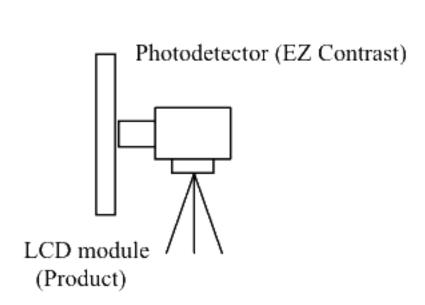
Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 50mA/One circuit, Display mode: SVGA,

Horizontal cycle= 1/37.463kHz, Vertical cycle= 1/59.94Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation 20minutes after a product works in the dark room. Also measurement methods are as follows.





Note3: See "4.10.2 Definition of contrast ratio".

Note4: See "4.10.3 Definition of luminance uniformity".

Note5: These coordinates are found on CIE 1931 chromaticity diagram.

Note6: Product surface temperature: TopF= 27°C

Note7: See "4.10.4 Definition of response times".

Note8: See "4.10.5 Definition of viewing angles".

4.10.2 Definition of contrast ratio

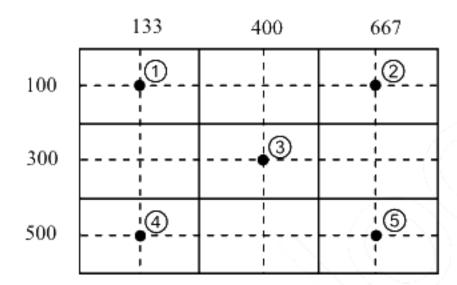
The contrast ratio is calculated by using the following formula.

4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

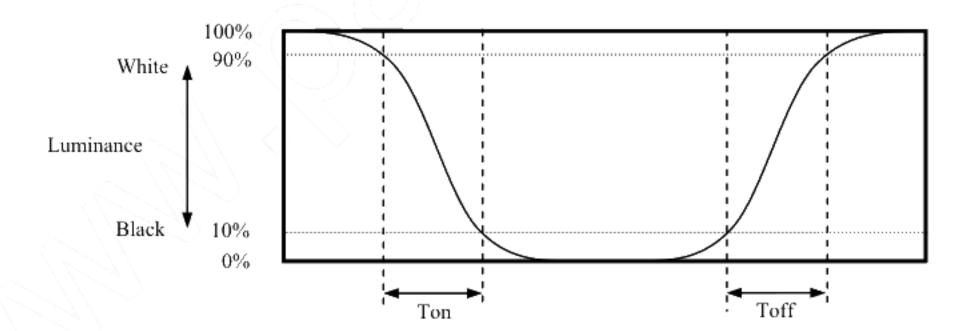
Luminance uniformity (LU) =
$$\frac{\text{Maximum luminance from } \textcircled{1} \text{ to } \textcircled{5}}{\text{Minimum luminance from } \textcircled{1} \text{ to } \textcircled{5}}$$

The luminance is measured at near the 5 points shown below.

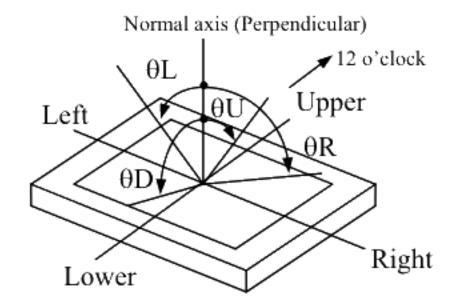


4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time when the luminance change from 90% down to 10%. Also Toff is the time when the luminance change from 10% up to 90% (See the following diagram.).



4.10.5 Definition of viewing angles



5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

	Condition	Expected luminance lifetime Note1, Note2	Unit
LED	25°C (Ambient temperature of the product) Continuous operation, IL= 50mA/One circuit	70,000	h
elementary substance	80°C (Surface temperature at screen) Continuous operation, IL= 50mA/One circuit	60,000	h

Note1: Expected luminance lifetime is not the value for LCD module but the value for LED elementary substance.

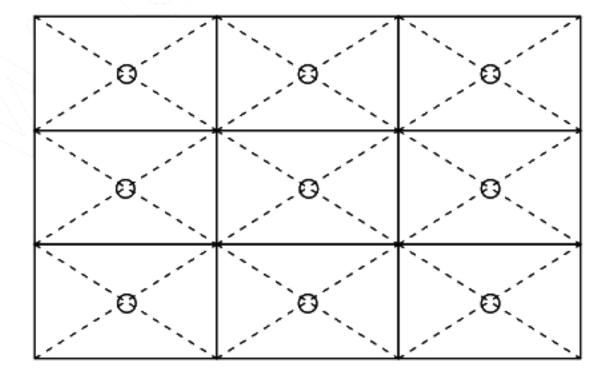
Note2: The lifetime changes particularly depending on the ambient temperature. Especially in case that the product works under high temperature environment, the lifetime becomes short.

6. RELIABILITY TESTS

Test item	Condition	Judgment Note1			
High temperature and humidity (Operation)	① 60 ± 2°C, RH= 90%, 240hours ② Display data is black.				
High temperature (Operation)	 ① 80 ± 3°C ,240hours ② Display data is black. 				
Heat cycle (Operation)	 30 ± 3°C1hour 50cycles, 4 hours/cycle Display data is black. 				
Thermal shock (Non operation)	 30 ± 3°C30minutes 80 ± 3°C30minutes 100cycles, 1hour/cycle Temperature transition time is within 5 minutes. 	No display malfunctions			
ESD (Operation)	 ① 150pF, 150Ω, ±10kV ② 9 places on a panel surface Note2 ③ 10 times each places at 1 sec interval 				
Dust (Operation)	 ① Sample dust: No. 15 (by JIS-Z8901) ② 15 seconds stir ③ 8 times repeat at 1 hour interval 				
Vibration (Non operation)	 5 to 100Hz, 19.6m/s² 1 minute/cycle X, Y, Z directions 120 times each directions 	No display malfunctions			
Mechanical shock (Non operation)	 539m/ s², 11ms ±X, ±Y, ±Z directions 5 times each directions 	No physical damages			

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!



This sign has the meaning that a customer will be injured by personnel or the product will sustain a damage, if the customer has wrong operations.



This sign has the meaning that a customer will be injured by personnel, if the customer has wrong operations.

7.2 CAUTIONS



* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s² and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (\$\phi16mm jig))

7.3 ATTENTIONS



7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- 3 When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ④ The torque for product mounting screws must never exceed 0.294N·m. Higher torque might result in distortion of the bezel.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑥ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ① Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.



7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left in an unpacking room. Evaluate the storage time sufficiently because a dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with packing state)
- 3 Do not operate in high magnetic field. If you do, circuit boards may be broken down by it.
- This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flicker, vertical seam or small spot may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.
- The product gives AR (antireflection) coating of the polarizer surface. Though AR (antireflection) coating actualizes the low reflection with the multilayer structure, the color of reflection may differ among products and the color change of reflection may occur in the same product by fluctuation of AR (antireflection) coating.

7.3.4 Other

- ① All VCC and GND terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- Pack the product with original shipping package, in order to avoid any damages during transportation, when returning the product to NEC for repairing and so on.
- ⑤ The information of China RoHS directive six hazardous substances or elements in this product is as follows.

	China RoHS directive six hazardous substances or elements											
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)							
×	0	0	0	0	0							

- Note1: ○: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of SJ/T11363-2006 standard regulation.
 - X: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of SJ/T11363-2006 standard regulation.

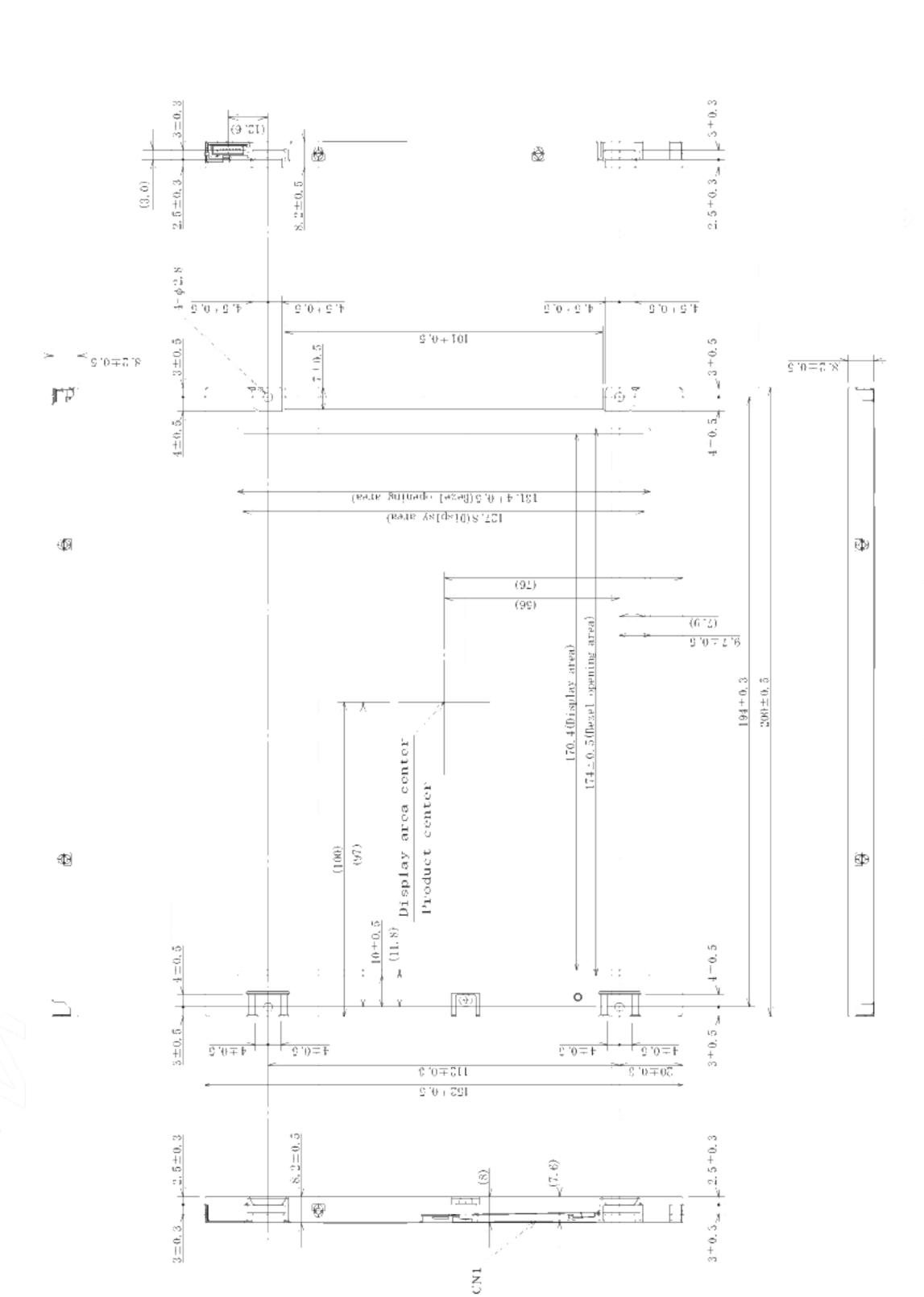
3

PRELIMINARY

m

8. OUTLINE DRAWINGS

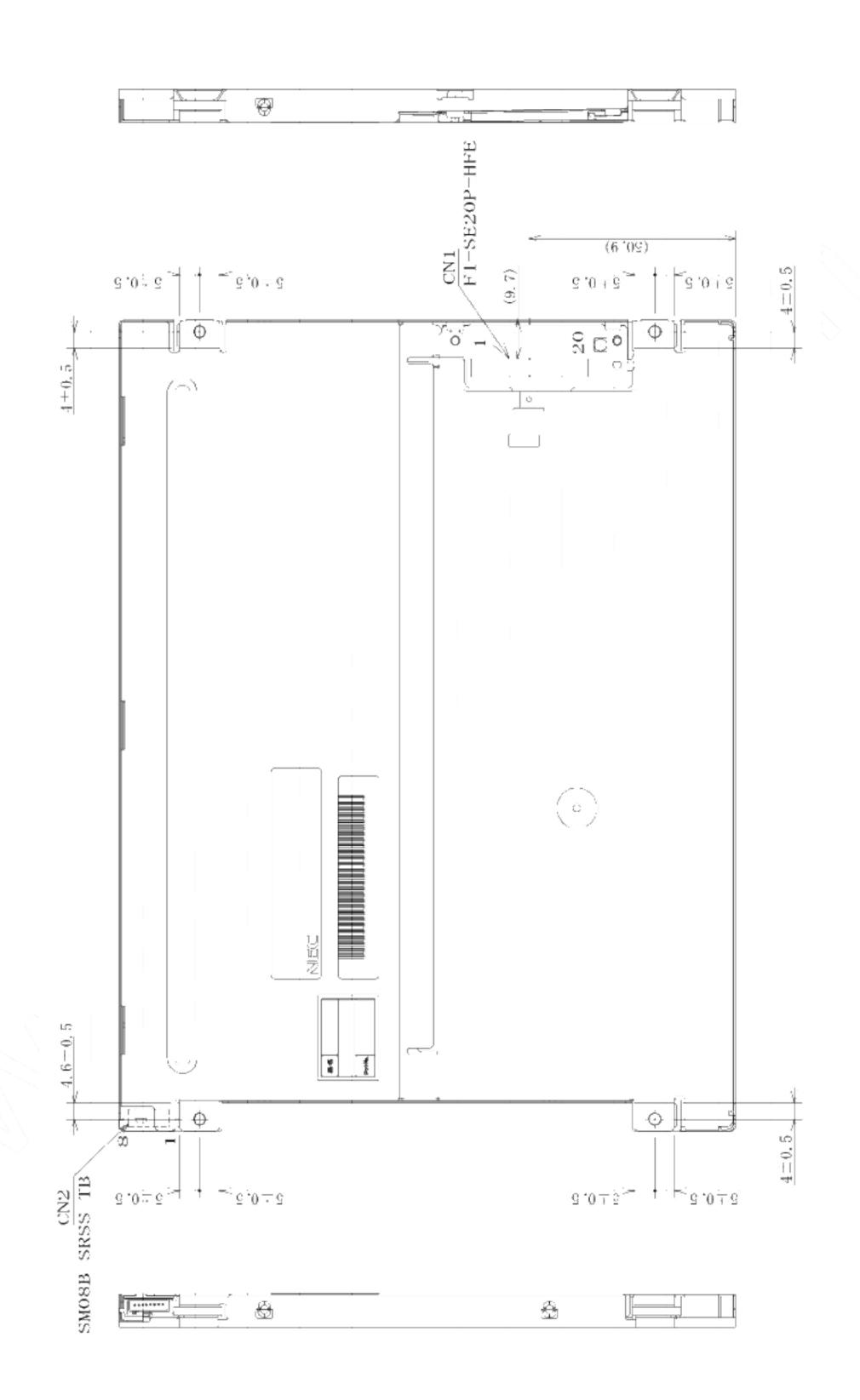
8.1 FRONT VIEW



Note1: The values in parentheses are for reference. Note2: The torque for product mounting screws must never exceed 0.294N·m.

٠٠,

8.2 REAR VIEW



Note1: The values in parentheses are for reference. Note2: The torque for product mounting screws must never exceed 0.294N·m.

REVISION HISTORY

Edition	Document number	Prepared date	Revision contents and signature
3rd	DOD-PP-	Aug. 6,	Revision contents
edition	1046	2010	P4 Features • LED backlight type → Long life LED backlight type • Replaceable lamp holder for backlight → Replaceable lamp for backlight • Color Conversion(Tentative name) → Color Enhancement • Acquisition product for UL60950-1/CSA C22.2 No.60950-1-03
			(File number: E170632) (addition) • Compliant with the European RoHS directive (2002/95/EC) P5 General Specifications, P8 Detailed Specifications- Mechanical Specifications • Module size: 10.5 ± 0.5 (D) mm → 8.2 ± 0.5 (D) mm P5 General Specifications • Weight: TBD g (typ.) → 260 g (typ.) • Contrast ratio: (900:1) (typ.) → 800:1 (typ.) • Polarizer pencil-hardness: [by JIS K5400] → [by JIS K5600] • Response time: (18)ms (typ.) → 18ms (typ.) • Luminance: (800) cd/m² (typ.) → 800cd/m² (typ.) • Backlight-Lamp holder set: TBD → 84LHS17 • Power consumption: (5.6)W (typ.) → 5.0W (typ.) P8 Detailed Specifications • Mechanical Specifications • Weight: TBD (typ.), TBD (max.) → 260 (typ.), 280 (max.) • Absolute Maximum Ratings • Backlight-Power dissipation(PD) (elimination)
			-Forward current(IL): TBD mA → 60 mA • Note6: Ta= 70°C and RH= 36% → Ta= 80°C and RH= 24% P9 Electrical Characteristics • LCD panel signal processing board • Power supply current (ICC): (300)(typ.), (450)(max.)mA → 230(typ.), 340(max.)mA P10 Electrical Characteristics • Backlight lamp • Forward current- Remarks: Note3 (elimination) • Forward voltage- +25°C IL=50mA: - (min.), 23.1 (typ.), 26.6 (max.)V → 18.6 (min.), 21.0 (typ.), 23.8 (max.)V (correction) - +80°C IL=50mA: - (min.), - (typ.), 26.2 (max.) (addition) 30°C IL=50mA: - (min.), - (typ.), 26.2 (max.) (addition) - 30°C IL=50mA: - (min.), - (typ.), 26.4 (max.) (addition) • Note3 (elimination) • Note3 (elimination) • Fuse: → specified P20 Scanning Directions • Figure1 and Figure2: CN1 and CN2 (addition) P22 Timing characteristics • LKC- Frequency (1/tc): 40.0 (max.) MHz → 42.0 (max.) MHz P24 Optics- Optical characteristics • Luminance(L): TBD (min.), (800) (typ.) cd/m² → 480 (min.), 800 (typ.) cd/m² • Contrast ratio (CR): TBD (min.), (900) (typ.) → 400 (min.), 800 (typ.) • Chromaticity-White(Ws): TBD(min., typ., max.) → 0.263(min.), 0.313(typ.), 0.363(max.) -White(Wy): TBD(min., typ., max.) → 0.279(min.), 0.329(typ.), 0.379(max.) -Red(Rs): TBD (typ.) → 0.568 (typ.) -Green(Gx): TBD (typ.) → 0.363(typ.) -Green(Gy): TBD (typ.) → 0.354(typ.) -Green(Gy): TBD (typ.) → 0.354(typ.) -Green(Gy): TBD (typ.) → 0.354(typ.) -Blue(Bx): TBD (typ.) → 0.157(typ.) -Blue(Bx): TBD (typ.) → 0.157(typ.) -Blue(Bx): TBD (typ.) → 0.157(typ.) -Blue(By): TBD (typ.) → 0.157(typ.)

REVISION HISTORY

Edition	Document number	Prepared date	Ro	evision contents and signat	ture			
3rd edition	DOD-PP- 1046	Aug. 6, 2010	 7.8±0.5 → 8.2±0.5 (3 points) 8.2±0.5 (1 points) (12.6) Rear View CN1(SM08B-SRSS-TB) CN2(FI-SE20P-HFE) (added) (9.7) (50.9) and 4±0.5 (1 Nameplate label Barcode 	recautions- Attentions Other: (§) (addition) 1 Outline Drawings (revised) Front View $20 \rightarrow 20\pm0.3$ $112\pm0.5 \rightarrow 112\pm0.3$ $5\pm0.5 \rightarrow 4.5\pm0.5$ (4 points) $7.8\pm0.5 \rightarrow 8.2\pm0.5$ (3 points) 8.2 ± 0.5 (1 points), (12.6) and (3.0) (addition) Rear View CN1(SM08B-SRSS-TB), Pin No.1 to 20 (addition) CN2(FI-SE20P-HFE) (addition) (9.7), (50.9) and 4 ± 0.5 (1 points) (addition) Nameplate label, Barcode label (addition) 2.5 ± 0.3 (2 points) and 3 ± 0.3 (2 points) (elimination)				
			Signature of writer Approved by T. OGAWA	Checked by	A. KUMANO			