NEC NEC LCD Technologies, Ltd.

TFT COLOR LCD MODULE

NL2432HC22-40J

8.9cm (3.5 Type) QVGA

DATA SHEET

DOD-PP-0129 (3rd edition)

This DATA SHEET is updated document from DATA SHEET DOD-PP-0011(2).

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INTRODUCTION

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Examples: Military systems, aircraft control equipment, aerospace equipment, nuclear reactor control systems, medical equipment/devices/systems for life support, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.

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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL2432HC22-40J is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a controller, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

• PDAs

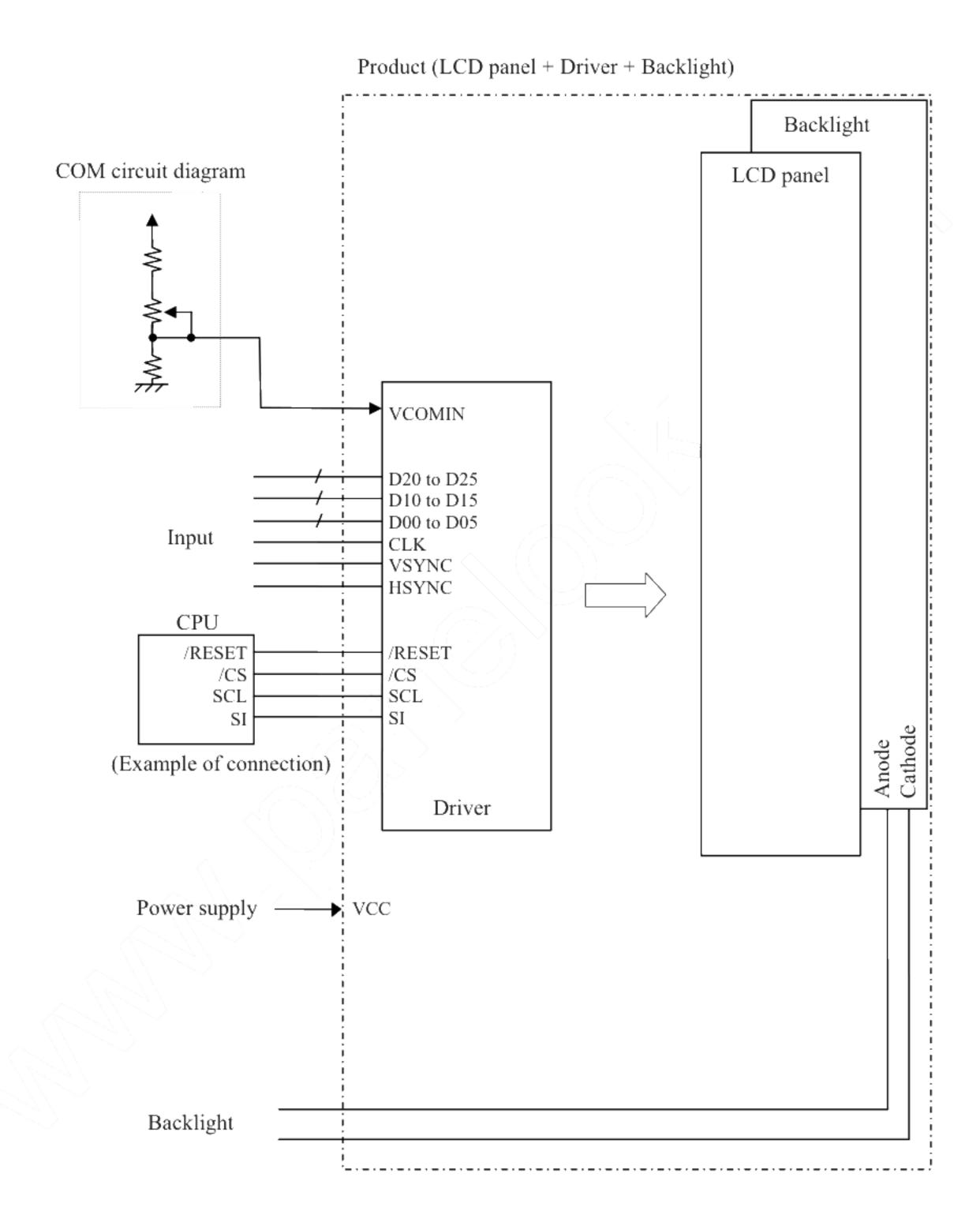
1.3 FEATURES

- Adoption of SR-NLT (Super-Reflective Natural Light TFT) (Transflective type)
- · Backlight attached
- High luminance
- High contrast
- Including LCD controller and power supply
- 6-bit digital RGB signals

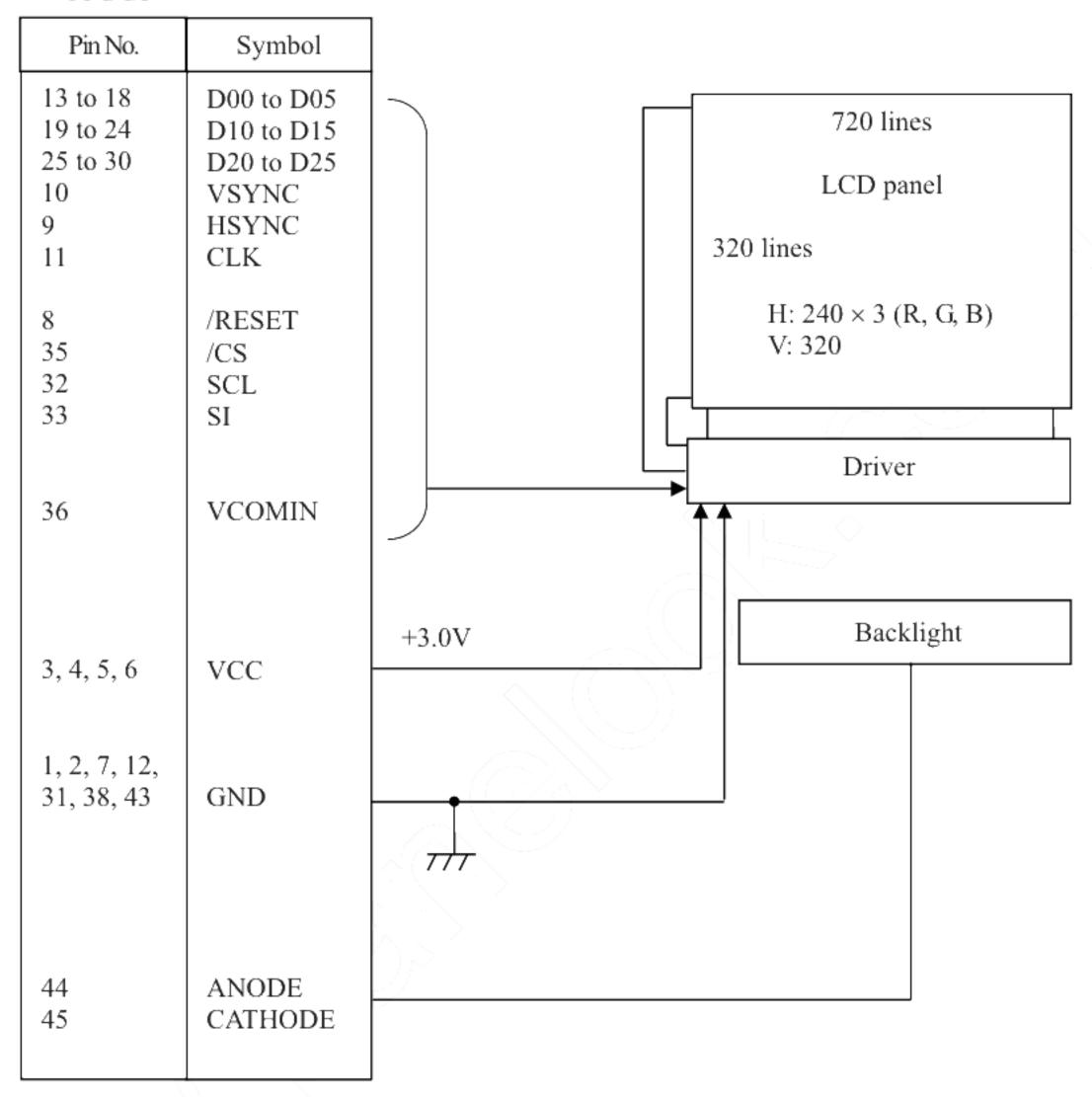
2. GENERAL SPECIFICATIONS

Display area	53.64 (H) × 71.52 (V) mm
Diagonal size of display	8.9cm (3.5 inches)
Drive system	a-Si TFT active matrix
Display color	262,144 colors
Pixel	240 (H) × 320 (V) pixels
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe
Dot pitch	0.0745 (H) × 0.2235 (V) mm
Pixel pitch	0.2235 (H) × 0.2235 (V) mm
Module size	63.5 (H) × 85.0 (V) × 3.0 (D) mm (typ.)
Weight	25 g (typ.)
Polarizer surface	Clear
Polarizer pencil-hardness	3H (min.) [by JIS K5400]
Luminance	$At IL = 20mA$ $220cd/m^2 \text{ (typ.)}$
Reflectance ratio	15% (typ.)
Contrast ratio	At transmissive mode, IL= 20mA 150:1 (typ.) At reflective mode 15:1 (typ.)
Response time	$Ton+Toff(10\% \longleftrightarrow 90\%)$ 30 ms (typ., at transmissive mode) 16 ms (typ., at reflective mode)
Designed viewing direction	Viewing direction without image reversal: down side (6 o'clock)
Signal system	6-bit digital signals for data of RGB colors, Dot clock (CLK), Horizontal synchronous signal (HSYNC), Vertical synchronous signal (VSYNC) Serial interface signals (/CS, SCL, SI)
Supply voltage	VCC: 3.0V (typ.)
Power consumption	LCD panel: 50mW (typ.) Backlight: 384mW (typ., at IL= 20mA)

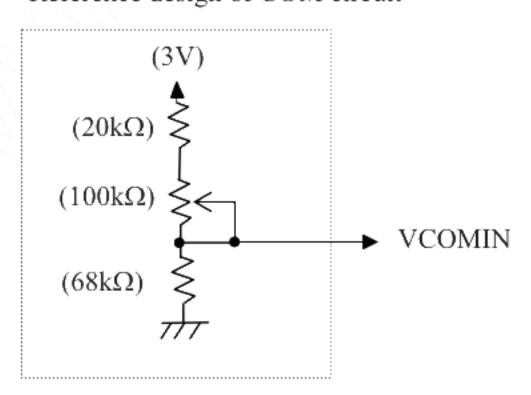
3. BLOCK DIAGRAM



FPC I/F



Reference design of COM circuit



X

4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

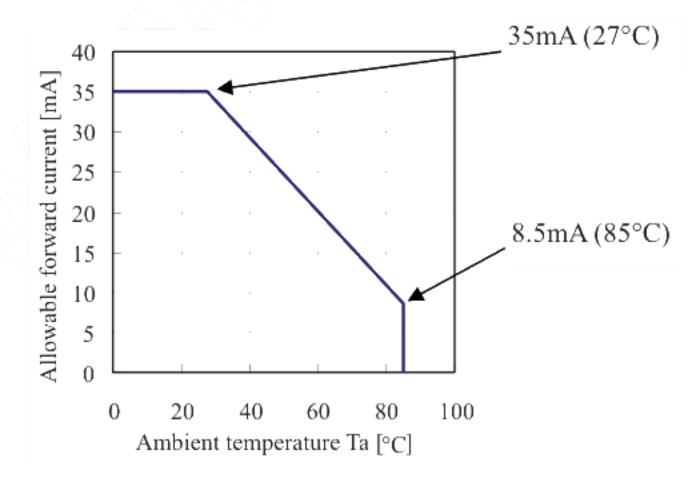
Parameter	Specification	Unit	
Module size	$63.5 \pm 0.3 \text{ (W)} \times 85.0 \pm 0.3 \text{ (H)} \times 3.0 \pm 0.2 \text{ (D)}$	Note1	mm
Display area	53.64 (H) × 71.52 (V)	Note1	mm
Weight	25 (typ.), 28 (max.)		g

Note1: See "7. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

	Parameter	Symbol	Rating	Unit	Remarks
Supply volt	age	VCC	-0.5 to +3.5	V	Ta= 25°C
Logic input	voltage	VI	-0.5 to VCC+0.5	V	Logic signals
	Reverse voltage	VR	≤ 30	V	
	Power dissipation	PD	≤ 738	mW	Ta= 25°C
Backlight	Forward current	IL	Note1	mA	
	Pulse forward current		100	mA	Pulse width ≤ 10 ms, Duty $\leq 1/10$
Storage tem	Storage temperature		-20 to +70	°C	-
Operating to	emperature	Тор	-20 to +70		Product surface Note2
			≤ 95		Ta≤ 40°C
Relative hu	midity Note3	RH	≤ 85	%	40°C <ta≤ 50°c<="" td=""></ta≤>
			≤ 70		50°C <ta≤ 55°c<="" td=""></ta≤>
Absolute hu	ımidity Note3	АН	≤ 73 Note4	g/m³	Ta> 55°C
Storage alti	tude))	≤ 13,600	m	-20°C ≤ Ta ≤ 70°C
Operating a	ltitude		≤ 4,850	m	-20°C ≤ Ta ≤ 70°C

Note1: Allowable forward current



Note2: Measured at display area

Note3: No condensation

Note4: Water amount at Ta= 55°C and RH= 70%

4.3 ELECTRICAL CHARACTERISTICS

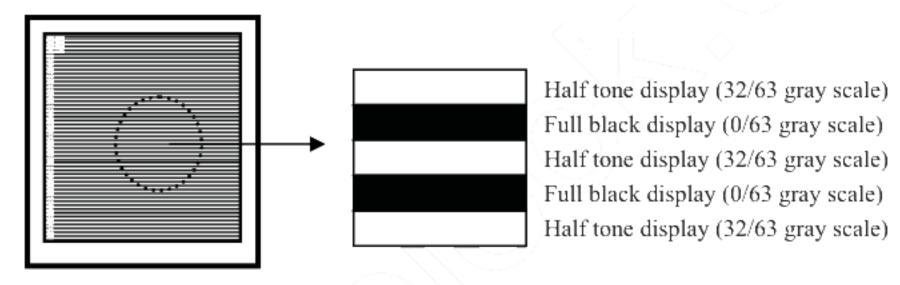
(1) Logic/ LCD driving

 $(Ta=25^{\circ}C)$

Parameter	Symbol	min.	typ.	max.	Unit	Remarks	
Logic supply voltage	VCC	2.85	3.0	3.15	V	-	
Logic input high voltage	VIH	0.8VCC	-	VCC	V	Logic gignal	
Logic input low voltage	VIL	0	-	0.2VCC	V	Logic signal	
COM high voltage	COM/H	-	1.8	-	V	at VCC= 3.0V Note1	
VCC supply current	ICC	-	16.5	26	mA	at VCC= 3.0V Note2	

Note1: The optimum value for COM/H is in the range of 1.3 V to 2.3 V.

Recommended adjustment display for COM/H



Note2: PPCLK= (5.0)MHz, PPHSYNC= (19.53)kHz, PPVSYNC= (60)Hz, Checkered flag pattern (by EIAJ ED-2522)

(2) Backlight

 $(Ta=25^{\circ}C)$

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward Current	/ IL	-	20	22	mA	-
Forward Voltage	VL VL	-	19.2	21.0	V	at IL= 20mA

4.4 SETTING OF THE INTERNAL RESISTER

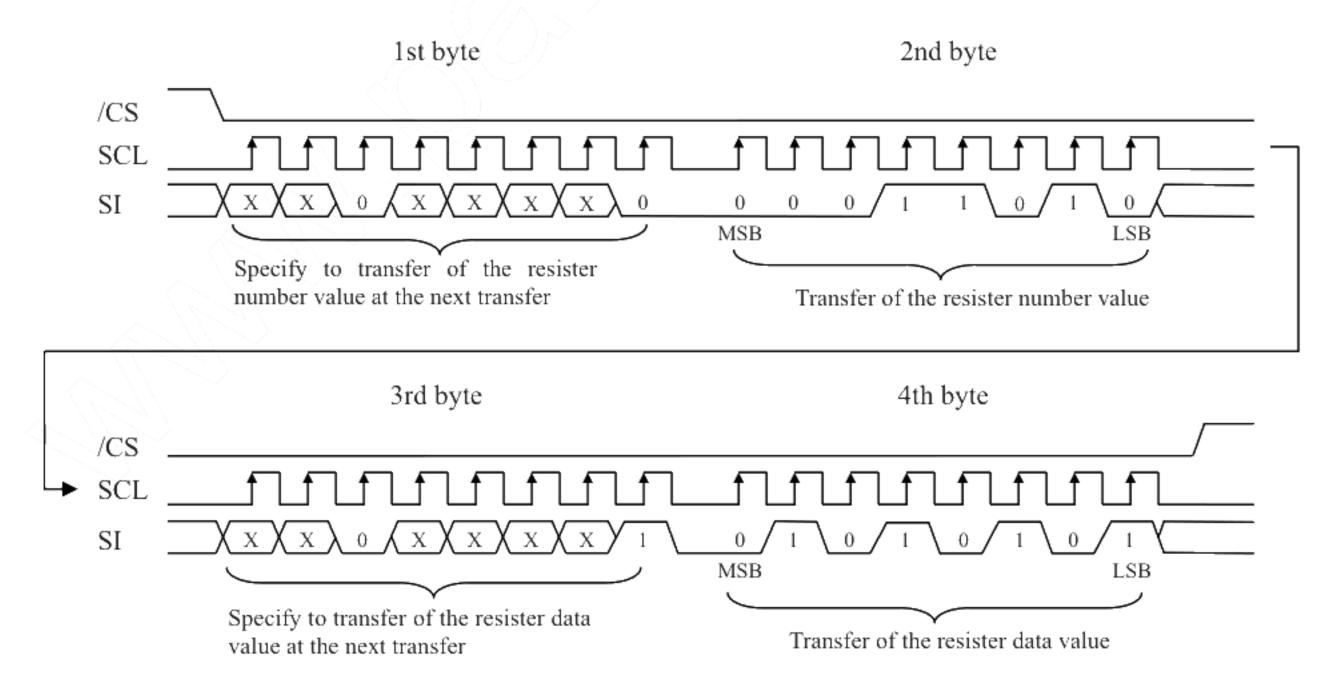
Initial setting of the internal Resister is undefined data. So the Resister Data must be written in the Resister, after initialization by the /RESET pin. The Resister Data can be written from serial interface pins (/CS, SCL and SI). The setting method is as follows.

(1) Command E	3yte Function
---------------	---------------

Bits	Functions	Discription
A7	-	-
A6	-	-
A5	Read / Write	0:Write 1:Read
A4	-	<u>-</u>
A3	-	<u>-</u>
A2	-	- <u>-</u>
Al	-	<u>-</u> ((&)
A0	Register Number / Data	0:Register Number 1:Register Data

(2) Timing chart /CS SCL SI A1 X A0 D7) D0 A3) (A2 X D6 X D5 X D4 X D3 X D2 X D1 / MSB LSB Command Byte Data Byte - Register Number or Register Data

Ex) When data 55h is written to Register R26 (R1Ah)



Note1: During 32-bit transfer of the Resister Data, /CS pin (Pin No.35) must be maintained active.

Note2: "X" is set in accordance with the usage conditions.

(3) Command sequence

①Power On

Sequence	Register Number	Data	Comment	Sequence	Register Number	Data	Comment
1	Power On			26	R76	01h	-
2	1ms min. w	ait.		27	R77	01h	- <
3	Reset by the	e /RESET pi	in (Pin No. 8).	28	R80	00h	
4	1ms min. w	ait after /RE	SET ¹ .	29	R81	00h	
5	R3	01h	-	30	R82	2Eh	
6	R1	00h	-	31	R83	C4h	
7	R100	0Fh	-	32	R86	15h	N. 2/ -
8	R101	3Fh	-	33	R87	EDh	-
9	R102	3Fh	-	34	R95	3Fh	-
10	R103	00h	-	35	R96	22h	-
11	R104	00h	-	36	R25	76h	-
12	R105	30h	-	37 (/	R26	54h	-
13	R106	04h	-	38	R27	67h	-
14	R107	37h	- /	39	R28	60h	-
15	R108	17h	-	40	R29	04h	
16	R109	00h	- /	41	R30	1Ch	-
17	R110	40h	- (()	42	R31	A9h	-
18	R111	30h	(5)\\	43	R32	00h	-
19	R112	04h	↑ <u></u>	44	R33	20h	-
20	R113	37h	<u> </u>	45	R24	77h	-
21	R114	17h	0//52	46	30 μs min	. wait.	
22	R115	00h	KU-7-	47	Data input start		
23	R116	40h)) -	48	R59	01h	-
24	R2	40h	-	49	20 ms min. wait.		
25	R75	04h	<u> </u>	50	R0	00h	49

②Power Off

Sequence	Register Number	Data	Comment		
1	R0	08h	-		
2	25 ms min. wait.				
3	R24	00h	-		
4	20 ms mir	ı. wait.			
5	R1	08h	-		
6	Power Off				

$\\ {\it \ \, 3} Standby$

Sequence	Register Number	Data	Comment		
1	R0	08h	-		
2	25 ms min. wait.				
3	R24	00h	-		
4	20 ms min. wait.				
5	R1	08h	-		

Wake Up

Sequence	Register Number	Data	Comment		
1	R1	00h	-		
2	R24	FFh	-		
3	30 μs min. wait.				
4	R0	00h	-		

Note1: Be sure to perform reset by the /RESET pin (Pin No. 8) every power-on

Note2: Write the Resister Data every power-on, because the data are not stored in the product.

4.5 INTERFACE PIN CONNECTIONS

CN1 (FPC)

Adaptable socket: FH23-45S-0.3SHW(05) (Hirose Electric Co., Ltd.(HRS))

Pin No.	Symbols	Function	ons	Pin No.	Symbols	Function	IS		
1	GND	Ground	Notel	25	D20	Red data (LSB)			
2	GND	Ground	Note1	26	D21	Red data	22		
3	VCC			27	D22	Red data	<u> </u>		
4	VCC	Dowar gunnly	Note1	28	D23	Red data			
5	VCC	Power supply	Note1	29	D24	Red data	\sim		
6	VCC			30	D25	Red data (MSB)			
7	GND	Ground	Note1	31	GND	Ground	Note1		
8	/RESET	Reset		32	SCL	Serial clock			
9	HSYNC	Horizontal synchro	nous signal	33	SI	Serial input			
10	VSYNC	Vertical synchronou	ıs signal	34	RSVD	Keep this pin Open.			
11	CLK	Dot clock		35	/CS	Chip selection			
12	GND	Ground	Note1	36	VCOMIN	COM high voltage in	put		
13	D00	Blue data (LSB)		37	N.C.	Keep this pin Open.			
14	D01	Blue data		38	GND	Ground	Note1		
15	D02	Blue data		39	N.C.				
16	D03	Blue data		40	N.C.	Vaan this nin Onan			
17	D04	Blue data	<i></i>	41	N.C.	Keep this pin Open.			
18	D05	Blue data (MSB)	\mathcal{C}	42	N.C.				
19	D10	Green data (LSB)		/ 43	GND	Ground	Note1		
20	D11	Green data		44	ANODE	LED voltage (Anode)			
21	D12	Green data		45	CATHODE	LED voltage (Cathod	e)		
22	D13	Green data	>, \						
23	D14	Green data		1					
24	D15	Green data (MSB)		1					

Note1: All GND terminals should be used without any non-connected lines.

Note2: Do not fold the FPC. When folding the FPC, pattern disconnection may occur. In case of bending FPC, the minimum curvature (R) must be more than 1.0 mm.

Description of terminals

Terminals	Description					
	When /RESET is L, an internal reset is performed.					
/RESET	The reset operation is executed at the /RESET signal level.					
	Be sure to perform reset by this pin every power-on.					
/CS	This pin is used for chip selection signals. When /CS= L, the chip is active and can					
/03	perform data I/O operations including command and data I/O.					
SCL	This pin is clock input of serial interface.					
SI	This pin is data input of serial interface.					
VCOMIN	This pin is the Common high voltage. The voltage needs to be adjusted.					
VCOMIN	See "3 BLOCK DIAGRAM - Reference design of COM circuit".					
ANODE,	Defer to the below "Circuits of beaklight"					
CATHODE	Refer to the below "Circuits of backlight".					

Note1: Circuits of backlight



Circuits of backlight

4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

This product can display in equivalent to 262,144 colors in 64 gray scales. Also the relation between display colors and input data signals is as the following table.

Display	colors						Data	signa	1 (0:	Low	leve	l, 1: l	High I	level)					
Display		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	В5	В4	В3	В2	В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0,0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
colors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0 /	0	0	0
03	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
Basic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
ñ	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	I	I	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0		0	0	0	0	0	1	0	0	0	0	/ 0	0	0	0	0	0	0	0
scale	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray	1										: \						:		
55,	↓									4							:		
Rec	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	$\frac{1}{}$	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scale		0	0	0	0	0	- 0	0	0	0	0	0	1	0	0	0	0	0	0
38 /	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green gray	<u> </u>							7			:						:		
en en	↓										:						:		
Gre	bright	0	0	0	0	0	0	1	1	1	1	0	I	0	0	0	0	0	0
	C	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	<u>I</u>	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u> </u>		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
ည်း	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale		\ :																	
) e	 	0	0	0	0	0	0	0	0	0	: 0	0	0	1	1	1	. 1	0	,
<u>B</u>	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Plue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Blue	U	U	U	U	U	U	<u> </u>	U	υ	U	U	U	1	1	1	1	1	1

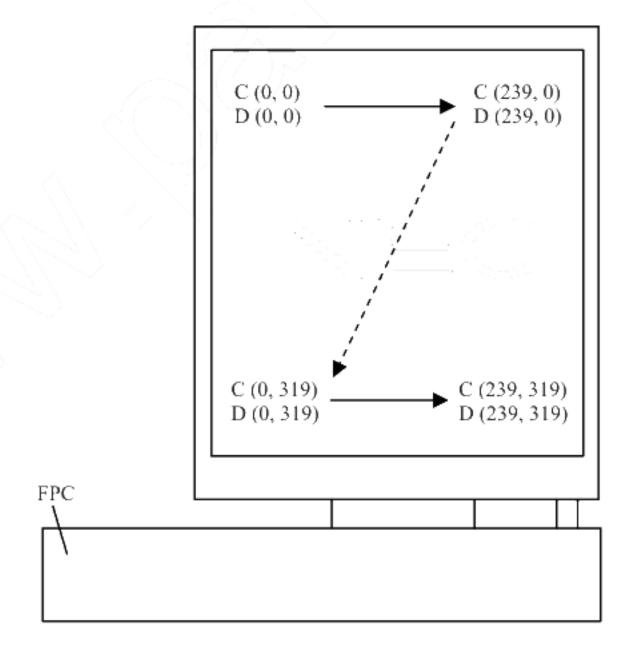
4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See figure of "4.8 SCANNING DIRECTIONS".).

C (0, 0)	C (1,	0)				
R G	B R G	В				
	1					
C(0, 0)	C(1, 0)	•••	C(X, 0)	•••	C(238, 0)	C(239, 0)
C(0, 1)	C(1, 1)	•••	C(X, 1)	•••	C(238, 1)	C(239, 1)
•	•	•	•	•	•\) •
:	:	• • •		• • •		•••
C(0, Y)	C(1, Y)	•••	C(X, Y)	•••	C(238, Y)	C(239, Y)
•	•	•	•	(• L.	○ •	•
:		• • •			•	
C(0, 318)	C(1, 318)	• • •	C(X, 318)		C(238, 318)	C(239, 318)
C(0, 319)	C(1, 319)	•••	C(X, 319)	\ ···	C(238, 319)	C(239, 319)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view. Also the arrow shows the direction of scan.



Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

D (X, Y): The data number of input signal for LCD panel

4.9 INPUT SIGNAL TIMINGS

4.9.1 RGB interface (Ta= 25°C, VCC= 3.0V)

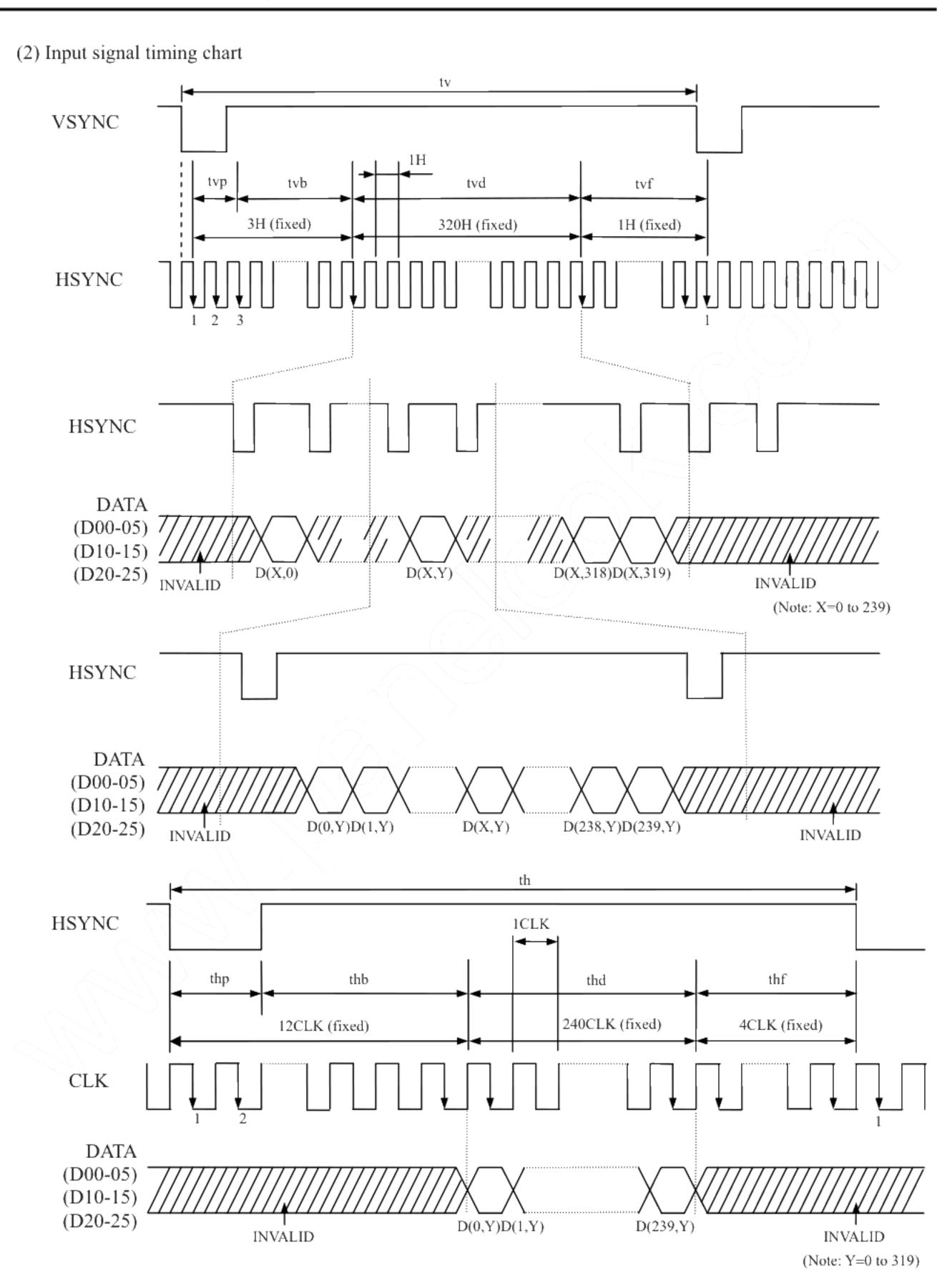
(1) Timing characteristics

	Parameter		Symbol	min.	typ.	max.	Unit	Remarks
	Frequency		1/tc	4.81	5.0	5.12	MHz	200ns (typ.)
CLK	Duty	Duty		0.4	0.5	0.6	-	
	Rise time, Fall	time	terf	-	-	15	ns	$\langle \langle (\cdot \setminus \overline{\cdot}, \cdot) \rangle \rangle$
DATA	CLK-DATA	Setup time	tds	15	-	-	ns	
(D00-05) (D10-15)	CLK-DAIA	Hold time	tdh	15	-	-	ns	<u> </u>
(D20-25)	Rise time, Fall	time	tdrf	-	-	15 /	ns	Z'
	Cycle		4h	50.0	51.2	53.2	μs	19.53kHz (typ.)
	Cycle		th		256	_	CLK	
	Display period		thd		240	L	CLK	
	Front-porch		thf		4	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	CLK	
HSYNC	Pulse width		thp	2	8	, ×-	CLK	
	Back-porch	Back-porch		4-//			CLK	-
	CLK-	Setup time	ths	15	\) -	-	ns	
	HSYNC	Hold time	thh	15	// -	-	ns	
	Rise time, Fall	time	thrf	<u> </u>	-	15	ns	
	Cuala			16.2	16.59	17.24	ms	60Hz (typ.)
	Cycle		tv	324			Н	
	Display period		tvd		320		Н	
VEVNC	Front-porch	77 N	tvf		1		Н	
VSINC	VSYNC Pulse width		tvp	1	2	-	Н	-
	Back-porch		tvb		1		Н	
	VSYNC-HSY	NC timing	tvh	15	-	-	ns	
	Rise time, Fall	time	tvrf	-	-	15	ns	

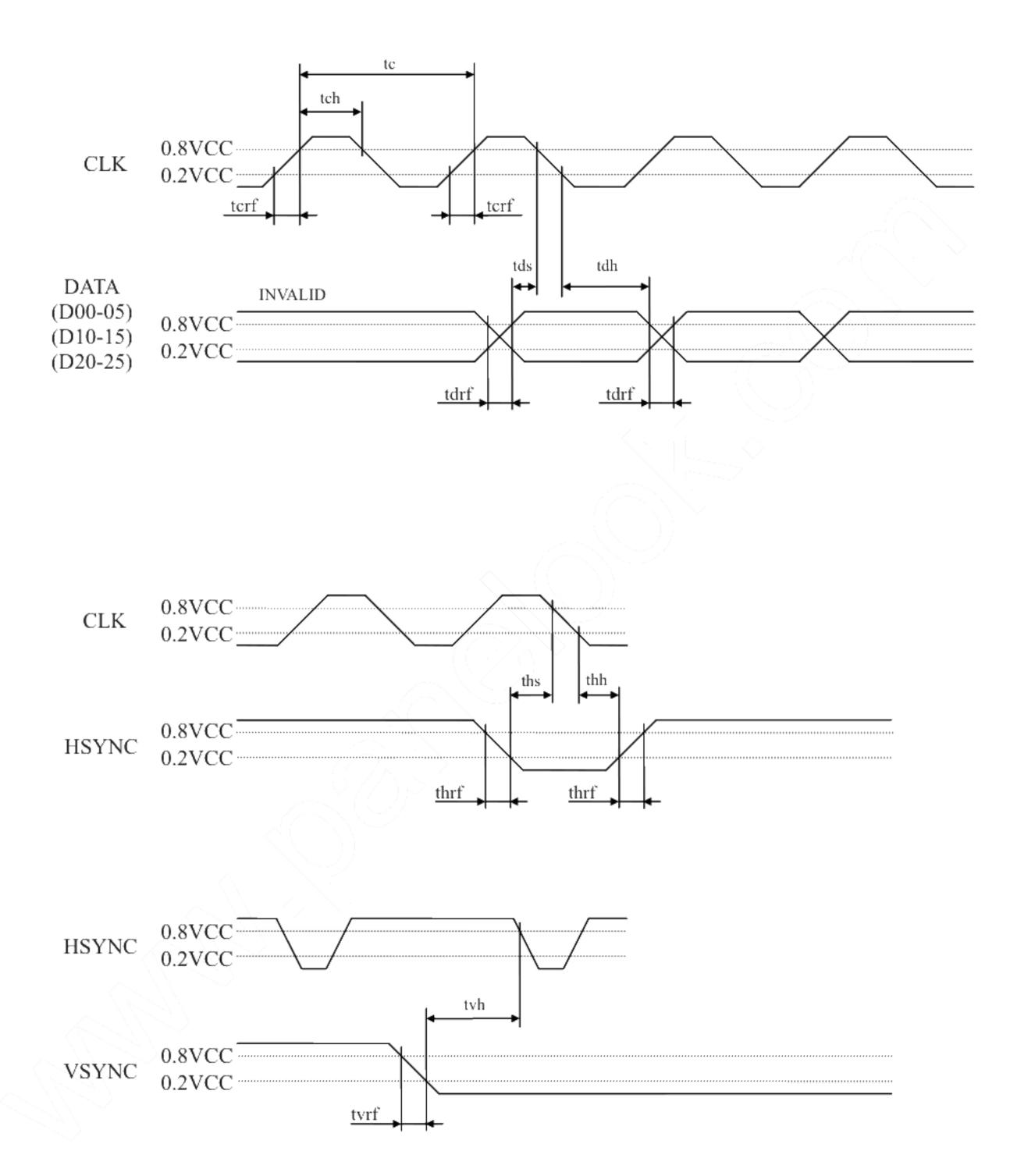
Note1: Definition of parameters is as follows.

te= 1CLK, ted= tch/te, th= 1H

Note2: All parameters should be kept within the specified range.



Note1: Unless otherwise specified, the input level is defined to be VIH= 0.8VCC, VIL= 0.2VCC.



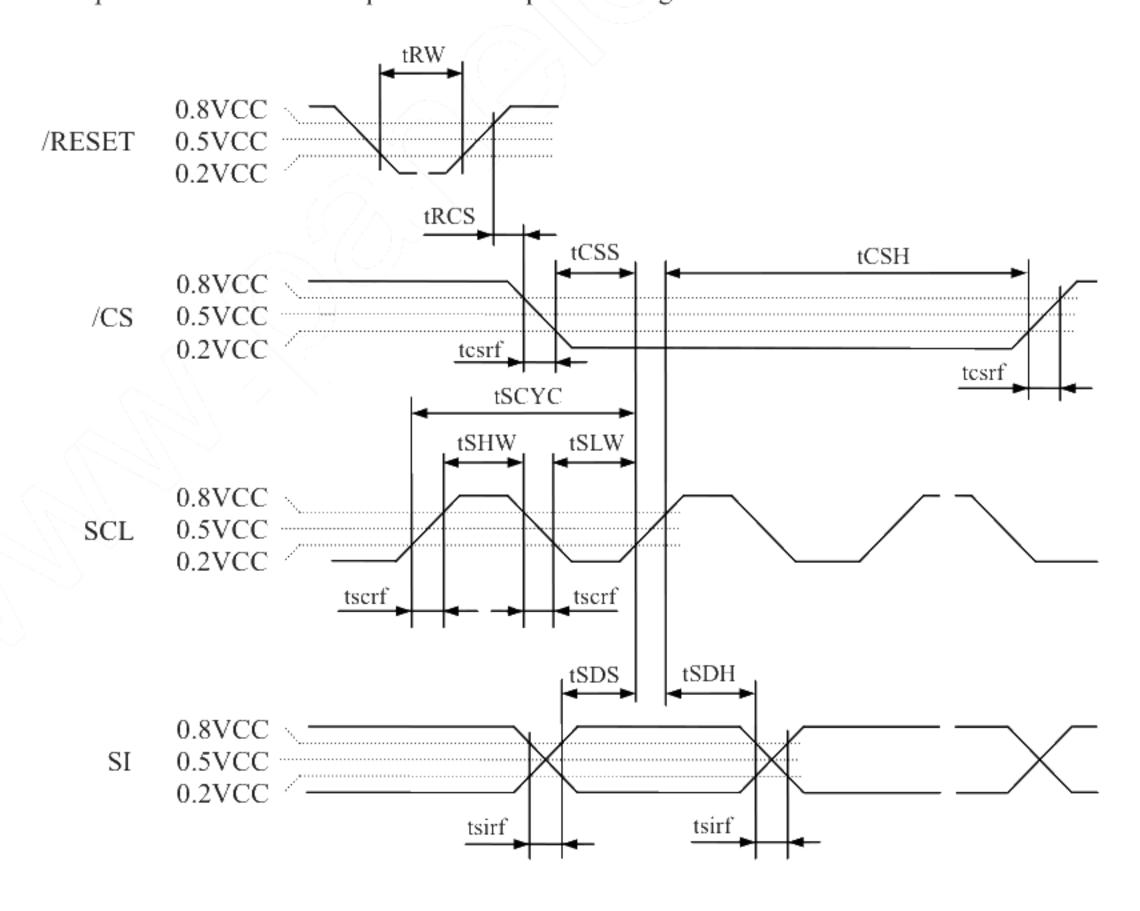
Note1: Unless otherwise specified, the input level is defined to be VIH= 0.8VCC, VIL= 0.2VCC.

4.9.2 Serial interface (Ta= 25°C, VCC= 3.0V)

(1) Timing characteristics

Parameter	Symbol	Condition	min.	typ.	max.	Unit	Remarks
Serial clock cycle	tSCYC	READ	450	-	-	ns	-
Serial clock cycle	iscre	WRITE	100	-	-	ns	-
SCI high lavel pulse width	tSHW	READ	210	-	-	ns	, ·
SCL high level pulse width	ISHW	WRITE	40	-	-	ns	
SCI Jaw lavel pulse width	tSLW	READ	210	-	-	ns	<u> </u>
SCL low level pulse width	ISLW	WRITE	40	-		ns	· .
/CS rise time, fall time	tesrf	/CS	-	-	15	ns	-
SCL rise time, fall time	tscrf	SCL	-	-	15	ns	-
SI rise time, fall time	tsirf	SI	-	- (15	ns	-
/CS setup time	tCSS	/CS	50	-	\ <u>\</u> -57	ns	-
/CS hold time	tCSH	/CS	30		O -	ns	-
Data setup time	tSDS	SI	30	J-227	-	ns	-
Data hold time	tSDH	SI	15		-	ns	-
Reset pulse width	tRW	/RESET	2	<i>)</i> -	-	μs	-
/RESET↑ to /CS time	tRCS	/RESET↑ to /CS	<u></u> T ≤	-	-	ms	-

Note1: All parameters should be kept within the specified range.



Note2: Unless otherwise specified, the input level is defined to be VIH= 0.8VCC, VIL= 0.2VCC.

4.10 OPTICAL CHARACTERISTICS

<Backlight turning OFF>

(Note1, Note3, Note4)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Reflectance ratio	White, at center	RE	10	15	-	%	Note6
Contrast ratio	White/Black, at center	CR	10	15	-	- 75	Note7

Reference data

(Note1, Note3, Note4)

Parameter	Condition		Symbol	min.	typ.	max.	Unit	Remarks
Chromaticity	Whit	White		-	0.32		<i>y</i> -	
coordinates	VVIIII	C	Wy	-	0.34	<u>.</u> (£	-	Note8
Color gamut	at center, against NT	at center, against NTSC color space		-< .	5	\	%	
Despanse time	White to black	90%→ 10%	Ton	-	7	14	7724	Note9
Response time	Black to white	10%→ 90%	Toff	7/ -	9	18	ms	Note10

<Backlight turning ON>

(Note2, Note3, Note5)

Parameter	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Luminance	White at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	L	150	220	-	cd/m ²	-
Contrast ratio	White/Black at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	CR	75	150	1	-	Note7
Luminance uniformity	White θR= 0°, θL= 0°, θU= 0°, θD= 0° Maximum luminance: 100%	LU	60	70	-	%	Note11

Reference data

(Note2, Note3, Note5)

Parar	neter	Condit	Condition		min.	typ.	max.	Unit	Remarks
Chromaticity		Whit	Wx	0.25	0.30	0.35	-		
coordinate	S	VY IIII	C	Wy	0.27	0.32	0.37	-	Note8
Color gam	ut	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ \theta U = 0^{\circ}, \ \theta D = 0^{\circ}$ at center, against NTSC color space		С	-	40	1	%	
Response t	ima	White to black	90%→ 10%	Ton	-	7	14	,,,,,,	Note9
Kesponse t	iiiie	Black to white	10%→ 90%	Toff	-	23	46	ms	Note10
	Right	θU= 0°, θD= 0°, CR≥ 5		θR	-	35	-	0	
Viewing	Left	θU= 0°, θD=	0°, CR≥ 5	θL	-	35	-	0	
angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}$	0°, CR≥ 5	θU	-	35	-	0	
	Down	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}$	0°, CR≥ 5	θD	-	35	-	0	

Note1: Measurement conditions are as follows.

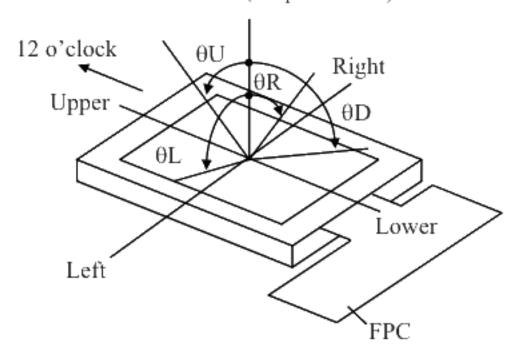
Ta= 25 °C, VCC= 3.0V

Note2: Measurement conditions are as follows.

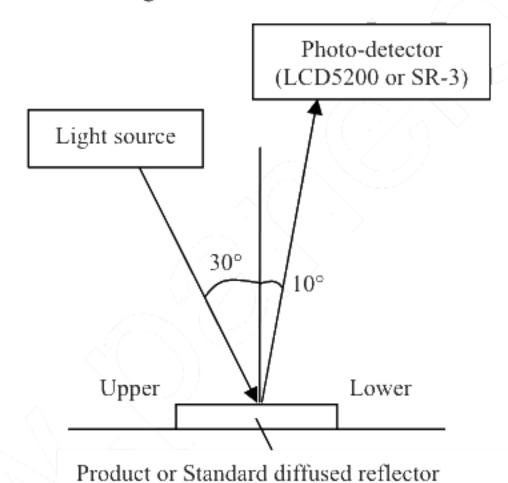
Ta= 25 °C, VCC= 3.0V, IL= 20mA

Note3: Definition of viewing angles

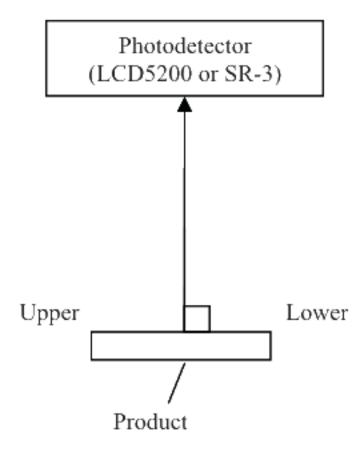
Normal axis (Perpendicular)



Note4: In reflective mode (Backlight turning OFF), Reflectance ratio, Contrast ratio, Chromaticity coordinates and Color gamut are measured as follows.



Note5: In transmissive mode (Backlight turning ON), Luminance, Contrast ratio, Chromaticity coordinates and Color gamut are measured as follows.



Note6: Definitions of reflectance ratio

The reflectance ratio is calculated by using the following formula.

Reflectance (RE) =
$$\frac{\text{Luminance of reflected light at white screen}}{\text{Luminance of standard diffused reflector}} \times 100$$

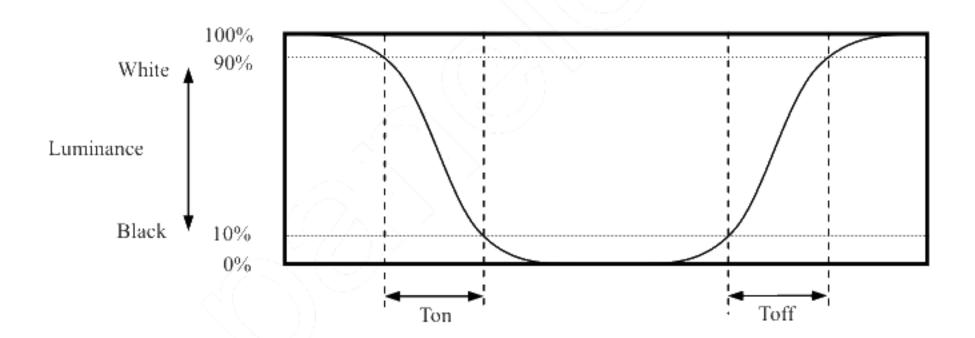
Note7: Definition of contrast ratio

The contrast ratio is calculated by using the following formula.

Note8: The White chromaticity coordinates are deviated by the LED deviation in addition to color filter deviation.

Note9: Definition of response times

Response time is measured, the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time it takes the luminance change from 90% down to 10%. Also Toff is the time it takes the luminance change from 10% up to 90% (See the following diagram.).

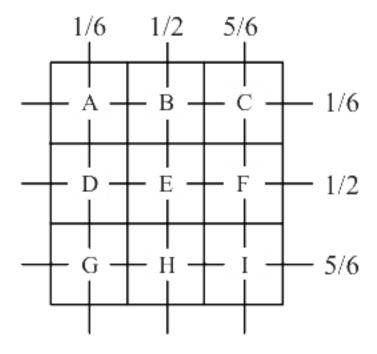


Note10: Product surface temperature: Top= 25°C

Note11: Definition of luminance uniformity

Luminance uniformity is calculated by using the following formula.

Luminance uniformity (LU) =
$$\frac{\text{Minimum luminance from A to I}}{\text{Maximum luminance from A to I}} \times 100$$



5. RELIABILITY TESTS

Test item	Condition	Judgement Note1			
High temperature and humidity (Operation)	 55 ± 2°C, RH = 85%, 240 hours Display data is black. 				
Heat cycle (Operation)					
Thermal shock (Non operation)	 3°C30 minutes 20 ± 3°C30 minutes 100 cycles, 1 hour/cycle Temperature transition time is within 5 minutes. 				
Low pressure (Non operation)	① 15kPa ② -20 ± 3°C24 hours ③ 70 ± 3°C24 hours	No display malfunctions			
Low pressure (Operation)	① 53.3 kPa ② -20 ± 3°C24 hours ③ 70 ± 3°C24 hours				
ESD (Operation)	 150pF, 150Ω, ±10kV 3 places on a panel surface 10 times each places at 1 sec interval 				
Dust (Operation)	 ① Sample dust: No. 15 (by JIS-Z8901) ② 15 seconds stir ③ 8 times repeat at 1 hour interval 				
Vibration (Operation)	(1) 30 to 100Hz, 19.6m/s ² Vibration (2) 30 minutes/cycle				
Mechanical shock (Non operation)	 3,920m/ s², 2.5ms ±X, ±Y, ±Z direction 1 times each directions 	No physical damages			

Note1:Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect specifications.

6. PRECAUTIONS

6.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. Be sure to read "6.2 CAUTIONS" and "6.3 ATTENTIONS", after understanding these contents!



This sign has the meaning that customer will be injured by himself or the product will sustain a damage, if customer has wrong operations.



This sign has the meaning that customer will get an electrical shock, if customer has wrong operations.



This sign has the meaning that customer will be injured by himself, if customer has wrong operations.

6.2 CAUTIONS



* Do not touch the working backlight. There is a danger of an electric shock.



- * Do not touch the working backlight. There is a danger of burn injury.
- * Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: To be not greater 3,920m/s² and to be not greater 2.5ms)

6.3 ATTENTIONS



6.3.1 Handling of the product

- Take hold of both ends without touching the FPC when the product (LCD module) is picked up from the tray.
- ② Do not hook nor pull the FPC in order to avoid any damage.
- 3 When the product is put on the table temporarily, display surface must be placed downward.
- When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ⑤ The product must be installed and/or handled without undue stress such as bends or twist. Bends, twist or undue stress to any portion may cause display failures. And also do not put heavy or hard materials on the product.
- ⑤ Do not hit or rub the surface of panel with hard materials, because it is easily scratched. (Polarizer pencil-hardness: 3H)
- When cleaning the panel surface, wipe it with a soft dry cloth.
- ® Do not push nor pull the FPC while the product is working.
- Do not fold the FPC. When the FPC is folded, pattern disconnection may be caused. In case of bending FPC, the minimum curvature (R) must be more than 1.0 mm.
- When installing the product, do not contact a conductor such as a metal to the FPC excluding the terminal area. There is a risk of short circuit which is caused by breakage of insulation layer of the FPC.
- 1 When installing the product, apply the waterproof design to avoid going of water into the product.

*

6.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurring by temperature difference, the product packing box should be opened after enough time being left under the environment of an unpacking room. Evaluate the leaving time sufficiently because a situation of dew condensation occurring is changed by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with packing state)
- 3 Do not operate in high magnetic field. Circuits may be broken down by it.
- This product is not designed as radiation hardened.

6.3.3 Characteristics

The following items are neither defects nor failures.

- ① Response time, luminance and color may be changed by ambient temperature.
- ② Display mura, flicker, vertical seam or small spot may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking.
- Optical characteristics may be changed depending on input signal timings.

6.3.4 Other

- ① All GND terminals should be used without any non-connected lines.
- ② Do not disassemble the product.
- ③ Pack the product with original shipping package, in order to avoid any damages during transportation, when returning the product to NEC.

-):;

Symbols

Pin No.

Symbols

Pin No.

GND

g VCC

ΛCC

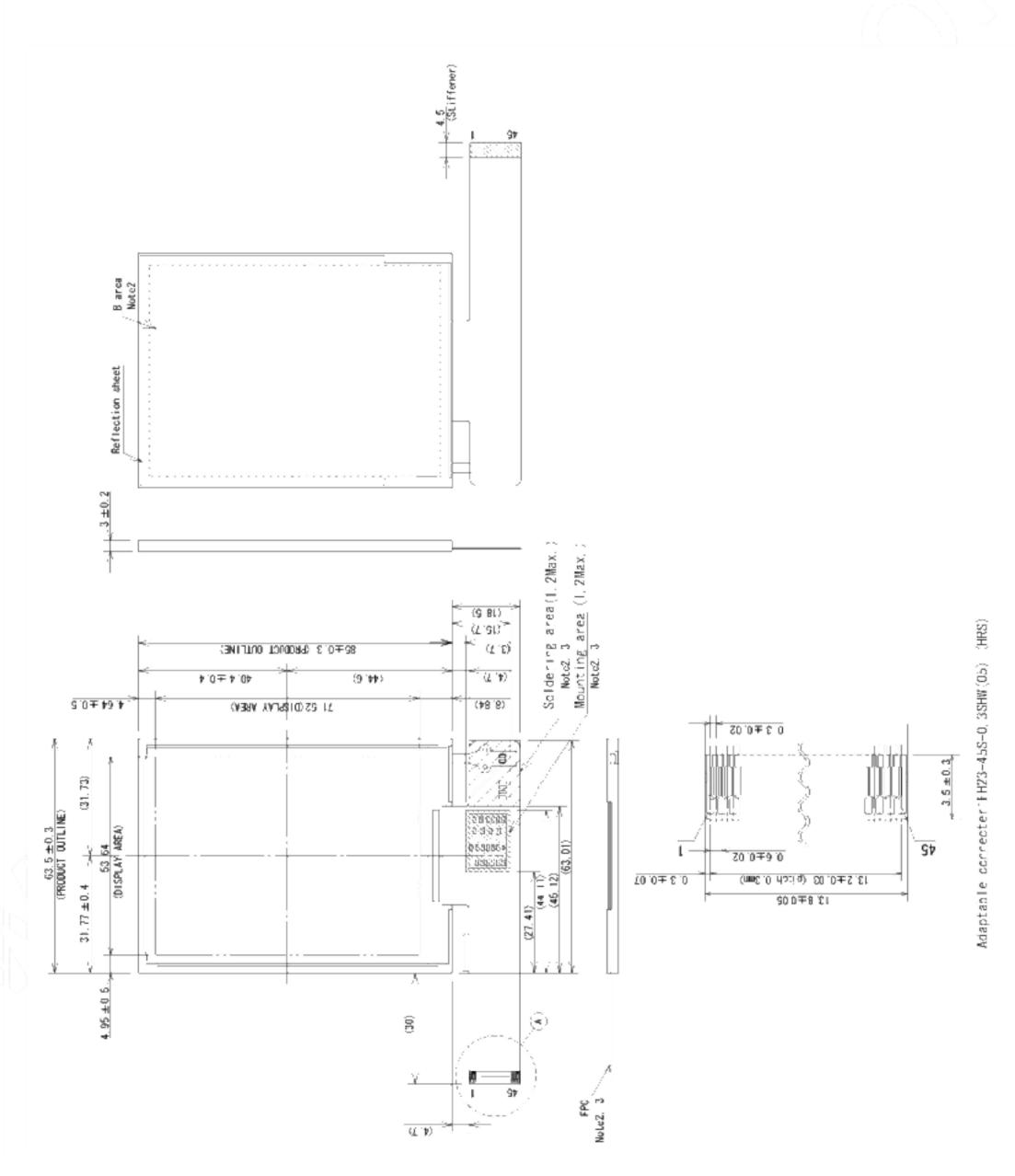
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49

D20

DZI D22 D23

7. OUTLINE DRAWINGS



ANODE

4 8

D11 D12

19 20 21

D13 D14

2 2

D15

GND

43

D10

VCOMIN

3653

ON.

12 \equiv

D00

13

CLK

GND

88

D00

7

N.C.

N.C.

8

D03

16

N.C.

4

D04

17

D05

18

N.C.

Ċ.

33

D02

15

RSVD

34 33

VSYNC

10

SO/

GND

SCL

32

/RESET HSYNC

00 6

GNB

2 6

S

33

D25

30

ΛCC

VCC

YO.

D24

Detail A

Note1: The values in parentheses are for reference.

Note2: When installing the product to the customer equipment, do not apply undue stress to the B area, FPC, Soldering Area and Mounting Area.

If not, it may cause display un-uniformity or break down of the product.

Note3: While the product is working, do not contact a conductor such as a metal to the Soldering Area and Mounting Area of the FPC.