TFT COLOR LCD MODULE

NL10276BC24-21F

31cm (12.1 Type)
XGA
LVDS interface (1port)

PRELIMINARY DATA SHEET

DOD-PP-1260 (2nd edition)

This PRELIMINARY DATA SHEET is updated document from DOD-PP-1256(1).

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

INTRODUCTION

The Copyright to this document belongs to NLT Technologies, Ltd. (hereinafter called "NLT"). No part of this document will be used, reproduced or copied without prior written consent of NLT.

NLT does and will not assume any liability for infringement of patents, copyrights or other intellectual property rights of any third party arising out of or in connection with application of the products described herein except for that directly attributable to mechanisms and workmanship thereof. No license, express or implied, is granted under any patent, copyright or other intellectual property right of NLT.

Some electronic parts/components would fail or malfunction at a certain rate. In spite of every effort to enhance reliability of products by NLT, the possibility of failures and malfunction might not be avoided entirely. To prevent the risks of damage to death, human bodily injury or other property arising out thereof or in connection therewith, each customer is required to take sufficient measures in its safety designs and plans including, but not limited to, redundant system, fire-containment and anti-failure.

The products are classified into three quality grades: "Standard", "Special", and "Specific" of the highest grade of a quality assurance program at the choice of a customer. Each quality grade is designed for applications described below. Any customer who intends to use a product for application other than that of Standard quality grade is required to contact an NLT sales representative in advance.

The **Standard** quality grade applies to the products developed, designed and manufactured in accordance with the NLT standard quality assurance program, which are designed for such application as any failure or malfunction of the products (sets) or parts/components incorporated therein a customer uses are, directly or indirectly, free of any damage to death, human bodily injury or other property, like general electronic devices.

Examples: Computers, office automation equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment, industrial robots, etc.

The **Special** quality grade applies to the products developed, designed and manufactured in accordance with an NLT quality assurance program stricter than the standard one, which are designed for such application as any failure or malfunction of the products (sets) or parts/components incorporated therein a customer uses might directly cause any damage to death, human bodily injury or other property, or such application under more severe condition than that defined in the Standard quality grade without such direct damage.

Examples: Control systems for transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, medical equipment not specifically designed for life support, safety equipment, etc.

The **Specific** quality grade applies to the products developed, designed and manufactured in accordance with the standards or quality assurance program designated by a customer who requires an extremely higher level of reliability and quality for such products.

Examples: Military systems, aircraft control equipment, aerospace equipment, nuclear reactor control systems, medical equipment/devices/systems for life support, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.

CONTENTS

INTRODUCTION	2
4. 0.1.100	
1. OUTLINE	
1.1 STRUCTURE AND PRINCIPLE	
1.2 APPLICATION	
1.3 FEATURES	
2. GENERAL SPECIFICATIONS	
3. BLOCK DIAGRAM	
4. DETAILED SPECIFICATIONS	
4.1 MECHANICAL SPECIFICATIONS	
4.2 ABSOLUTE MAXIMUM RATINGS	
4.3 ELECTRICAL CHARACTERISTICS	
4.3.1 LCD panel signal processing board	9
4.3.2 Backlight lamp	
4.3.3 Power supply voltage ripple	
4.3.4 Fuse	
4.4 POWER SUPPLY VOLTAGE SEQUENCE	11
4.4.1 LCD panel signal processing board	11
4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS	
4.5.1 LCD panel signal processing board	12
4.5.2 Backlight lamp	12
4.5.4 Connection between receiver and transmitter for LVDS	1.1
4.5.4 Confiection between receiver and transmitter for LVDS	
4.6 DISPLAY COLORS AND INPUT DATA SIGNALS	
4.6.1 Combinations of input data signals, FRC and MSL signal	
4.6.2 16,777,216 colors	10
4.6.3 262,144 colors	20
4.7 DISPLAY POSITIONS	21
4.8 SCANNING DIRECTIONS	
4.9 INPUT SIGNAL TIMINGS	
4.9.1 Outline of input signal timings	22
4.9.2 Timing characteristics	23
4.9.3 Input signal timing chart	
4.10 OPTICS	25
4.10.1 Optical characteristics	25
4.10.2 Definition of contrast ratio	
4.10.3 Definition of luminance uniformity	
4.10.4 Definition of response times	26
4.10.5 Definition of viewing angles	
5. ESTIMATED LUMINANCE LIFETIME	27
6. RELIABILITY TESTS	28
7. PRECAUTIONS	29
7.1 MEANING OF CAUTION SIGNS	
7.2 CAUTIONS	29
7.3 ATTENTIONS	29
7.3.1 Handling of the product	
7.3.2 Environment	30
7.3.3 Characteristics	30
7.3.4 Others	
8. OUTLINE DRAWINGS	31
8.1 FRONT VIEW	31
8.2 REAR VIEW	32
REVISION HISTORY	33

NL10276BC24-21F

1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL10276BC24-21F is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

• For industrial use

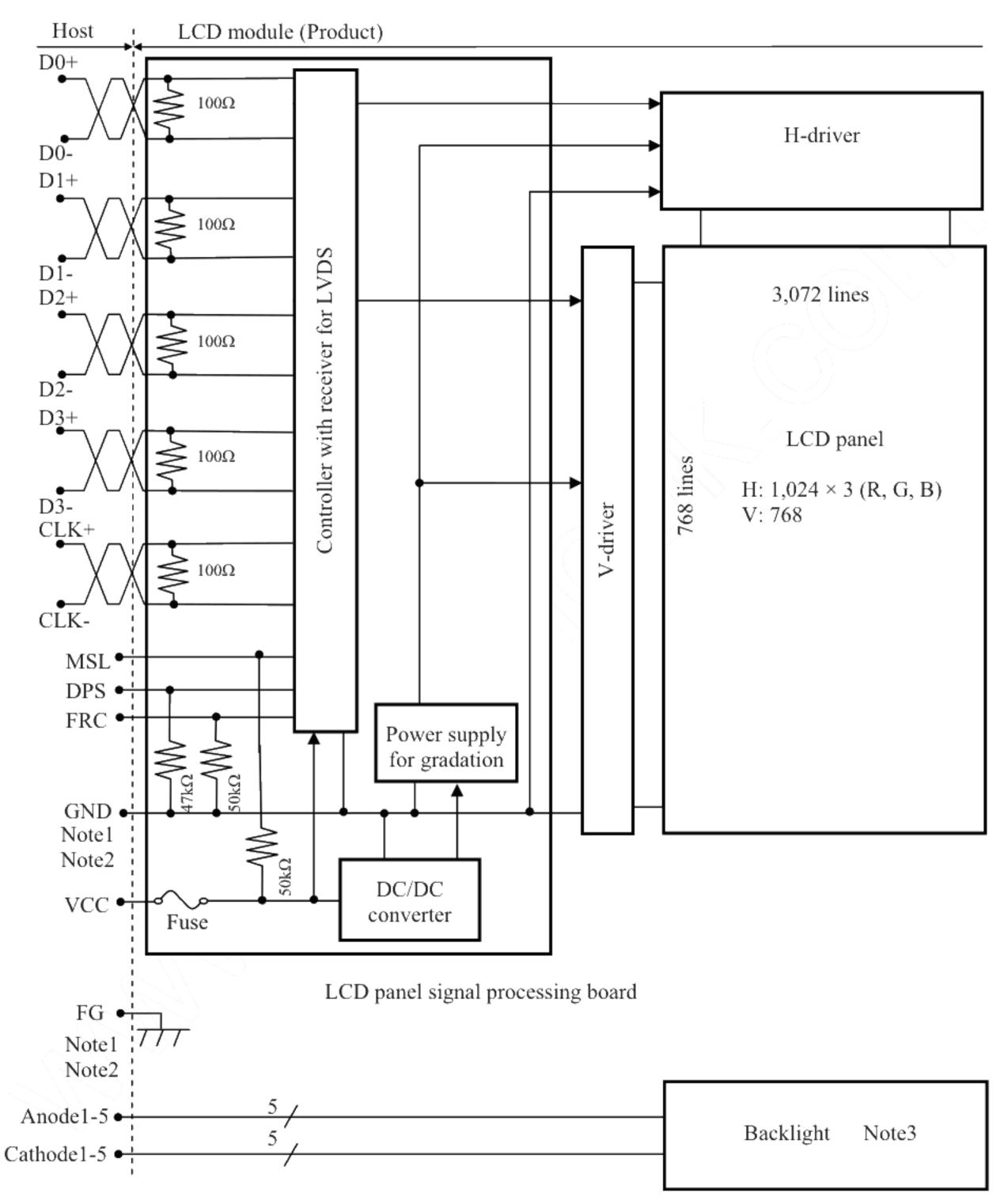
1.3 FEATURES

- Long life LED backlight type
- High luminance
- High contrast
- Wide viewing angle
- Wide temperature range
- LVDS interface
- · Reversible-scan direction
- Selectable 8bit or 6bit digital signals for data of RGB
- Replaceable lamp for backlight
- Acquisition product for UL60950-1/CSA C22.2 No.60950-1-03 (File number: E170632)
- Compliant with the European RoHS directive (2002/95/EC)

2. GENERAL SPECIFICATIONS

Display area	245.76 (H) × 184.32	(V) mm					
Diagonal size of display	31cm (12.1 inches)						
Drive system	a-Si TFT active matr	ix					
D: 1 1	16,777,216 colors (A	t 8-bit input, FRC terminal= High)					
Display color	262,144 colors (At 6-bit input, FRC terminal= Low or Open)						
Pixel	1,024 (H) × 768 (V)	1,024 (H) × 768 (V) pixels					
Pixel arrangement	RGB (Red dot, Green	n dot, Blue dot) vertical stripe					
Dot pitch	$0.08 \text{ (H)} \times 0.24 \text{ (V)} \text{ n}$	nm					
Pixel pitch	$0.24 \text{ (H)} \times 0.24 \text{ (V)} \text{ n}$	nm					
Module size	260.5 (W) × 203.0 (H	H) × 8.7 (D) mm (typ.)					
Weight	490 g (typ.)						
Contrast ratio	900:1 (typ.)						
	At the contrast ratio	≥10:1					
Viewing angle	1	side 80° (typ.), Left side 80° (typ.)					
		80° (typ.), Down side 80° (typ.)					
	At DPS= Low or Ope						
Designed viewing		without image reversal: Up side (12 o'clock)					
direction	_	with contrast peak: Down side (6 o'clock)					
	Viewing angle wi	th optimum grayscale (γ≒ 2.2): Normal axis					
Dolarizar surface	Clear	(perpendicular)					
Polarizer surface Polarizer	Cicai						
pencil-hardness	3H (min.) [by JIS K5	[600]					
	At, LCD panel center						
Color gamut	* / / /	ist NTSC color space]					
Dagnanga tima	$Ton+Toff (10\% \longleftrightarrow 90\%)$						
Response time	18 ms (typ.)						
Luminance	Luminance 1	800 cd/m ² (typ.), $At IL = 50mA/One \ circuit$					
Luminance	Luminance 2	1000 cd/m ² (typ.), AIL= 70mA/One circuit					
	LVDS 1port						
Signal system	(Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent)						
Signal system	[8bit/6bit digital signals for data of RGB colors, Dot clock (CLK),						
	Data enable (DE)]						
Power supply voltage	LCD panel signal pro	ocessing board: 3.3V					
	LED backlight type:						
	Replaceable part						
	1	Type No. 121LHS32					
		- J P					
		Odriver board (Option)					
Backlight	LED driver board	d:					
	Luminance 1	Type No. 104PW03F					
	Luminance 2	user					
	Corresponding w	viring harness:					
	Luminance 1	Type No. 121CBL03					
	Luminance 2	user					
D	Luminance 1	At IL= 50mA/One circuit, Checkered flag pattern 7.52 W (typ.)					
Power consumption	I main and 2	At IL= 70mA/One circuit, Checkered flag pattern					
	Luminance 2	10.3 W (typ.)					

3. BLOCK DIAGRAM



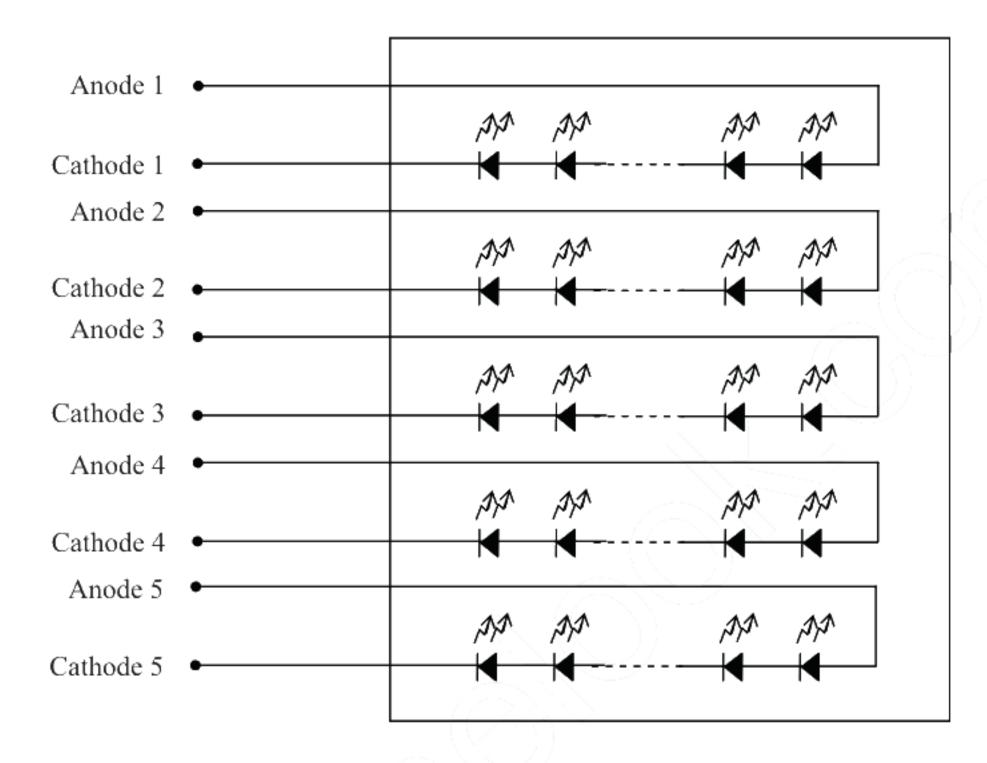
Note1: Relations between GND (Signal ground) and FG (Frame ground) in the LCD module are as follows.

GND - FG Not connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds be connected together in customer equipment.

Note3: Backlight in detail

Backlight



4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$260.5 \pm 0.5 \text{ (W)} \times 203.0 \pm 0.5 \text{ (H)} \times 8.7 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	245.76 (H) × 184.32 (V)	Notel	mm
Weight	490(typ.), 540(max.)		g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

Parameter				Rating	Unit	Remarks	
Power supply voltage	LCD panel sign	nal processing board	VCC	-0.3 to +4.0	V		
Input valtaga	-	ay signals Note1	VD			-	
Input voltage for signals		ion signals Note2	VF	-0.3 to VCC +0.3	V		
Backlight	Forward	Luminance 1	IL	60	A	por ope circuit	
Backlight	current	Luminance 2	I IL	80	mA	per one circuit	
	Storage temperature			-30 to +80	°C	-	
	Luminance 1	Front surface		-30 to +80		Note3	
Operating		Rear surface	TopR	-30 to +80		Note4	
temperature	Luminance 2	Front surface	TopF	-30 to +60		Note5	
		Rear surface	TopR	-30 to +60		Note6	
				≤ 95	%	Ta ≤ 40°C	
		_))		≤ 85	%	40°C < Ta ≤ 50°C	
	Relative humid Note5	lity	RH	≤ 55	%	50°C < Ta ≤ 60°C	
				≤ 36	%	60°C < Ta ≤ 70°C	
			≤ 24	%	70°C < Ta ≤ 80°C		
	Absolute humic Note7	АН	≤ 70 Note8	g/m	-		

Note1: D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-

Note2: DPS, FRC and MSL.

Note3: Measured at LCD panel surface (including self-heat)

Note4: Measured at the center of LCD panel surface (including self-heat)

Note5: Measured at LCD module's rear shield surface (including self-heat)

Note6: Measured at the center of LCD module's rear shield surface (including self-heat)

Note7: No condensation

Note8: Water amount at Ta= 80°C and RH= 24%

2

2

NL10276BC24-21F

4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

 $(Ta=25^{\circ}C)$

Parameter	Symbol	min.	typ.	max.	Unit	Remarks	
Power supply voltage		VCC	3.0	3.3	3.6	V	-
Power supply current		ICC	-	460 Note1	720 Note2	mA	at VCC= 3.3V
Permissible ripple voltage		VRP	-	-	100	mVp-p	for VCC
Differential input threshold	High	VTH	-	-	+100	mV	at VCM= 1.2V
voltage	Low	VTL	-100	-	-	mV	Note3
Terminating resistance		RT	-	100	<u></u>	Ω	-
Input voltage for DPS, FRC	High	VFH	0.7VCC	-	VCC	, V	CMOS level
and MSL signals	Low	VFL	0	-((0.3VCC	V	CWOS level
Input current for DPS, FRC	High	IFH			300	μΑ	
and MSL signals	Low	IFL	-300		-	μΑ	_

Note1: Checkered flag pattern [by EIAJ ED-2522]

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver

NL10276BC24-21F

4.3.2 Backlight lamp

1. Luminance 1: 800 cd/m² (typ.)

(Ta= 25°C, Note1, Note2 Note3)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	50.0	55.0	mA	-
		21.2	24	27.2	V	Ta= +25°C at IL= 50 mA /One circuit
Forward Voltage	VL	19.0	-	-	V	Ta= +80°C at IL= 50 mA /One circuit
Porward voltage		V L	-	-	29.8	v
		-	-	30.1	v	Ta= -30°C at IL= 55 mA /One circuit

2. Luminance 2: 1000 cd/m² (typ.)

(Ta= 25°C, Note1, Note2 Note3)

						· ,
Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	- 🤇	70	77	mA	-
		22.0	25.1	28.3	V	Ta= +25°C at IL= 70 mA /One circuit
Forward Voltage	VL	20.3	-		V	Ta= +60°C at IL= 70 mA /One circuit
rorward voltage		-	-	31.1	V	Ta= -30°C at IL= 70 mA /One circuit
		-	-	31.4	V	Ta= -30°C at IL= 77 mA /One circuit

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The Luminance uniformity may be changed depending on the current variation between 5 circuits. It is recommended that the current value difference among the circuits be less than 5%.

4.3.3 Power supply voltage ripple

This product works if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power sup	ply voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

2

2

2

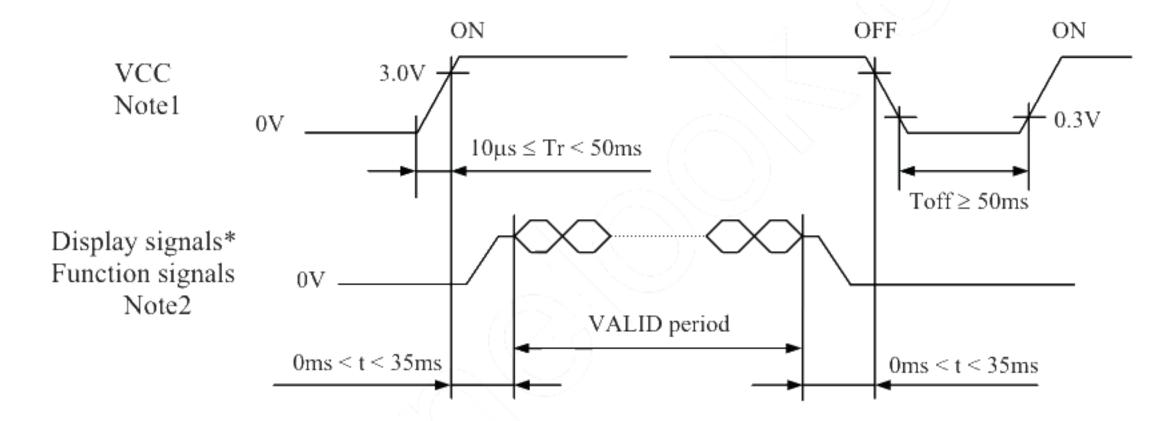
4.3.4 Fuse

Parameter		Fuse	Rating	Fusing current	Remarks	
rarameter	Туре	Supplier	Kanng	Fusing current	Remarks	
VCC	FCC16202AB	KAMAYA ELECTRIC	2.0A	4.0A	Note1	
	I CC10202AB	Co., Ltd.	36V	4.0A		

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



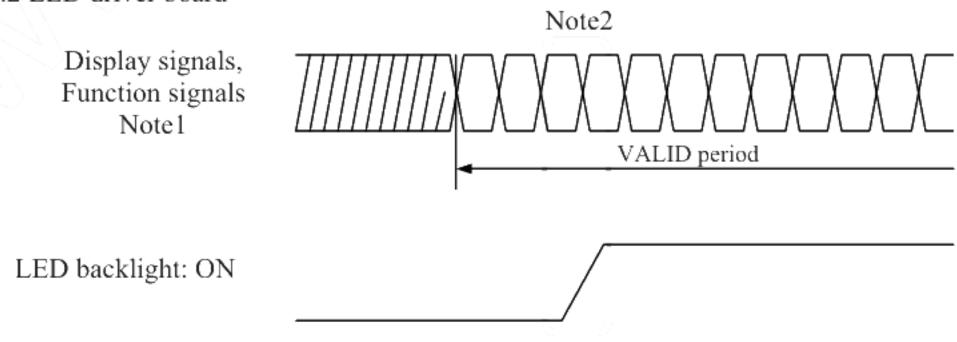
^{*} These signals should be measured at the terminal of 100Ω resistance.

Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-) and function signals (DPS, FRC and MSL) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

4.4.2 LED driver board



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE))
Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JAE))

A	iapta	ible plug:	r I	-8208 (Japan)	Aviation Electronics	industry Limit	ea (JAE))
Pin No. Symbol Signal		Signal	Input data	signal: 8bit	Input data	Remarks	
1 1111	INO.	Syllibol	Signai	MAP A	MAP B	signal: 6bit	- Remarks
1	A	D3+	Pixel data	R0-R1,G0-G1,B0-B1	- 76	Note1, Note2	
	В	GND	Ground		-	Ground	Note3
2	Α	D3-	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7		Note1, Note2
	В	GND	Ground			Ground	Note3
3	3	DPS	Selection of scan direction		Reverse scan Normal scan		Note4
4	4	FRC	Selection of the number of colors	Hi	gh	Low or Open	Note1 Note5
4	5	GND	Ground		Ground		Note3
(5	CLK+			Pixel clock		
1	7	CLK-	Pixel clock		Note2		
8	8	GND	Ground		Note3		
Ġ	9	D2+	Pixel data	B4-B7,DE	Note2		
1	0	D2-	r ixei data	D4-B7,DE	B2-B5,D	L	Notez
1	1	GND	Ground		Ground		Note3
1	2	D1+	Pixel data	G3-G7,B2-B3	G1-G5,B0	-R1	Note2
1	3	D1-	1 ixer data	G3-G7,B2-B3	G1-G5,B0	-101	Notez
1	4	GND	Ground		Ground		Note3
1	5	D0+	Pixel data	R2-R7,G2	R0-R5,G	40	Note2
1	6	D0-)	K2-K7,G2	Note2		
1	7	GND	Ground		Note3		
1	8	MSL	Selection of LVDS input map	Low High L			Note5
19 VCC			Power supply		Note3		
2	0	VCC	1 Ower suppry	Power supply Note		110103	

Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: All GND and VCC terminals should be used without any non-connected lines.

Note4: See "4.8 SCANNING DIRECTIONS".

Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".

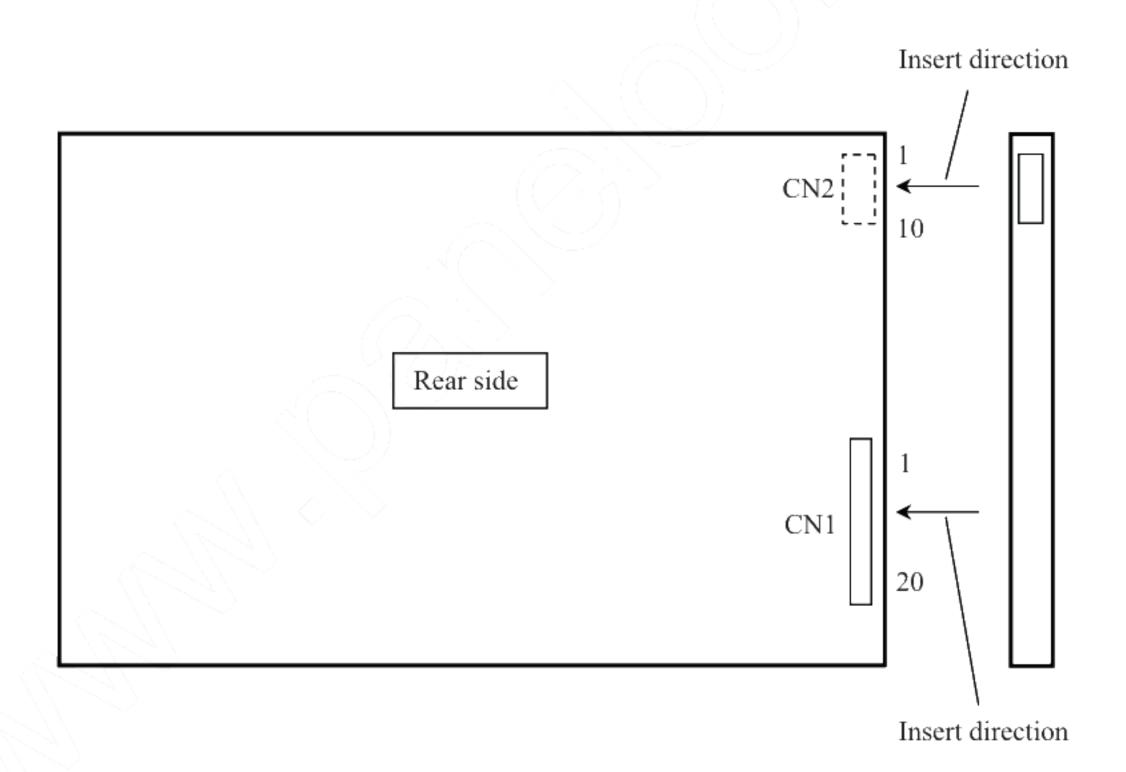
4.5.2 Backlight lamp

CN2 plug (LCD module side): SM10B-SRSS-TB (J.S.T. Mfg. Co., Ltd.)

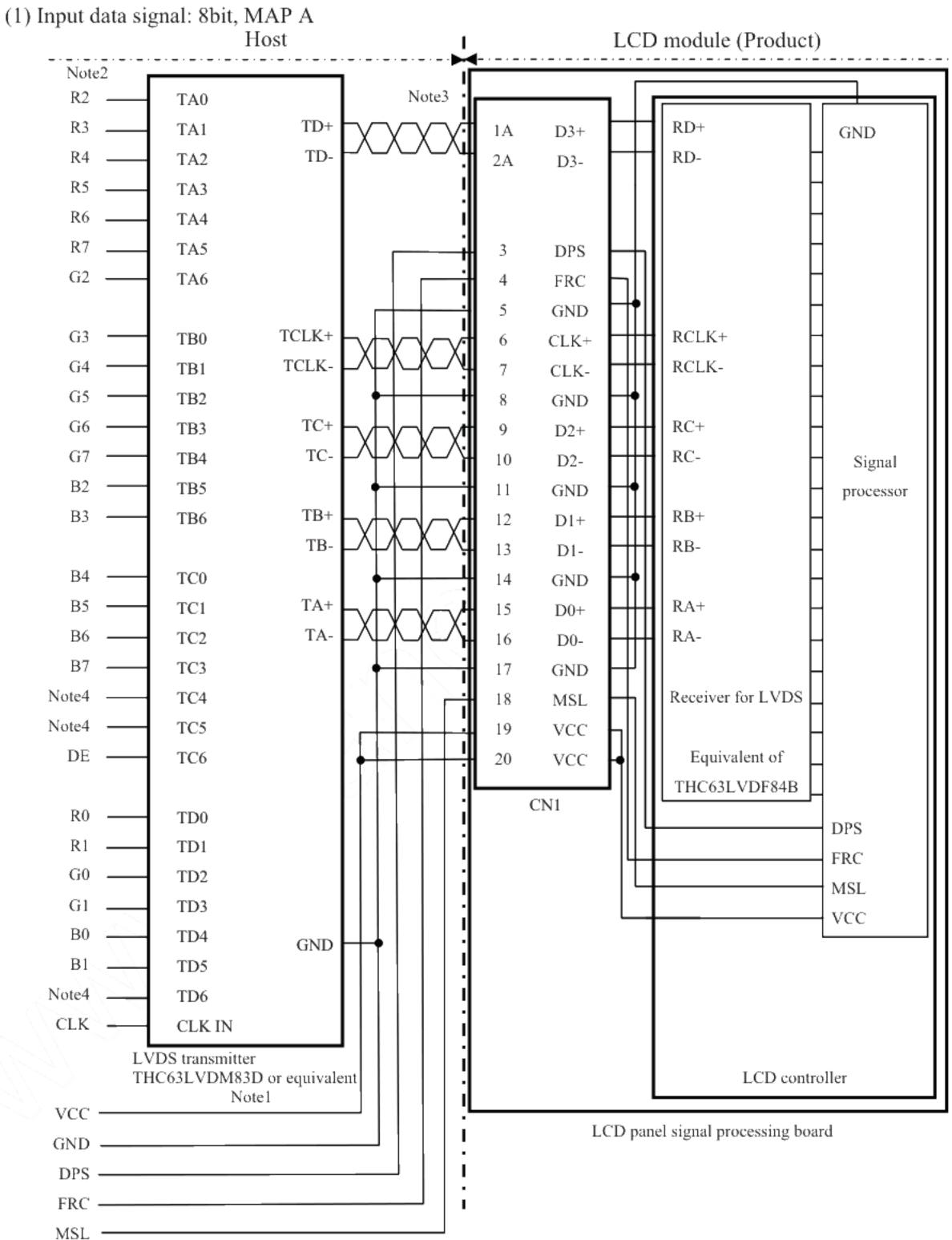
Adaptable socket: SHR-10V-S (J.S.T. Mfg. Co., Ltd.)

Pin No.	Symbol	Signal	Remarks
1	Al	Anode l	-
2	K1	Cathode l	-
3	A2	Anode2	- /^
4	K2	Cathode2	- <u>-</u>
5	A3.	Anode3	- < (\\
6	K3	Cathode3	
7	A4	Anode4	(/-\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
8	K4	Cathode4	2-5N - 77
9	A5	Anode5	
10	K5	Cathode5	\\\\\);-

4.5.3 Positions of plug and socket



4.5.4 Connection between receiver and transmitter for LVDS



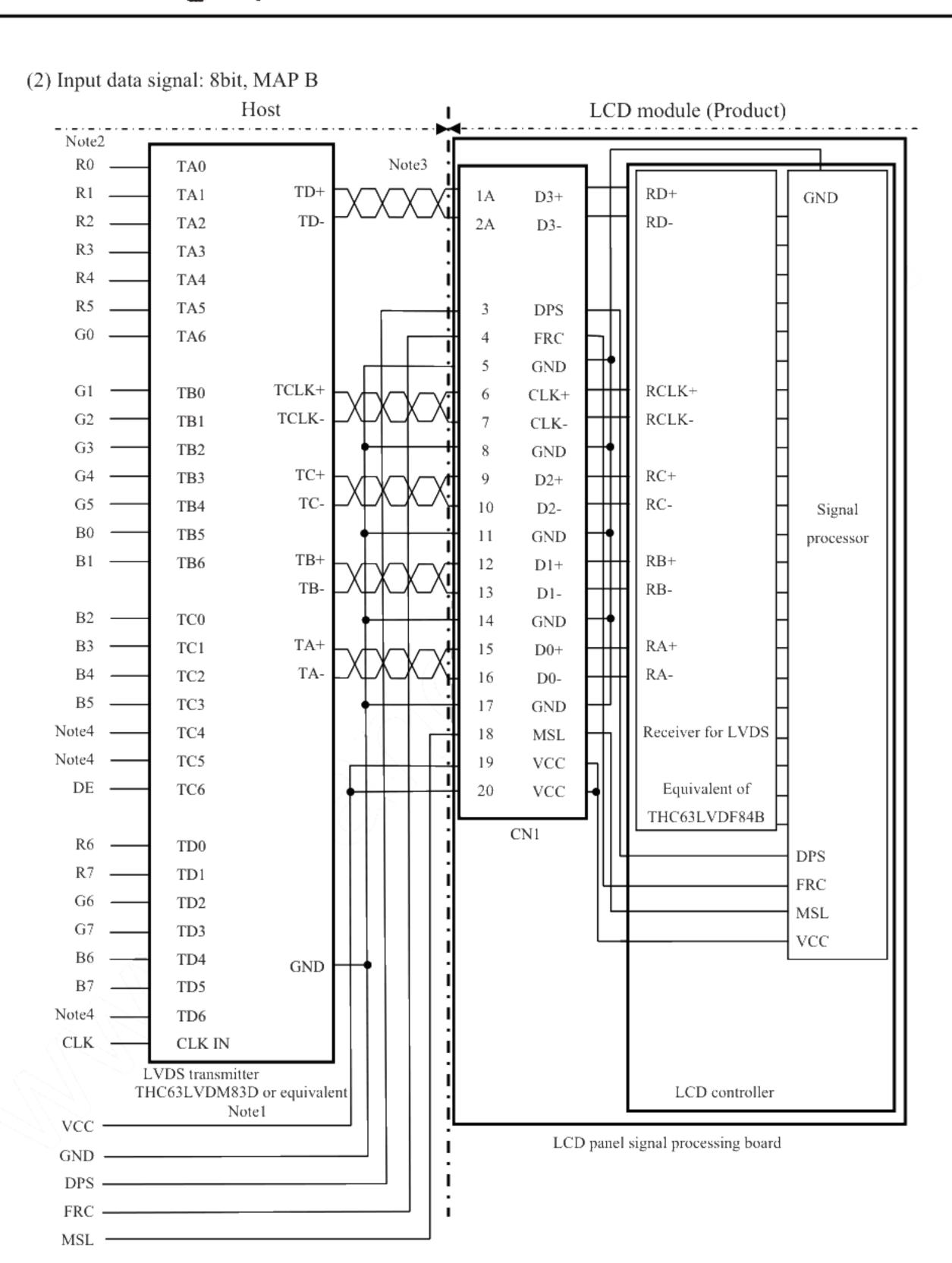
Note1: Recommended transmitter: THC63LVDM83D (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel

signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep TC4, TC5 and TD6 open to avoid noise problem.



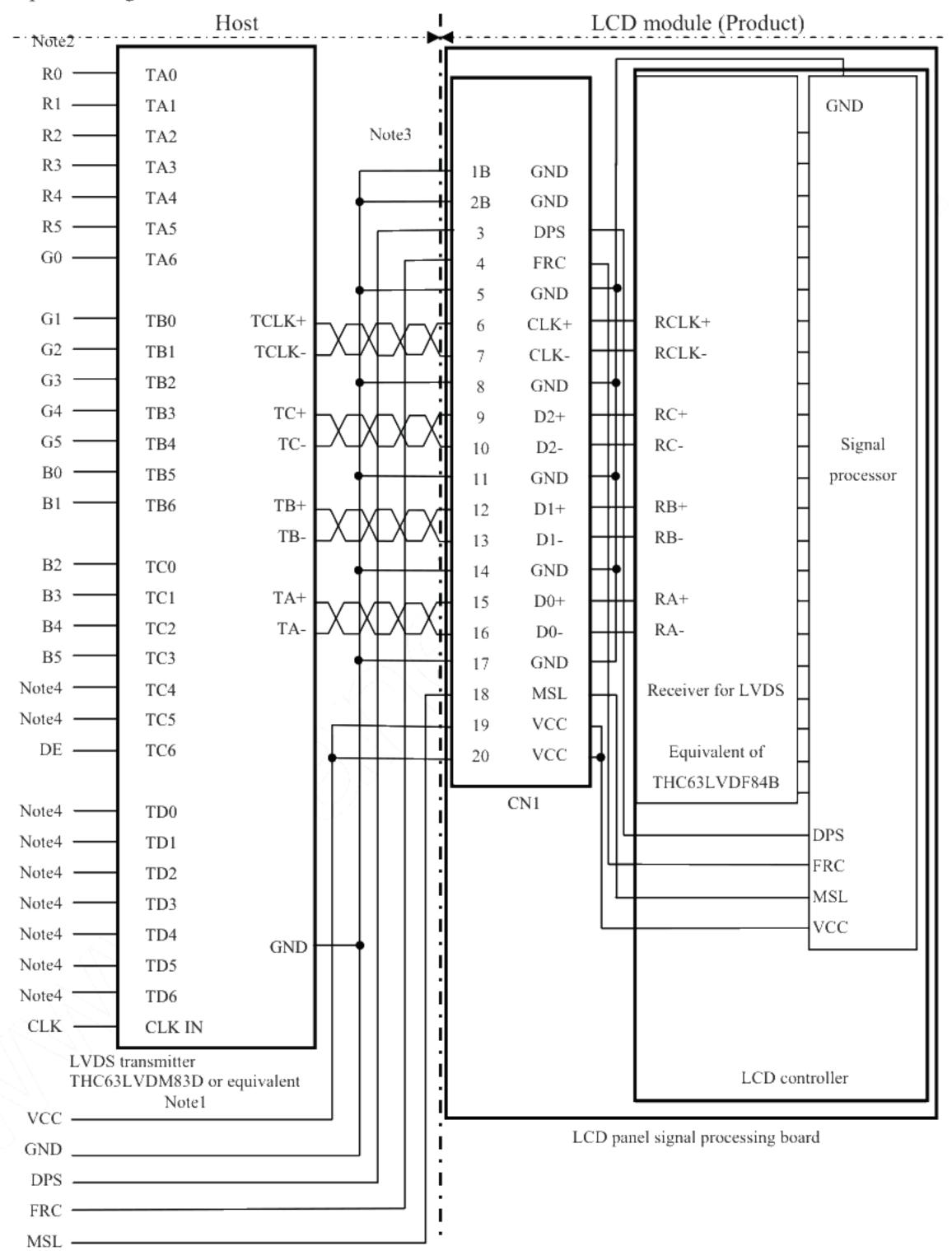
Note1: Recommended transmitter: THC63LVDM83D (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R7, G7, B7

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep TC4, TC5 and TD6 open to avoid noise problem.

(3) Input data signal: 6bit



Note1: Recommended transmitter THC63LVDM83D (THine Electronics Inc.) or equivalent

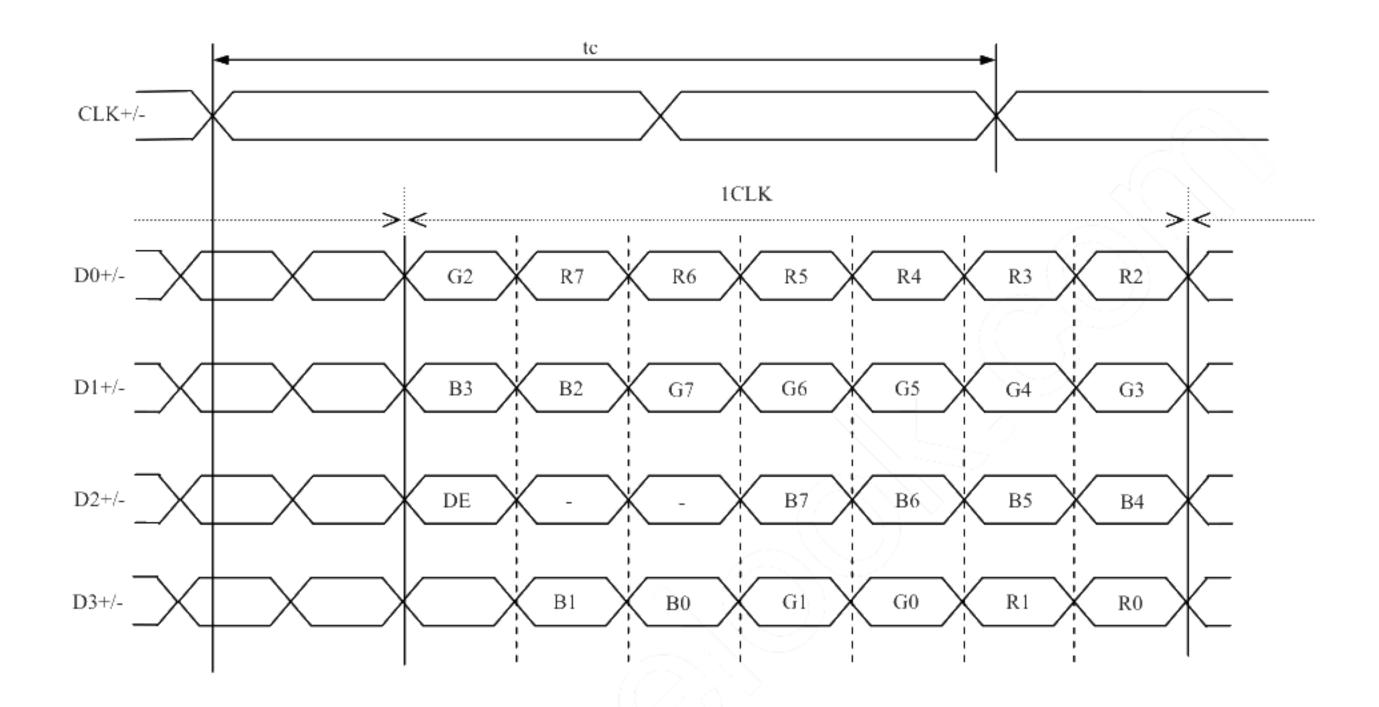
Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R5, G5, B5

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

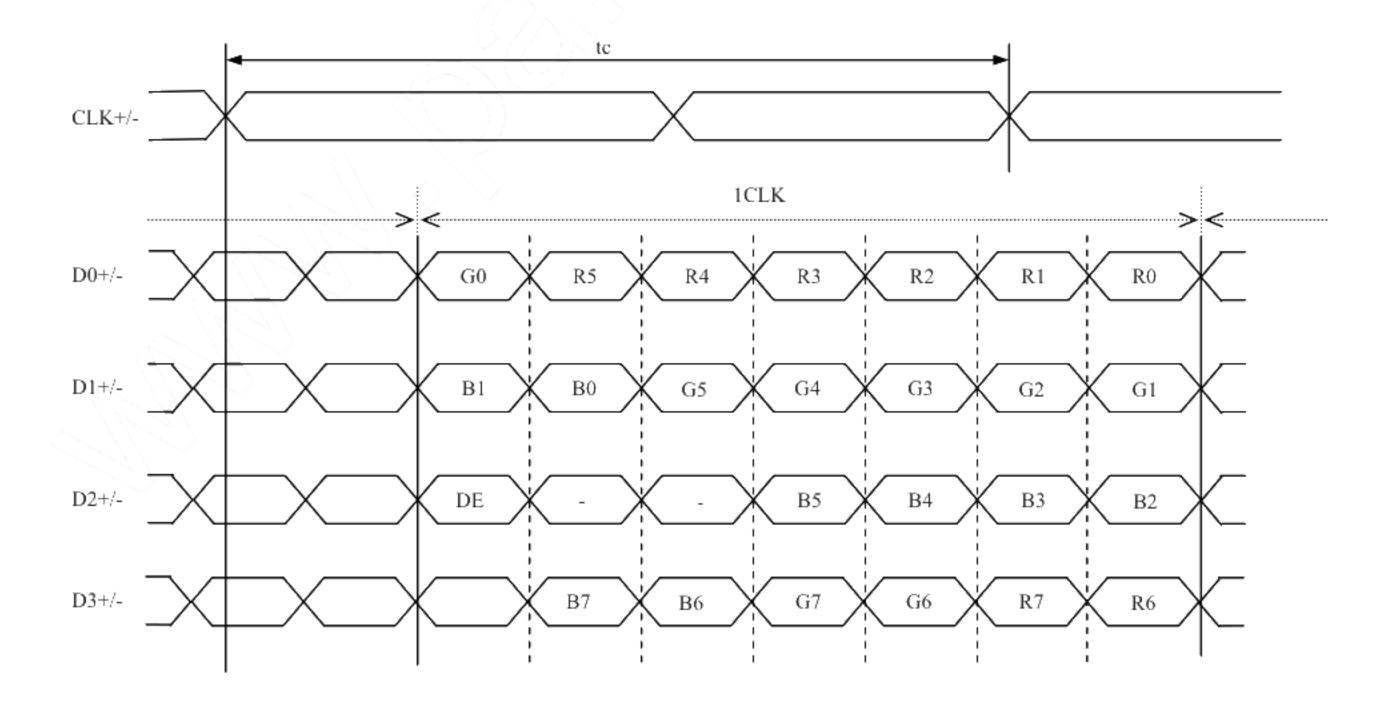
Note4: Input signals to TC4, TC5 and TD0-6 are not used inside the product, but do not keep TC4, TC5 and TD0-6 open to avoid noise problem.

4.5.5 Input data mapping

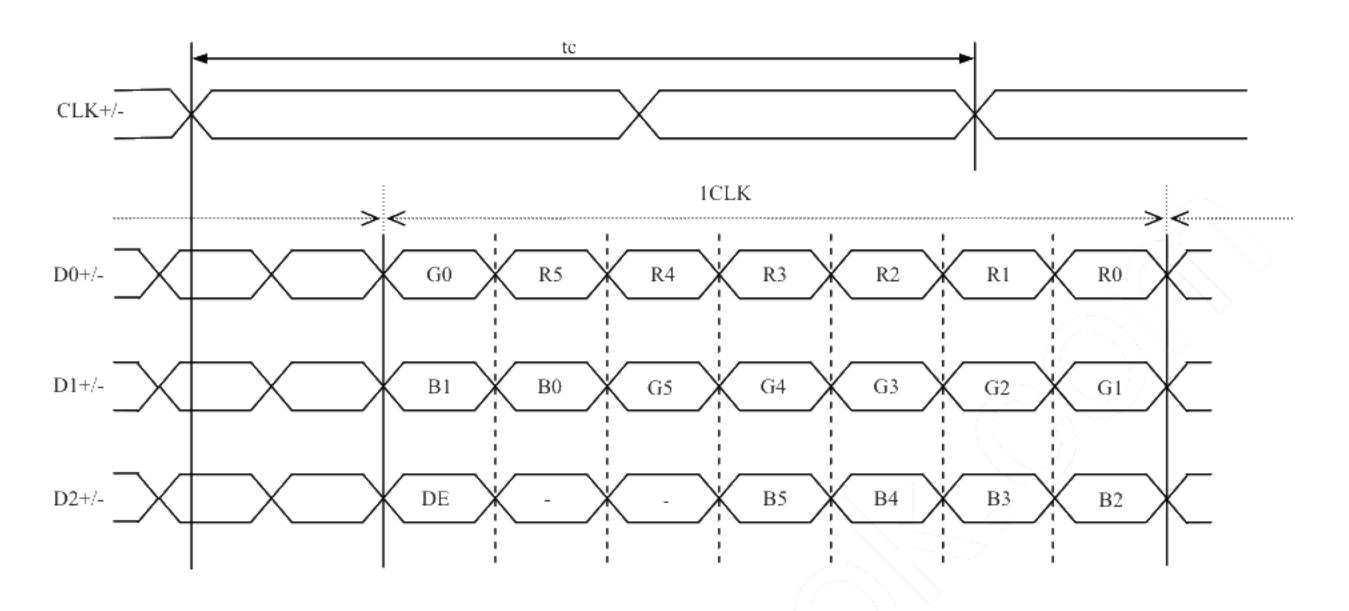
(1) Input data signal: 8bit, MAP A



(2) Input data signal: 8bit, MAP B



(3) Input data signal: 6bit



4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations of input data signals, FRC and MSL signal

This product can display 16,777,216 colors equivalent with 256 gray scales and 262,144 colors with 64 gray scales by combination of input data signals, FRC and MSL signal. See the following table.

Combination	Input data signals	Input Data mapping	CN1- Pin No.1 and 2	FRC terminal	MSL terminal	Display colors	Remarks
1	8 bit	MAP A	D3+/-	High	Low	16,777,216	Notel
2	8 bit	MAP B	D3+/-	High	High	16,777,216	Notel
3	6 bit	$\Diamond \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	GND	Low or Open	Low	262,144	Note2

Note1: See "4.6.2 16,777,216 colors". Note2: See "4.6.3 262,144 colors".

4.6.2 16,777,216 colors

This product can display 16,777,216 colors equivalent with 256 gray scales by combination ① or ②. (See "4.6.1 Combinations of input data signals, FRC and MSL signal".)

Also the relation between display colors and input data signals is as follows.

Display	colors								Data	a sig	nal	(0: I	Low	leve	el, 1	: Hiş	gh le	evel))						
Dispin		R7	R6	R5	R4	R3	R2	R1	R0	G7	7 G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	В4	В3	B2	В1	Β0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
l	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	4	1	1	1
lors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ြိ	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	I	1
Basic Colors	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-1	1	_1_	/1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ا _د		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	↑					:								:/-								:			
135	↓					:																:			
Rec	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	/ 0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	-0	0	0	-0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
SCS	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
ray	↑					:								:								:			
Green gray scale	↓					:								:								:			
Jre6	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
~		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u> </u>		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	1 1				> :	:								:								:			
5	1													:								:			
Blu	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	l	l	l	l	l	1

4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③. (See "4.6.1 Combinations of input data signals, FRC and MSL signal".) Also the relation between display colors and input data signals is as follows.

Display	colors						Data	a signa	al (0:	Low	level	, 1: F	ligh le	vel)					
Display	COIOIS	R5	R4	R 3	R 2	R 1	R 0	G5	G4	G3	G2	GΙ	G0	В5	В4	В3	В2	В1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1-	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0 <	0	0	0
loo	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	. 1	1
Basic colors	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
B ₃	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1,	- 1	_1_	//1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
o l		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	↑			:	:						:/\						:		
l gr	\downarrow			:	:								~				:		
Rec	bright	1	1	1	1	0	1	0	0	0	0_	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
\vdash	Red	1	1	1	1	1	1	0	0	0	/ 0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	-0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
SC.	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green gray scale	Î			:	/						:						:		
l s	↓			:		\					:						:		
Gre	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
~		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	T	. <		\bigcirc :							:						:		
9 e	1	\ 	0		_				0		:			١, ١					
Blu	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	I
	Dive	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	I	I	1	I	I

4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0,	0) B					
C(0, 0)	C(1, 0)		C(X, 0)		C(1022, 0)	C(1023, 0)
C(0, 1)	C(1, 1)	• • •	C(X, 1)		C(1022, 1)	C(1023, 1)
•	•	•	•	•	•	$\langle \cdot \rangle$.
	•		•		• (7	\ · · ·
•	•	•	•	•	• \ \)) •
C(0, Y)	C(1, Y)		C(X, Y)		C(1022, Y)	C(1023, Y)
•	•	•	•	•	((•))	•
	•		•	• • •	• //	•
•	•	•	•	<u> </u>	/s. •	•
C(0, 766)	C(1, 766)	• • •	C(X, 766)	• /• /•	C(1022, 766)	C(1023, 766)
C(0, 767)	C(1, 767)		C(X, 767)		C(1022, 767)	C(1023, 767)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view.

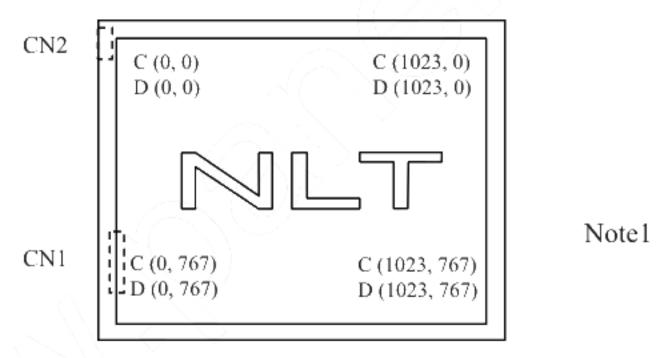


Figure 1. Normal scan (DPS: Low or Open)

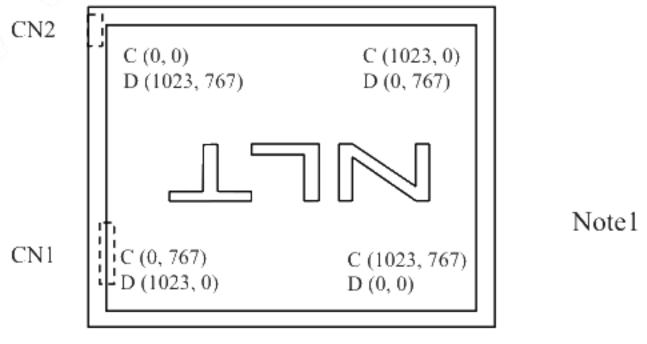


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

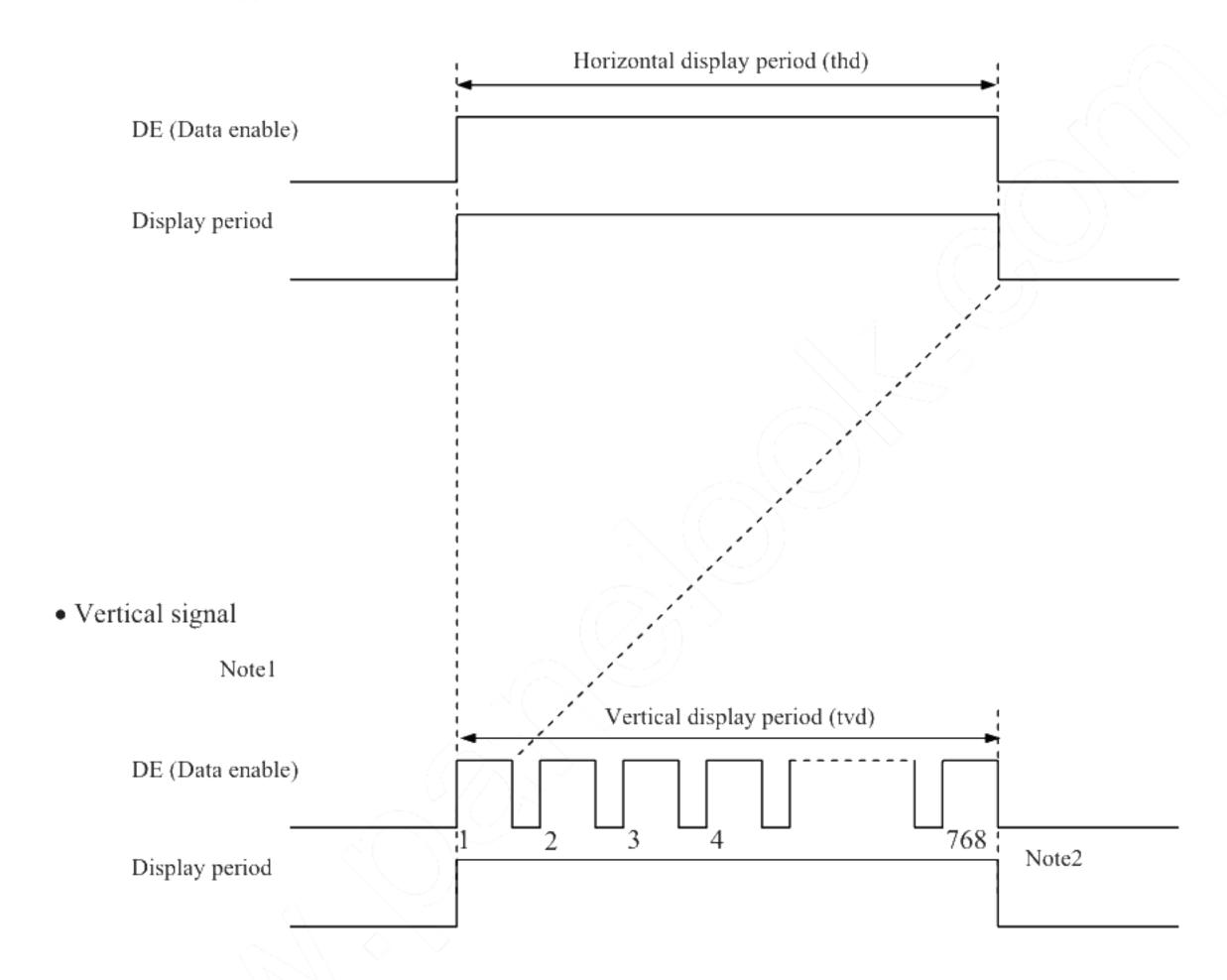
C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".)

D (X, Y): The data number of input signal for LCD panel signal processing board

4.9 INPUT SIGNAL TIMINGS

- 4.9.1 Outline of input signal timings
- Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing. Note2: See "4.9.3 Input signal timing chart" for the pulse number.



NL10276BC24-21F

4.9.2 Timing characteristics

(Note1, Note2, Note3)

	Parameter		Symbol	min.	typ.	max.	Unit	Remarks
	Fre	quency	1/tc	60.0	65.0	68.0	MHz	15.385ns (typ.)
CLK	I	Outy	-				-	
Rise tii		ne, Fall time	-				ns	_
	CLK-DATA	Setup time					ns	
DATA	CLK-DATA	Hold time	-		-		ns	
	Rise tim	Rise time, Fall time					ns	
		Cycle	th	19.67	20.676	22.4	μs	48.363kHz (typ.)
	Horizontal	Cycle	ui	-	1,344	-	CLK	
		Display period	thd	1,024			CLK)) -
	37	Cycle	tv	13.3	16.666	18.5 /	ms	60.0Hz (typ.)
DE	Vertical (One frame)	Cycle	t v	780	806	-	Н	
		Display period	tvd		768	4	Н	-
	CLK-DE	Setup time	-				ns	
	CLK-DE	Hold time	-			\7	ns	-
	Rise tim	ne, Fall time	-				ns	

Note1: Definition of parameters is as follows.

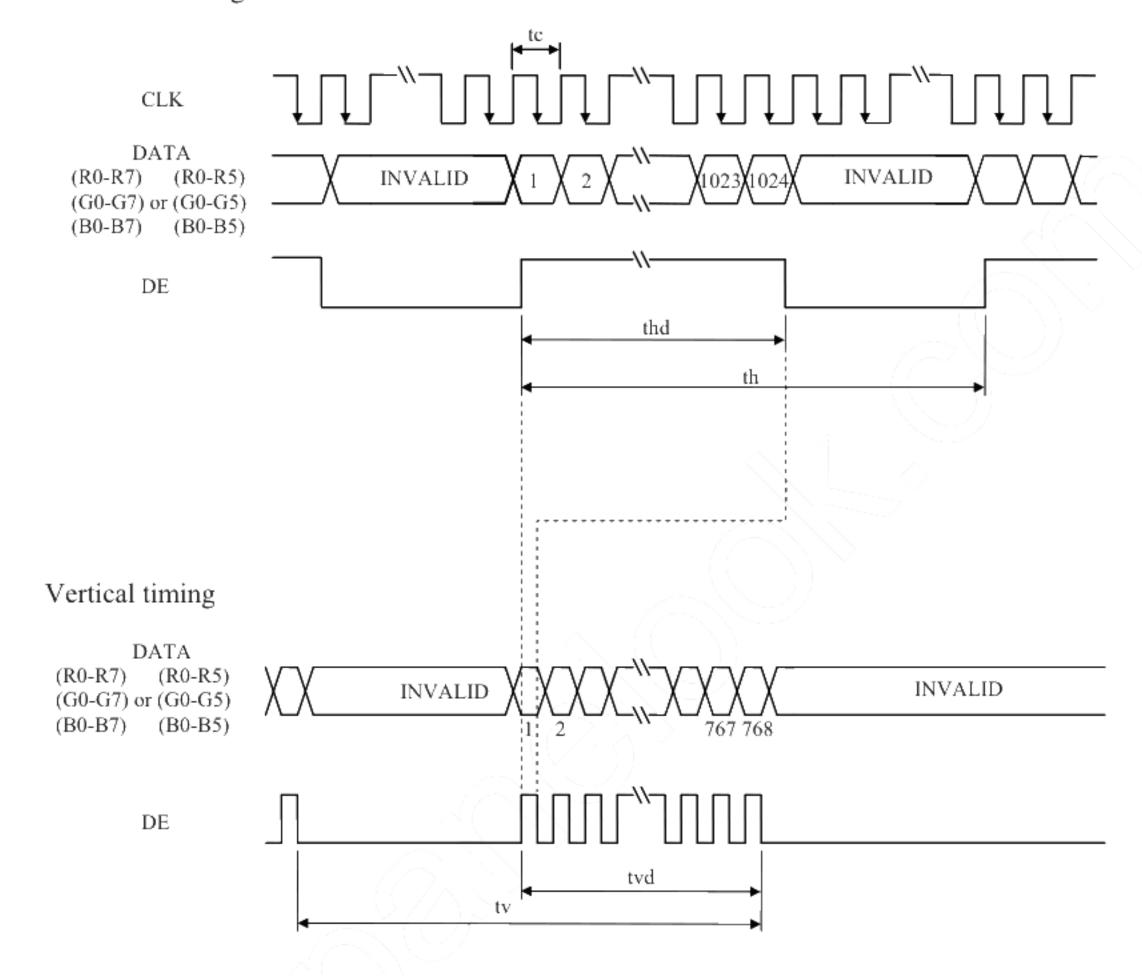
tc=1CLK, th=1H

Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

4.9.3 Input signal timing chart

Horizontal timing



24

NL10276BC24-21F

4.10 OPTICS

4.10.1 Optical characteristics

(Note1, Note2)

Paramet	er	Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminanc	e I	White at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	L	460	800	-	cd/m ²	BM-5A	Note3
Luminanc	e 2	White at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	L	575	1000		cd/m ²	BM-5A	Note4
Contrast ra	atio	White/Black at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	CR	540	900	-	,	BM-5A	Note5
Luminance uni	formity	White $\theta R = 0^{\circ}, \theta L = 0^{\circ}, \theta U = 0^{\circ}, \theta D = 0^{\circ}$	LU	-	1.25	1.4		BM-5A	Note6
	White	x coordinate	Wx	0.263	0.313	0.363	\mathbb{Z}		
	Willie	y coordinate	Wy	0.279	0.329	0.379	-3		
	Red	x coordinate	Rx	-	0.564	- \	_K -))		
Chromaticity	Red	y coordinate	Ry	-	0.346	-	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
Cinomaticity	Green	x coordinate	Gx	-	0.348	- /	-	SR-3	Note7
	Green	y coordinate	Gy	-	0.541		-	J 31X-3	Note
	Blue	x coordinate	Bx	- /	0.151		-		
	Dide	y coordinate	Ву	- (7	0.134	> -	-		
Color gan	nut	$\theta R=0^{\circ}, \theta L=0^{\circ}, \theta U=0^{\circ}, \theta D=0^{\circ}$ at center, against NTSC color space	C	35	40	-	%		
Darmonra t	ima	White to Black	Ton	/ /	3	5	ms	DM 5A	Note8
Response time		Black to White	Toff	N. - 27	15	21	ms	BM-5A	Note9
	Right	θU= 0°, θD= 0°, CR≥ 10	θR	70	80	-	o		
Viewing	Left	θU= 0°, θD= 0°, CR≥ 10	θL	≥ 70	80	-	0	EZ	Note
angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10^{\circ}$	θU	70	80	-	0	Contrast	10
	Down	θR= 0°, θL= 0°, CR≥ 10	θD	70	80	-	o		

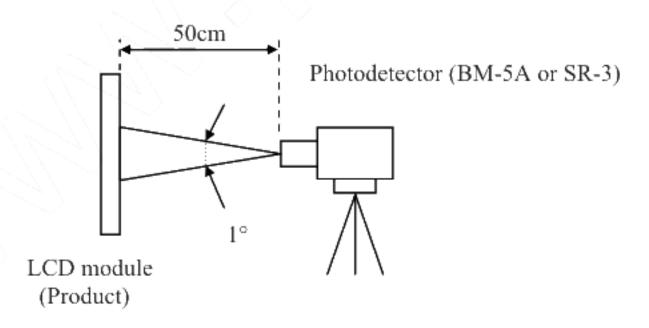
Note1: These are initial characteristics.

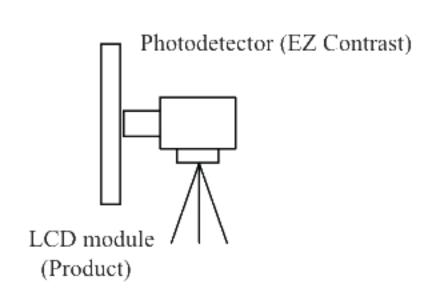
Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 50mA/One circuit, Display mode: XGA,

Horizontal cycle= 1/48.363kHz, Vertical cycle= 1/60.0Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation 20minutes after the product works in the dark room. Also measurement methods are as follows.





Note3: At IL= 50mA/One circuit Note4: At IL= 70mA/One circuit

Note5: See "4.10.2 Definition of contrast ratio".

Note6: See "4.10.3 Definition of luminance uniformity".

Note7: These coordinates are found on CIE 1931 chromaticity diagram.

Note8: Product surface temperature: TopF= 30 °C

Note9: See "4.10.4 Definition of response times".

Note10:See "4.10.5 Definition of viewing angles".

4.10.2 Definition of contrast ratio

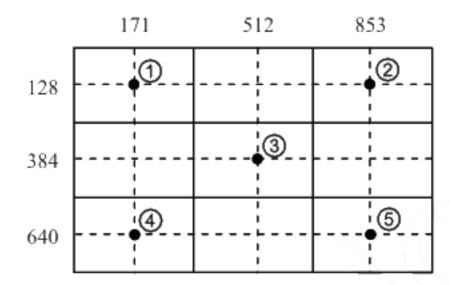
The contrast ratio is calculated by using the following formula.

4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

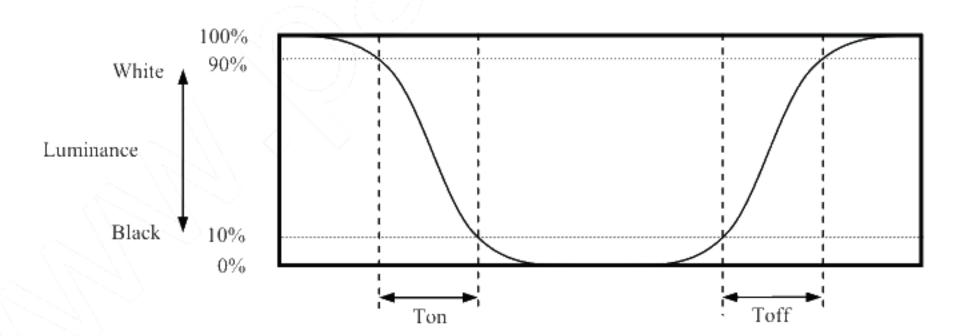
Luminance uniformity (LU) =
$$\frac{\text{Maximum luminance from } \textcircled{1} \text{ to } \textcircled{5}}{\text{Minimum luminance from } \textcircled{1} \text{ to } \textcircled{5}}$$

The luminance is measured at near the 5 points shown below.

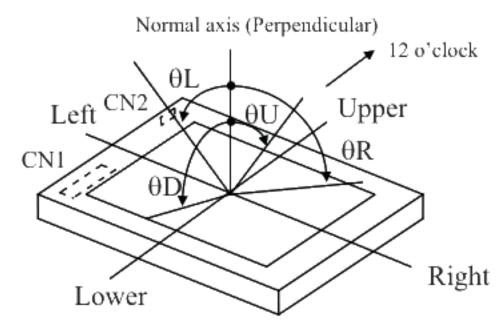


4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).



4.10.5 Definition of viewing angles



5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

1. Luminance 1: 800 cd/m² (typ.)

1. Lummanee 1. 600 ed/	(71)			
	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit		
LED alamentant substance	25°C (Ambient temperature of the product) Continuous operation, IL= 50mA/One circuit	70,000	l _b	
LED elementary substance	80°C (Surface temperature at screen) Continuous operation, IL= 50mA/One circuit	60,000	1 n	

2. Luminance 2: 1000 cd/m² (typ.)

	Condition						
LED elementary substance	25°C (Ambient temperature of the product) Continuous operation, IL= 70mA/One circuit	60,000	h				
LED elementary substance	60°C (Surface temperature at screen) Continuous operation, IL= 70mA/One circuit	-	11				

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for an LCD module but the value for LED elementary substance.

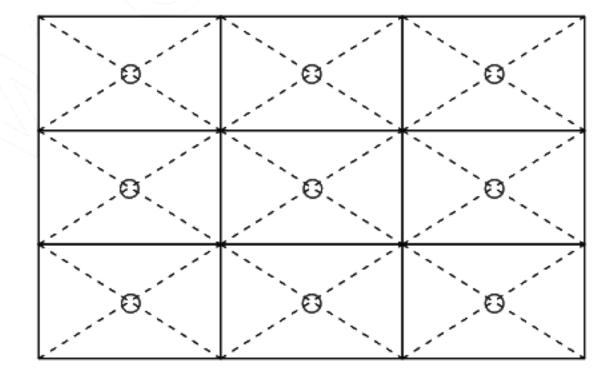
Note3: By ambient temperature, the lifetime changes particularly. Especially in case the product works under high temperature environment, the lifetime becomes short.

6. RELIABILITY TESTS

Test item	Condition	Judgment Note1		
High temperature and humidity (Operation)	① 60 ± 2°C, RH= 90%, 240hours ② Display data is black.			
High temperature (Operation)	 80 ± 3°C, 240hours Display data is black. 			
Heat cycle (Operation)	 30 ± 3°C1hour 80 ± 3°C1hour 50cycles, 4 hours/cycle Display data is black. 			
Thermal shock (Non operation)	 30 ± 3°C30minutes 80 ± 3°C30minutes 100cycles, 1hour/cycle Temperature transition time is within 5 minutes. 	No display malfunctions		
ESD (Operation)	 ① 150pF, 150Ω, ±10kV ② 9 places on a panel surface Note2 ③ 10 times each places at 1 sec interval 			
Dust (Operation)	 Sample dust: No. 15 (by JIS-Z8901) 15 seconds stir 8 times repeat at 1 hour interval 			
Vibration (Non operation)	 5 to 100Hz, 19.6m/s² 1 minute/cycle X, Y, Z directions 120 times each directions 	No display malfunctions		
Mechanical shock (Non operation)	 539m/ s², 11ms ±X, ±Y, ±Z directions 5 times each directions 	No physical damages		

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

7.2 CAUTIONS



* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s² and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (\$\phi16mm jig))

7.3 ATTENTIONS



7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- 3 When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ④ The torque for product mounting screws must never exceed 0.23N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be ≤ 2.0mm.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑤ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ② Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- ① Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.



NL10276BC24-21F

7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
- 3 Do not operate in high magnetic field. If not, circuit boards may be broken.
- This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

7.3.4 Others

- ① All VCC and GND terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to NLT for repairing and so on.
- ⑤ The information of China RoHS directive six hazardous substances or elements in this product is as follows.

	China RoHS directive six hazardous substances or elements										
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)						
×	0	0	0	0	0						

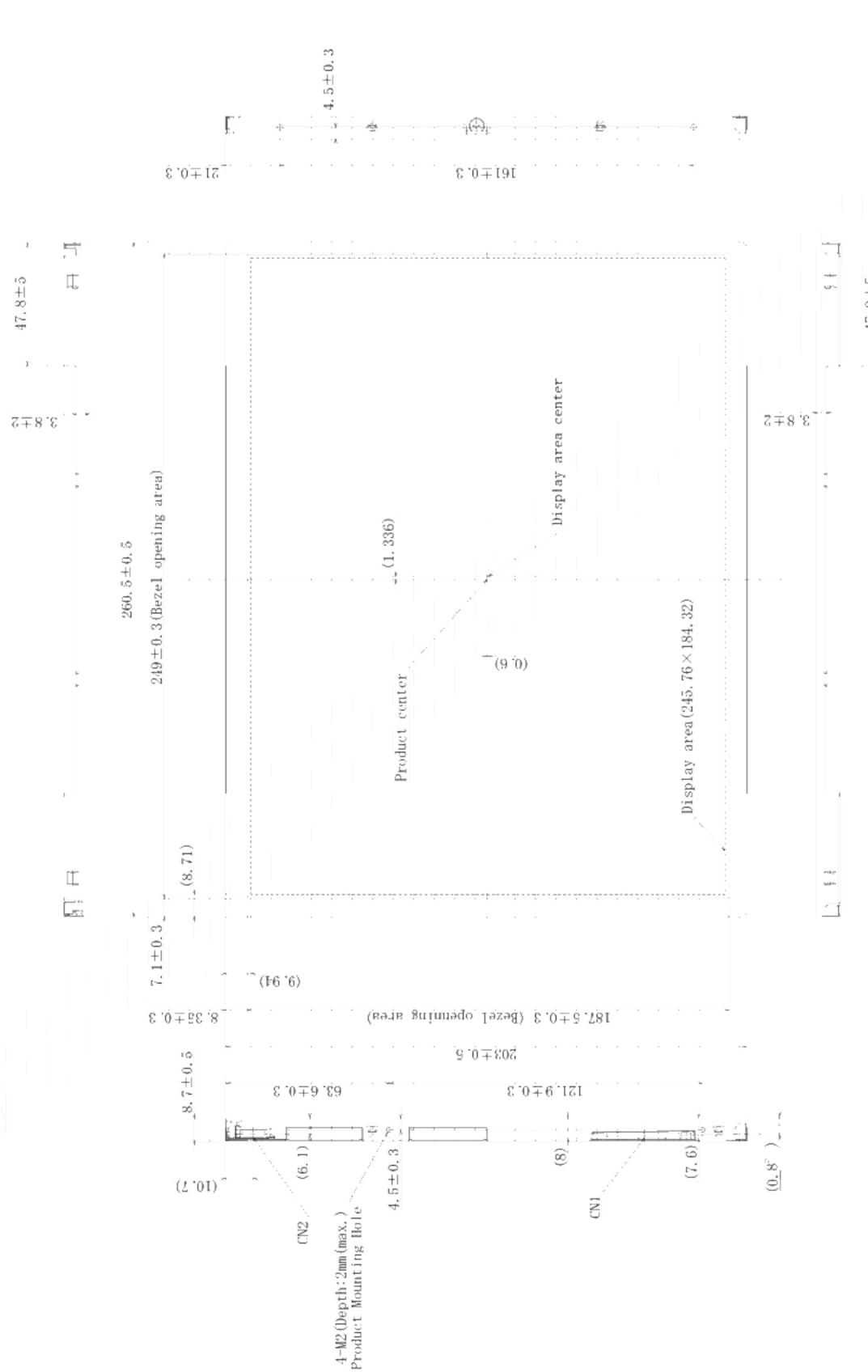
- Note1: ○: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of SJ/T11363-2006 standard regulation.
 - X: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of SJ/T11363-2006 standard regulation.

3

EC NEC LCD Technologies, Ltd.

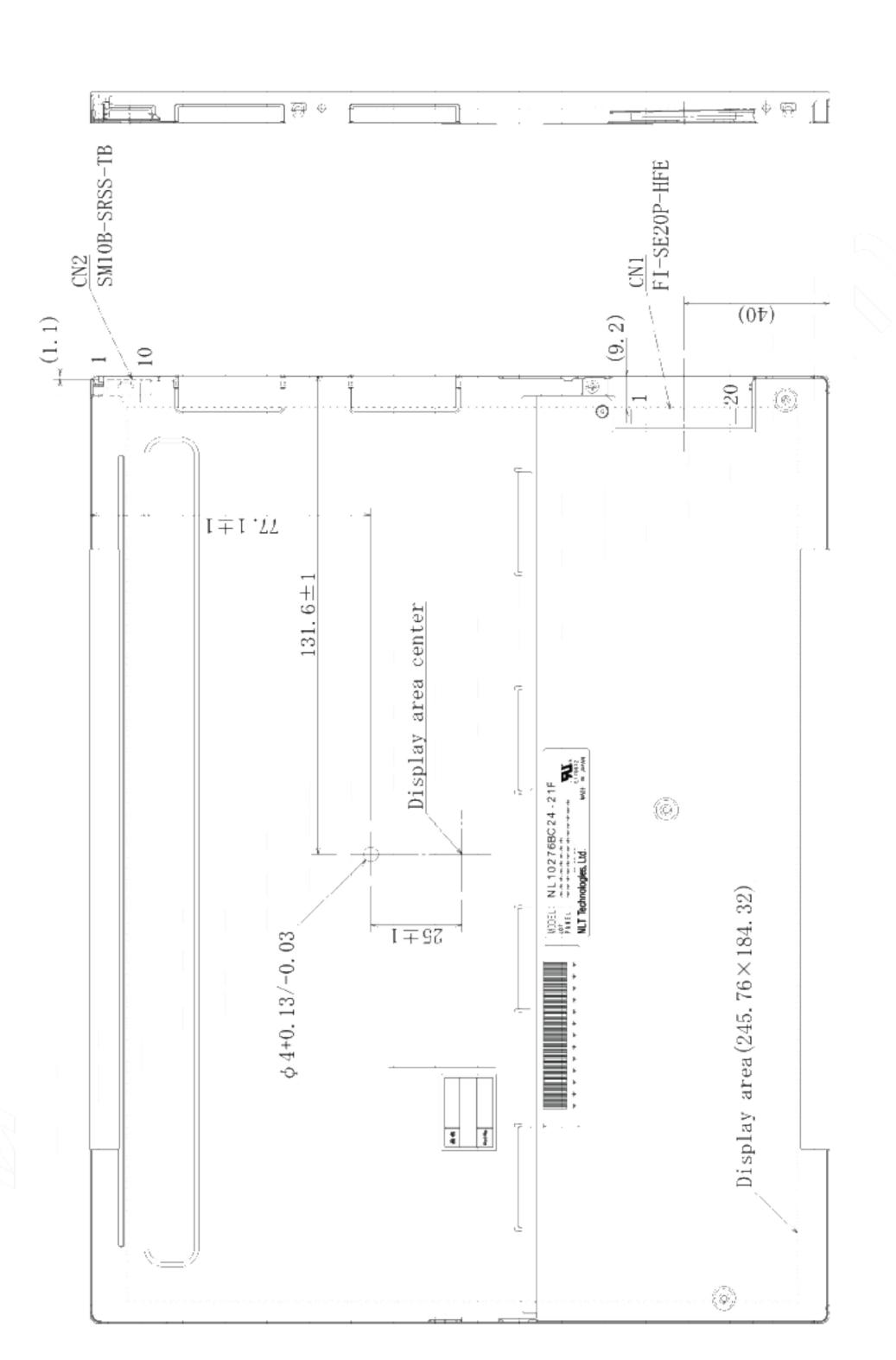
8. OUTLINE DRAWINGS

8.1 FRONT VIEW



Note1: The values in parentheses are for reference. Note2: The torque for product mounting screws must be ≤ 2.0 mm.

NEC LCD Technologies, Ltd.



Note1: The values in parentheses are for reference. Note2: The torque for product mounting screws must never exceed 0.23N⋅m. And the length of product mounting screws must be ≤ 2.0mm.

REVISION HISTORY

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document number	Prepared date	Re	evision contents and signat	ure
1st edition	DOD-PP- 1256	Aug. 2, 2011	Revision contents New issue. Signature of writer Approved by T. OGAWA	Checked by	Prepared by Ogama T. OGAWA
2ndt edition	DOD-PP- 1260	Aug. 9, 2011	 Power consumption P8 Absolute maximum ratings Table is revised. Backlight- Forward current Operating temperature P10 Backlight lamp Title (1. Luminance: 800 cd/m² (2. Luminance: 1000 cd/m² (3. Note2 (addition)) Signature of writer Approved by	d/m² (typ.)) (addition)	ng harness Prepared by
			T. OGAWA	Checked by	T. OGAWA