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Vincent Uh	EMERGING DISPLAY	ISSUE : SEP.02, 2009
ROVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 30
David Chang		VERSION: 6
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
Mo	DEL NO.:	
MO	DEL NO.:	
	ET0350G0DH6 (RoHS)	
FOR	MESSRS:	
		_)
CUSTOMER'S APPROV.	AL	
DATE :		
BY:		

MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION 0 - 1ET0350G0DH6 6 DOC . FIRST ISSUE JAN.06, 2009 RECORDS OF REVISION REVISED DATE **PAGE** SUMMARY NO. JAN.21, 2009 7. OUTLINE DIMENSIONS 9 MARK \triangle : MODIFY COMPONENT AREA & CONNECTOR DIMENSION 5.4 PARALLEL RGB INTERFACE (DE MODE) FEB.16, 2009 6 300 ddk 31-90 ddk 320 ddk 31-90 ddk 1 2 3 318 319 320 DATA 1 2 3 318 319 320 14 ADD 11.2 THE BRIGHTNESS CONTROLLED BY BACKLIGHT CURRENT OF LEDCTRL. MAR.09, 2009 3 4. ELECTRICAL CHARACTERISTICS POWER SUPPLY CURRENT FOR LED DRIVER: $TYP=(30) \rightarrow (220), MAX.=(50) \rightarrow (290)$ 9 7. OUTLINE DIMENSIONS MARK \triangle : CHANGE CONNECTOR & MODIFY DIMENSION 8. BLOCK DIMENSIONS 10 ADD FRAME GROUND APR.07, 2009 9 7. OUTLINE DIMENSIONS MARK A: MODIFY CN1 TYPE 4. ELECTRICAL CHARACTERISTICS SEP.02,2009 3 PARAMETER SYMBOL CONDITION MIN. TYP. POWER SUPPLY CURRENT FOR LED DRIVER ICC LED BIL—ON (220) PARAMETER SYMBOL CONDITION MIN. TYP. MAX. POWER SUPPLY CURRENT FOR LED DRIVER ICC VCC-VSS-33V LED B/L-ON — 220 290

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12.	TOUCH PANEL SPECIFICATION	$15 \sim 20$
13.	INSPECTION CRITERION	21 ~ 30

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1. GENERAL SPECIFICATIONS			
1.1 APPLICATION NOTES FOR PLEASE REFER TO:	CONTROLLER/DRIVER		
Н	X 8 2 3 8 - A		
INCLUDING PROHIBITED M	LY WITH EUROPEAN ROHS R IATERIALS/COMPONENTS CO IAVALENT CHROMIUM, POLY IYBROMINATED	NTAINING	LEAD
2. MECHANICAL SPECIFICATION	NS		
(1) DIAGONALS	3.5 inch		
(2) NUMBER OF DOTS	320W * (RGB) * 240	OH DOTS	
(3) MODULE SIZE	76.8W * 63.8H * 8.6	D(MAX) mm	
	(WITHOUT FPC)		
(4) EFFECTIVE AREA	71.9W * 54.46H mn	n(T/P)	
(5) ACTIVE AREA	70.08W * 52.56H m	m (LCD)	
	70.8W * 53.26H mm		
	0.073W * 0.219H m		
(7) PIXEL SIZE			
(8) LCD TYPE	ŕ	VE	
(9) COLOR (10) VIEWING DIRECTION	262K		
(11) BACK LIGHT		ITE	
(12) INTERFACE MODE			C MOD
(12) INTERCACE MODE	ROD TODIT I ARAL		C 1410D

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	VSS-0.3	4.0	V	_
STATIC ELECTRICITY	_			V	NOTE (1)
LED BACKLIGHT POWER DISSIPATION	PD	_	540	mW	_
LED BACKLIGHT FORWARD CURRENT	IF	_	25	mA	_
LED BACKLIGHT REVERSE VOLTAGE	VR	_	30	V	_

NOTE (1): LCD SHOULD BE GROUNDED DURING HANDING LCM.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPER.	ATING	STOF	RAGE	REMARK		
I I E WI	MIN.	MAX.	MIN.	MAX.	KEMAKK		
AMBIENT TEMPERATURE	-10°C	60°C	-20°C	70°C	NOTE (1), (2)		
HUMIDITY	NOTE (3)		NOTE (3)		NOTE (3)		WITHOUT CONDENSATION
VIBRATION		2.45m/s ² (0.25G)	_	11.76m/s ² (1.2G)	5~20Hz , 1HR 20~500Hz(20Hz) , 1HR 20~500Hz(500Hz) , 1HR X,Y,Z,TOTAL 3HRS		
SHOCK	_	29.4m/s ² (3G)	_	490m/s ² (50G)	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH		
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACCEPTABLE				

NOTE (1): Ta AT -20°C: 48HRS MAX.

70°C:168HRS MAX.

NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT

TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3): $Ta \le 60^{\circ}C : 90\%RH MAX (96HRS MAX)$.

 $Ta \! > \! 60^{\circ}\text{C}$: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY

OF 90%RH AT 60°C(96HRS MAX).

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4. ELECTRICAL CHARACTERISTICS

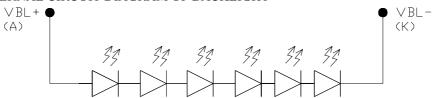
 $Ta = 25 \, ^{\circ}C$

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS		2.5	3.3	3.6	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VCC-VSS		2.7	3.3	3.6	V	
LOGIC HIGH INPUT VOLTAGE	VIH	H LEVEL	0.8*VDD		VDD	V	NOTE (1)
LOGIC LOW INPUT VOLTAGE	VIL	L LEVEL	0	_	0.2*VDD	V	NOTE (1)
POWER SUPPLY CURRENT	IDD	VDD-VSS=3.3V		10	15	mA	NOTE (2)
POWER SUPPLY CURRENT FOR LED DRIVER	ICC	VCC-VSS=3.3V LED B/L=ON		220	290	mA	
POWER SUPPLY FOR LED BACKLIGHT	$V_{BL^+} - V_{BL^-}$	IF=20mA	18	19.8	21.6	V	NOTE (3)
LED LIFE TIME		_	30K	40K		HRS	

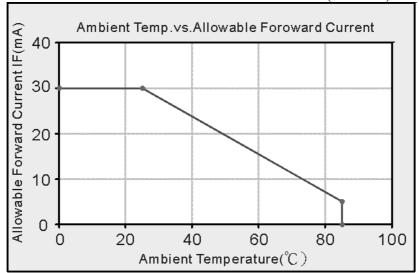
NOTE (1): APPLIED TO TERMINALS /RESET, B5~B0, G5~G0, R5~R0, DCLK, HSYNC, VSYNC, ENB.

NOTE (2): THE DISPLAY PATTERN IS ALL "WHITE".

NOTE (3): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (4): AMBIENT TEMP .VS. ALLOWABLE FORWARD CURRENT. (PER LED)



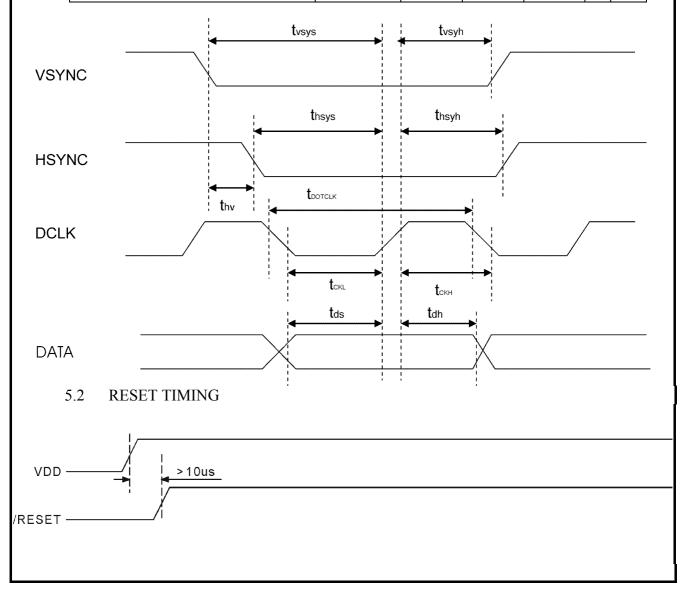
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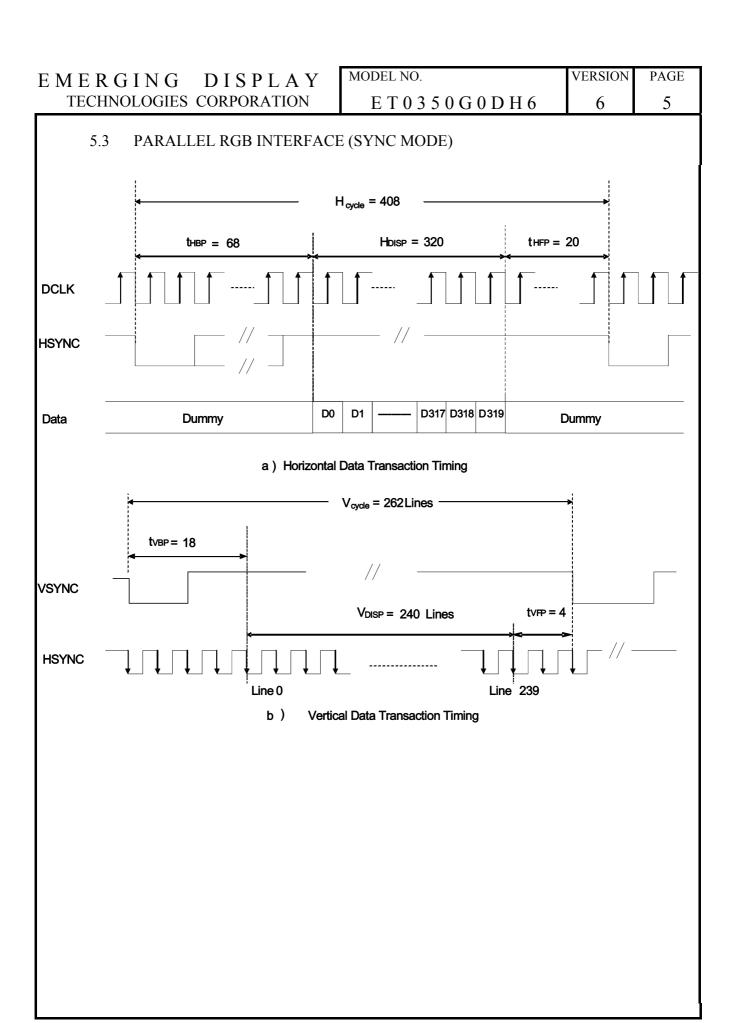
5. TIMING CHARACTERISTICS

5.1 PIXEL TIMING

Ta=25 °C

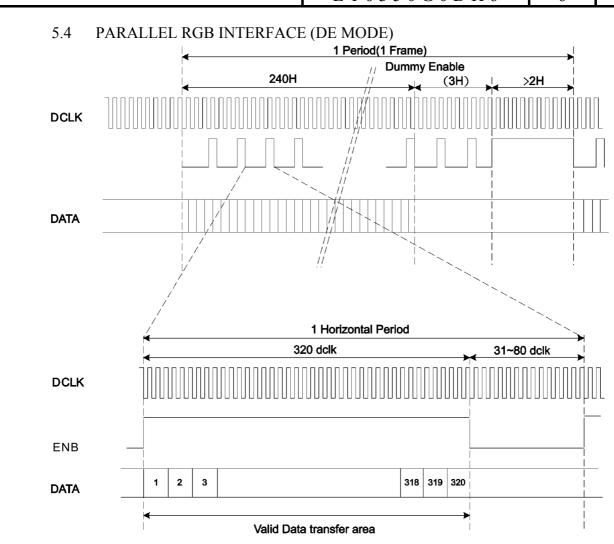
CHARACTERISTICS	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	fDCLK		6.5	10	MHz
DCLK PERIOD	tDCLK	100	154		ns
VERTICAL SYNC SETUP TIME	tvsys	20			ns
VERTICAL SYNC HOLD TIME	tvsyh	20			ns
HORIZONTAL SYNC SETUP TIME	thsys	20			ns
HORIZONTAL SYNC HOLD TIME	tvsyh	20			ns
PHASE DIFFERENCE OF SYNC SIGNAL FALLING EDGE	thv	1	_	240	tDCLK
DOTCLK LOW PERIOD	tCKL	50			ns
DOTCLK HIGH PERIOD	tCKH	50	_		ns
DATA SETUP TIME	tds	12	_		ns
DATA HOLD TIME	tdh	12			ns
RESET PULSE WIDTH	tRES	10	_	_	μs





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CHARACTERISTICS		SYMBOL	MIN.	TYP.	MAX.	
DCLK FREQUENCY		fDCLK		6.5	10	MHz
DCLK PERIOD		tDCLK	100	154	_	ns
HORIZONTAL FREQUENCY (L	INE)	fH	_	15.72	22.35	KHz
VERTICAL FREQUENCY (REF	RESH)	fV	_	60	90	Hz
HORIZONTAL BACK PORCH		tHBP		68	_	tDCLK
HORIZONTAL FRONT PORCH		tHFP	_	20	_	tDCLK
HORIZONTAL DATA START PO	DINT	tHBP	_	68	_	tDCLK
HORIZONTAL BLANKING PERIOD		tHBP + tHFP	_	88	_	tDCLK
HORIZONTAL DISPLAY AREA		HDISP	_	320	_	tDCLK
HORIZONTAL CYCLE		Hcycle	_	408	450	tDCLK
VERTICAL BACK PORCH		tVBP	_	18	_	Lines
VERTICAL FRONT PORCH		tVFP	_	4	_	Lines
VERTICAL DATA START POIN	T	tVBP	_	18	_	Lines
VERTICAL BLANKING PERIOR)	tVBP + tVFP	_	22	_	Lines
	NTSC		_	240	_	
VERTICAL DISPLAY AREA	PAL	VDISP	_	280(PALM=0)	_	Lines
	ГAL			280(PALM=1)	_	
VERTICAL CYCLE	NTSC	Vcycle		262	350	Lines
VERTICAL CICLE	PAL	Veyele		313	330	Lines

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6. OPTICAL CHARACTERISTICS (NOTE1)

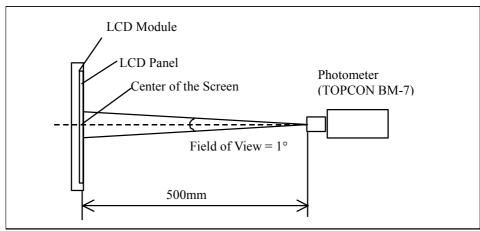
6.1 OPTICAL CHARACTERISTICS

 $Ta = 2.5 \pm 2$ °C

ITE		CVAIDOI	COMP	ITION	MINI	TVD	N / A 37	LIMIT	DEMARK
I T E M		SYMBOL	COND	HION	MIN.	TYP.	MAX.	UNIT	REMARK
			CR ≥ 10	$\theta_x=0$ °	50	55		deg.	NOTE(2)
VIEWING ANGLE		$\theta_{ ext{y-}}$			70	75			
VIEWING ANGL.	Ŀ	θ_{x^+}	CR ≥ 10		70	75		ueg.	NOTE(3)
		θ_{x}		$\theta_y=0^{\circ}$	70	75			
CONTRAST RAT	IO	CR	θx=0°,	θy=0°	350	450	_		NOTE (3)
RESPONSE TIME	1	$T_R(rise)$	θx=0°,	000		15	20	msec	NOTE (4)
KESFONSE IIIVIE		$T_F(fall)$	θx-0 ,	б у–0		35	50		
	WHITE	Wx			0.260	0.310	0.360		NOTE(5)
		Wy			0.260	0.310	0.360		
	RED	Rx	θx=0°, θy=0° IF=20mA NTSC: 60%		0.562	0.612	0.662		
COLOR OF CIE		Ry		•	0.305	0.355	0.405		
COORDINATE	GREEN BLUE	Gx		NTSC: 60%	0.262	0.312	0.362		
		Gy			0.533	0.583	0.633		
		Bx			0.090	0.140	0.190		
		By			0.020	0.070	0.120		
THE BRIGHTNESS OF MODULE		В	θx=0°, IF=2	θy=0° 0mA	300	350		cd/m ²	NOTE (6)
THE UNIFORMITY OF MODULE		_	θx=0°, IF=2		70			%	NOTE(6)

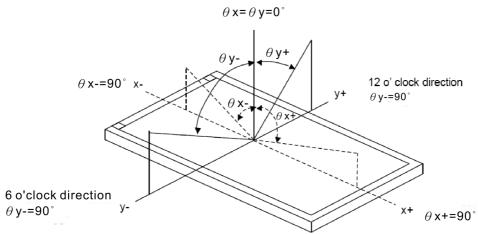
NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



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NOTE (2): DEFINITION OF VIEWING ANGLE:

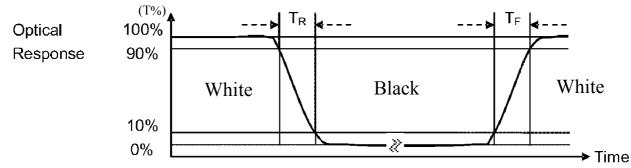


Normal

NOTE (3): DEFINITION OF CONTRAST RATIO:

 $\label{eq:contrast_ratio} \text{CONTRAST} \quad \text{RATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$

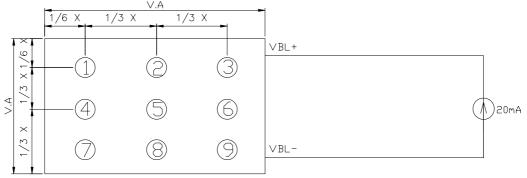
NOTE (4): DEFINITION OF RESPONSE TIME: TR AND TF THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5): THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

NOTE (6): BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY



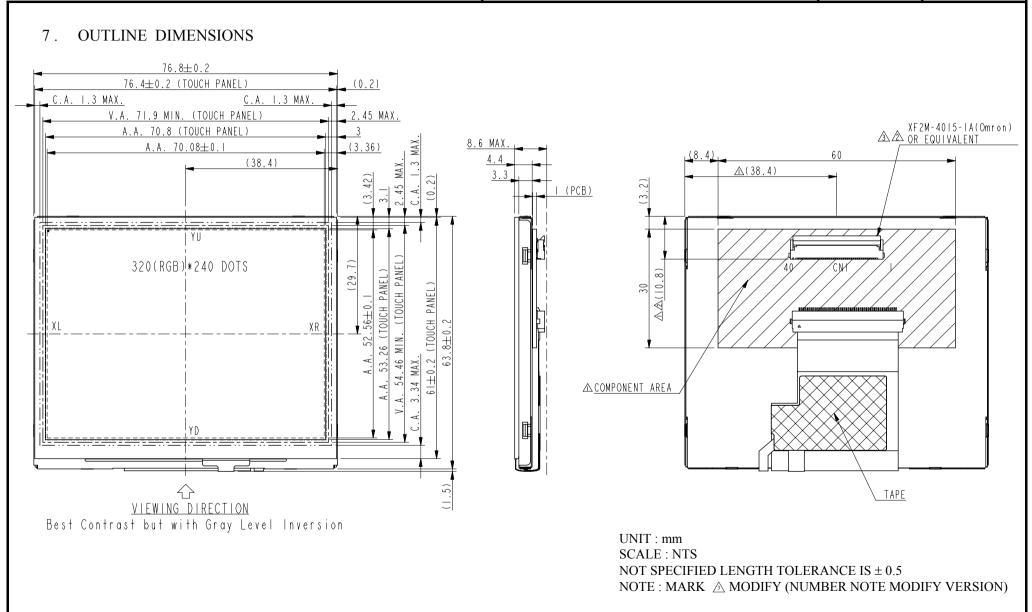
UNIT: mm

6.3 THE CALCULATING METHOD OF UNIFORMITY

 $UNIFORMITY: \\ \\ \left[1 - \frac{MAXIMUM \\ BRIGHTNESS - MINIMUM \\ BRIGHTNESS \\ \\ AVERAGE \\ BRIGHTNESS \\ \\ \right] \times 100\%$

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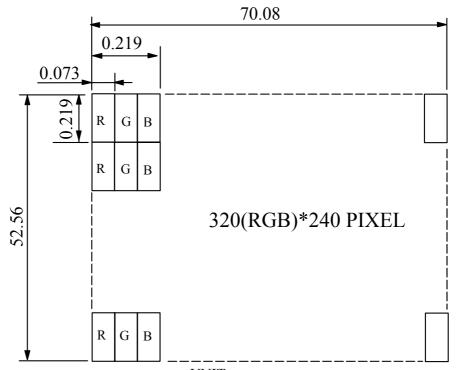


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9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm SCALE : NTS

NOT SPECIFIED TOLERANCE IS \pm 0.1 DOTS MATRIX TOLERANCE IS \pm 0.01

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10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION		
1	/RESET	I	HARDWARE RESET		
2	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)		
3	B5	I	BLUE DATA BIT 5		
4	B4	I	BLUE DATA BIT 4		
5	В3	I	BLUE DATA BIT 3		
6	B2	I	BLUE DATA BIT 2		
7	B1	I	BLUE DATA BIT 1		
8	В0	I	BLUE DATA BIT 0		
9	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)		
10	G5	I	GREEN DATA BIT 5		
11	G4	I	GREEN DATA BIT 4		
12	G3	I	GREEN DATA BIT 3		
13	G2	I	GREEN DATA BIT 2		
14	G1	I	GREEN DATA BIT 1		
15	G0	I	GREEN DATA BIT 0		
16	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)		
17	R5	I	RED DATA BIT 5		
18	R4	I	RED DATA BIT 4		
19	R3	I	RED DATA BIT 3		
20	R2	I	RED DATA BIT 2		
21	R1	I	RED DATA BIT 1		
22	R0	I	RED DATA BIT 0		
23	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)		
24	DCLK	I	DOT DATA CLOCK		
25	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)		
26	HSYNC	I	HORIZONTAL SYNC INPUT	יוק	
27	VSYNC	I	VERTICAL SYNC INPUT DE MODE:HSYNC , VSYNC FLOAT SYNC MODE:ENB FLOATING	111	
28	ENB	I	DATA ENABLE INPUT		
29	PWCTRL	I	PWCTRL REMARK LOGIC LEVEL H=3.3V L=0V L POWER ON SHUTDOWN		
			WHEN INTERNAL LED DRIVER : JP4 1-2(DEFAULT) WHEN EXTERNAL LED DRIVER : JP4 2-3		

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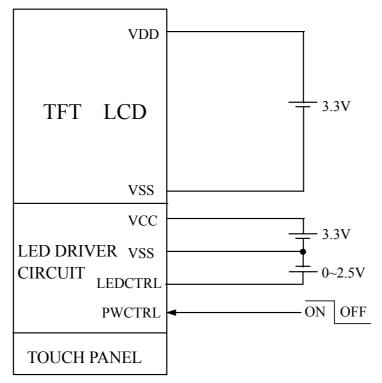
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PIN NO	SYMBOL	I/O		FUNCTION	
30	VDD	P	POWER SUPPLY VOLTAGE		
31	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)		
32	VSS	P	GROUND (VSS IS CONNECT CONDUCTIVE TAPE)	TED TO METAL HOUSING WITH	
33	VCC	P	POWER SUPPLY FOR LED I	DRIVER CIRCUIT	
34	VCC	P	POWER SUPPLY FOR LED I	ORIVER CIRCUIT	
35	NC	_	NON CONNECTION (USING (USING EXTERNAL LED DRI WHEN INTERNAL LED DRI WHEN EXTERNAL LED DRI	VER: JP1 1-2 (DEFAULT)	
36	LEDCTRL	Ι	BRIGHTNESS CONTROL FO LEDCTRL (USING INTERNA EXTERNAL LED DRIVER) WHEN INTERNAL LED DRI WHEN EXTERNAL LED DRI	AL LED DRIVER) OR CATHODE (USING VER: JP2 1-2 (DEFAULT) JP3 1-2 CONNECT (DEFAULT)	
37	YU	_	TOP PANEL		
38	XL	_	LEFT PANEL	TOLICH BANEL	
39	YD	_	BOTTOM PANEL	TOUCH PANEL	
40	XR		RIGHT PANEL		

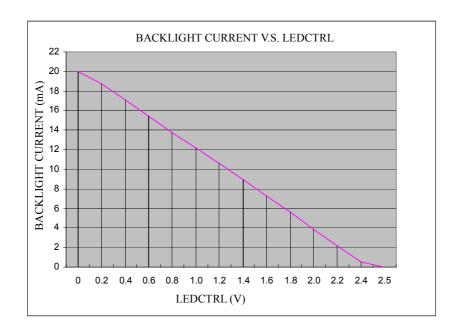
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11. POWER SUPPLY

1 1 .1 POWER SUPPLY FOR LCM



11.2 THE BRIGHTNESS CONTROLLED BY BACKLIGHT CURRENT OF LEDCTRL.



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12. TOUCH PANEL SPECIFICATION

12.1 ELECTRICAL CHARACTERISTICS

 $Ta = 25^{\circ}C$

ITEM	CONDITION	SPEC.	UNIT
LINEARITY		1.5	%
TERMINAL RESISTANCE	X AXIS	200 ~ 900	0
TERMINAL RESISTANCE	Y AXIS	200 ~ 900	\$2
INSULATION RESISTANCE	DC25V	20	$M\Omega$
INPUT VOLTAGE	_	5(TYP.)	V

12.2 PRECAUTIONS IN USE OF TOUCH PANEL

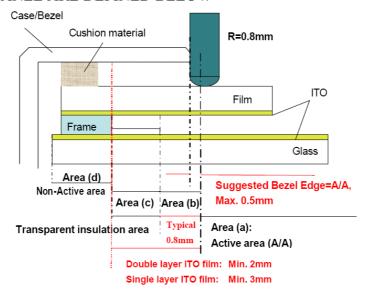
12.2.1 PURPOSE:

IN ORDER TO PREVENT ACCIDENTAL USE AND PERFORMANCE DETERIORATION, PLEASE KEEP THE FOLLOWING PRECAUTIONS AND INHIBITED POINTS.

12.2.2 ITEM AND ILLUSTRATION:

(1) STRUCTURE, AREA DEFINITION

THE STRUCTURE AND THE PERFORMANCE GUARANTEED AREA OF THIS TOUCH PANEL ARE DEFINED BELOW:



THE ABOVE FIGURE IS OUR DESIGN RULE OF TOUCH PANEL.

IF IT CANNOT MEET YOUR REQUIREMENT, PLEASE CONTACT WITH OUR ENGINEERS FOR FURTHER DISCUSSION.

ABOVE FIGURE ILLUSTRATES THE RECOMMENDED BEZEL AND CUSHION DESIGN. IN ORDER TO PREVENT

UNUSUAL PERFORMANCE DEGRADATION AND MALFUNCTION OF A TOUCH PANEL, PLEASE CARRY OUT THE SET

CASE DESIGNING AND A TOUCH PANEL ASSEMBLING METHOD AFTER SURELY CONSIDERING THE DEFINITION OF EACH AREA ILLUSTRATED IN ABOVE FIGURE.

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AREA(a): ACTIVE AREA

THE ACTIVE AREA IS GUARANTEED THE POSITION DATA DETECTABLE PRECISION, OPERATION FORCE AND OTHER OPERATIONS. IT IS STRONGLY RECOMMENDED TO PLACE THE OPERATION BUTTON OR MENU KEYS WITHIN THE ACTIVE AREA. DUE TO STRUCTURE, THE ACTIVE AREA IS LESS DURABLE AT THE EDGE OR CLOSE TO THE EDGE.

AREA(b): OPERATION NON-GUARANTEED AREA

THIS AREA DOES NOT GUARANTEE A TOUCH PANEL OPERATION AND
ITS FUNCTION. WHEN THIS AREA IS PRESSED, TOUCH PANEL SHOWS
DEGRADATION OF ITS PERFORMANCE AND DURABILITY SUCH AS A PEN
SLIDING DURABILITY BECOMES ABOUT ONE-TENTH COMPARED WITH
THE ACTIVE AREA (AREA-(A) AS GUARANTEED AREA) AND ITS
OPERATION FORCE REQUIRES ABOUT DOUBLE. ABOUT 0.5 MM OUTSIDE
FROM A BOUNDARY OF THE ACTIVE AREA CORRESPONDS TO THIS AREA.

AREA(c): PRESSING PROHIBITION AREA

THE AREA WHICH FORBIDS PRESSING, BECAUSE AN EXCESSIVE LOAD IS
APPLIED TO A TRANSPARENT ELECTRODE (ITO) AND A SERIOUS DAMAGE
IS GIVEN TO A TOUCH PANEL FUNCTION BY PRESSING.

AREA(d): NON-ACTIVE AREA
THE AREA DOES NOT ACTIVATE EVEN IF PRESSED.

- (2) CAUTIONS FOR INSTALLING AND ASSEMBLING
 - (i) DO NOT GIVE EXCESSIVE STRAIN TO THE PRODUCT.

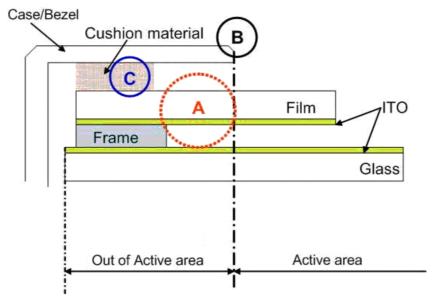
IT MAY CAUSE THE DAMAGE OF THE ITO FILM.

- (ii) FLEXIBLE PATTERN CABLE IS CONNECTED TO THE BODY BY THERMAL PRESSURE METHOD. SO, DO NOT APPLY EXCESSIVE FORCES TO THE FLEXIBLE PATTERN. DO NOT ADD AN EXCESSIVE FORCE TO A FPC (FLEX TAIL) THAT MAKES PEELING OFF OF THE FPC FROM THE PRODUCT. DO NOT FIX, ADHERE OR MOUNT ANY ADDITIONAL GOODS ON THE FPC SUCH AS ADDITIONAL FILM/PLATE ON THE FPC, BECAUSE SUCH ADDITIONAL GOODS WILL APPLY A STRESS AT THE FPC BONDING AREA. IT MAY AFFECT THE CONDUCTIVITY OF FPC WITH TOUCH PANEL.
- (iii) IN ORDER NOT TO APPLY LOAD ON THE DISPLAY, PROVIDE A CLEARANCE OF AT LEAST 0.3MM BETWEEN THE PRODUCT AND DISPLAY.
- (iv) WE RECOMMEND THE DESIGN OF A CASE OR BEZEL SHOULD COVERS THE BOUNDARY OF THE ACTIVE AREA INSIDE IN ORDER TO PREVENT AN OPERATION AT OUTSIDE OF THE ACTIVE AREA WHICH CAN NOT GUARANTEE THE FUNCTION OR DURABILITY (REFER TO ITEM 5.1.2. STRUCTURE, AREA DEFINITION).

 BEZEL'S EDGE PART MAY GUIDE THE PEN SLIDING ON THE SAME POSITION REPEATEDLY. IF THE BEZEL IS PLACED OUTSIDE OF THE ACTIVE AREA,

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(v) PRESSING INSIDE OF BOUNDARY OF THE FRAME(PART (A) AS SHOWN IN BELOW) MAY CAUSES FAULT OPERATION, SO PLEASE DESIGN TO AVOID PRESSING OF TOUCH PANEL AT PART (A) SUCH AS HAVING GASKET/CUSHION AT PART (C). PARTICULARLY THE AREA (B) SHALL BE FREE FROM BURR. THE GASKET/CUSHION MATERIAL AT THE PART (C) SHOULD NOT BE EXCEEDED TO INSIDE OF THE BOUNDARY OF THE FRAME.



- (vi) TO PREVENT GIVING DISTORTION TO THE FILM OF THE PRODUCT AND PEELING OFF OF THE FILM FROM THE PRODUCT, DO NOT FIX THE FILM AND A SET CASE OR A SHOCK ABSORBING MATERIAL ADHERED TO A SET CASE BY ADHESION.
- (vii) WIPE OFF THE STAIN ON THE PRODUCT BY USING SOFT CLOTH MOISTENED WITH ETHANOL. TAKE CARE NOT TO ALLOW ETHANOL TO SOAK INTO THE JOINT OF UPPER FILM AND BOTTOM GLASS. IT MAY OTHERWISE CAUSE PEELING OR DEFECTIVE OPERATION. DO NOT USE ANY ORGANIC SOLVENT OR DETERGENT OTHER THAN ETHANOL.
- (viii) THE CORNERS OF THE PRODUCT ARE NOT CHAMFERED AND ARE SHARP. WHEN POSITIONING AND FIXING THE PRODUCT ON THE CASE, PROVIDE A ROUND PART ON THE CORNER OF THE CASE SO AS NOT TO APPLY LOAD ON THE CORNER OF THE TRANSPARENT TOUCH PANEL.
 - (ix) DO NOT PRESS THE FILM OF THE PRODUCT WHEN THIS PRODUCT IS BUILT INTO A SET.
- (3) CAUTIONS FOR OPERATION
 - (i) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHO'S TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.

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- (ii) THE INPUT POSITION MAY BE FLUCTUATED A LITTLE THROUGH LONG-TIME USE. IT IS DESIRABLE TO PROVIDE A ZERO-ADJUSTMENT FUNCTION BY USING A CIRCUIT AND SOFTWARE.
- (iii) OPERATION AT THE OUT OF ACTIVE AREA IS OUT OF OUR GUARANTEE. IT CAUSES A SERIOUS DAMAGE OF A TRANSPARENT ELECTRODE. DO NOT OPERATE AT THE OUT OF ACTIVE AREA.
- (iv) IN CASE OF CLEANING THE PART OF THE CASE BOUNDARY OF ACCOMPLISHED SET, USE A SOFT CLOTH WITH A FINGER BERRY OR A COTTON BUD. DO NOT CLEAN WITH A THI NG OTHER THAN THE FINGER SUCH AS HARD OR SHARP EDGES LIKE A FINGER NAIL ETC. ON THE CLOTH, BECAUSE IT CAUSE TRANSPARENT CONDUCTIVE FILM CRACKS. PLEASE ADVISE THIS PROHIBITION TO YOUR LAST CUSTOMERS.

12.3 DURABILITY

12.3.1 STYLUS HITTING:

ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFACE PEN: R8 mm SILICON RUBBER

LOAD: 250g

FREQUENCY: 240 times/min MEASUREMENT POSITION:

1 POINT OF TOUCH PANEL ACTIVE AREA

REPEATED: OVER 1,000,000 TIMES

12.3.2 PEN TOUCH SLIDING DURABILITY:

100,000 TIMES OR OVER

WRITING WITH R0.8mm PLASTIC STYLUS PEN; WRITING FORCE 150g IN ACTIVE AREA.

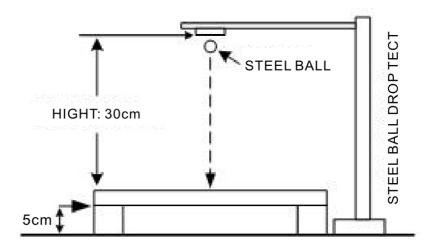
SPEED IS 60mm/sec.

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12.4 STEEL BALL DROP TEST

BY USING F9mm STEEL BALL FROM THE HEIGHT OF 30cm AND FALLING ON TOUCH PANEL SURFACE, MUST PASS BELOW CONDITIONS:

APPEARANCE: THE APPEARANCE WITHOUT ANY CHANGE, INCLUDING THE PANEL BROKEN.



12.5 APPEARANCE INSPECTION

PURPOSE:

TO ESTABLISH APPEARANCE STANDARD AND MAINTAIN PRODUCT QUALITY \circ

SCOPE:

TOUCH PANEL VIEW AREA WITHIN TOUCH PANEL •

12.5.1 RULE:

INSPECTION CONDITION

- (A) ENVIRONMENTAL LUMINANCE: 500 LUX •
- (B) DISTANCE BETWEEN HUMAN EYES AND PANEL: 30 CM (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT) •
- (C) VISUAL ANGEL : $> 60^{\circ}$
- (D) LIGHT SOURCE: FLUORESCENT LIGHT SOURCE •

12.5.2 JUDGE CRITERION:

JUDGEMENT UNDER ABOVE MENTIONED CRITERION (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT),

TESTING GOODS DEFECT CAN BE VISIBLE WITHIN 10 SECONDS, WHICH WILL BE JUDGED AS MAJOR DEFECTS •

SAMPLING STANDARD:

THE SAMPLING STANDARD WILL BE CONFIRMED BY BOTH OF EDT AND CUSTOMER.

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INSPECTION ITEMS	SEPC.		JUDGE CRITERION	OPERATION GUIDELINE
	W ≤ 0.11	mm & L≤10mm	ACCEPTABLE	REFL BACK GROUND
SCRATCH	$W > 0.1 mm \text{ or } L > 10 mm$ $W \le 0.1 mm \& L \le 5 mm$		NOT ACCEPTABLE	TESTING GOODS FLUORESCENT LIGHT SOURCE
LINEAR FOREIGN OBJECT			ACCEPTABLE	300mm
	W > 0.1	mm or L >5mm	NOT ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux REFL
GRANULAR FOREIGN OBJECT	D	≤ 0.3mm	ACCEPTABLE	FLUORESCENT LIGHT SOURCE TESTING GOODS 300mm
	D >0.3mm		NOT ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux
PET BUBBLES	D ≤0.6mm		ACCEPTABLE	D O
TET BOBBLES	D	>0.6mm	NOT ACCEPTABLE	D
CHIP ON GLASS	CORNER	$X \le 3$ mm $Y \le 3$ mm $Z < t$ t = t	ACCEPTABLE	Chip of glass
	EDGE $W \le 3 \text{mm} \cdot Y \le 3 \text{mm} \cdot Z <$			Z X X X

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13. INSPECTION CRITERION

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

13.2 INSPECTION CONDITIONS

13.2.1 (1)OBSERVATION DISTANCE: 35cm±5cm

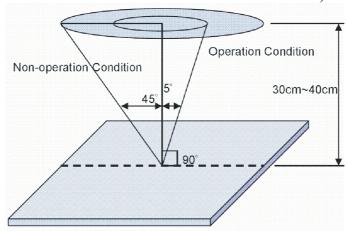
(2) VIEW ANGLE:

NON-OPERATION CONDITION: ±5°

(PERPENDICULAR TO LCD PANEL SURFACE)

OPERATION CONDITION: ±45°

(PERPENDICULAR TO LCD PANEL SURFACE)



13.2.2 ENVIRONMENT CONDITIONS:

AMBIENT TEMPERATURE		20°C~25°C
AMBIENT HUMIDITY		65±20%RH
AMBIENT	COSMETIC INSPECTION	MORE THAN 600Lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 Lux

13.2.3 INSPECTION LOT QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY (a)APPLICABLE STANDARD:

MIL-STD-105E

NORMAL INSPECTION, SINGLE SAMPLING

Level ∏

(b)AQL : MAJOR DEFECT : AQL 0.65 MINOR DEFECT : AQL 1.0

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MA JOB DEFECT	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
MAJOR DEFECT	2.BACKLIGHT	NO LIGHTFLICKERING AND OTHER ABNORMAL ILLUMINATION	0.65
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED 	
MINOR DEFECT	2.BEZEL ZONE	STAINSSCRATCHESFOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

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13.3.2 MODULE DEFECTS CALSSIFICATION

NO.	ITEM		CRIT	ΓERIA	
1.	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC			
2.	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND SPEC			
3.	DOT DEFECT	AND BLUE SC (2) BRIGHT DOT DARK DOT TOAL BRIGHT NOTE: 1. THE DEFINITIO THE SIZE OF A REGARDED AS 2. BRIGHT DOT: DOTS APPEAR PANEL IS DISPI 3. DARK DOT: DOTS APPEAR	REENS. TEMS AND DARK DOTS ON OF DOT: DEFECTIVE DOT ONE DEFECTIVE BRIGHT AND UNCLAYING UNDER B DARK AND UNCH	CHANGED IN SIZE IN	E DOT IS N WHICH LCD
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	LENGTH: L $L \le 0.3$ $0.3 < L \le 2.5$ $2.5 < L$ WIDTH: W mm, 1	WIDTH: W $W \le 0.05$ $0.05 < W \le 0.1$ $0.1 < W$	PERMISSIBLE NO. IGNORE 4 NONE	
5.	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	AVERAGE DIAMETER (mm): D $D \le 0.15$ $0.15 < D \le 0.5$ $0.5 < D$ NOTE: DIAMETER D=(a+b)/2		NUMBER OF PIECES IGNORE 4 NONE	

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NO.	ITEM		CRITERIA		
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED	
		DUDDI E ON THE	D ≤ 0.25	IGNORE	
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	N ≤ 5	
		TOLARIZER	0.5 < D	NOTE	
		SURFACE STATUS	D < 0.1 mm	IGNORE	
		SCIUTICE SITUOS	$0.1 < D \le 0.3$ mm	N ≤ 3	
		CF FAIL / SPOT	D < 0.1 mm	IGNORE	
			$0.1 < D \le 0.3$ mm	N ≤ 3	
BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS		NOTE: (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2			
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW			
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	SLIGHT VISIBLE THROU	NG 6% ND FILTER	
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMIN	ED BASED UPON THE ST	TANDARD SAMPLE.	
10.	BEZEL APPEARANCE	PRINTS STAINS O	HAVE RUST, BE DEFORM F OTHER CONTAMINATI MPLY WITH JOB SPECIFION	ION.	
11	РСВ	THE SEAL AREA (THAN THREE PLA (2)NO OXIDATION O (3)PARTS ON PCB MI CHARACTERISTIC THERE SHOULD B PARTS. (4)THE JUMPER ON T CHARACTERISTIC (5)IF SOLDER GETS (R CONTAMINATION PCE UST BE THE SAME AS OF CCHART. E NO WRONG PARTS, M	E SHOULD BE NO MORE B TERMINALS. N THE PRODUCTION ISSING PARTS OR EXCESS ORM TO THE PRODUCT ED PAD, ZEBRA PAD OR	

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NO.	ITEM	CRITERIA
		(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICENT SOLDER (a)LSI, IC
		A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD
		SOLDER FILLET
		(b)CHIP COMPONENT • SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING
		SOLDER FILLET
12. SOLDEI	RING	SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED
	SOLDER	
		(3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE

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NO.	ITEM	CRITERIA	
	SOLDERING	(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE	
12.			
		 (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB. 	
13.	BACKLIGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.	
14.	GENERAL APPEARANCE	 (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG. (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY 	

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NO. ITEM			CRITERIA		
		THE LCD WITH EXTENSIVE OF GENERAL GLASS CHIP:	$ \begin{array}{c c} a \\ & \leq t/2 \\ \hline & t/2 > , \leq 2t \\ \hline *W=DISTANCI \end{array} $	b < VIEWING AREA ≤ W/2 E BETWEEN AREA AND LOOGE E LENGTH	c ≤ 1/8X ≤ 1/8X
15. CI	15. CRACKED GLASS	CORNER PART: CHIP ON ELECTRODE PAD a	PANEL ED $X = LCD SIDI$ $t = GLASS TI$ a $\leq t$ $* X=LCD SIDE$	AREA AND LODGE E LENGTH HICKNESS b ≤ 0.5mm WIDTH	c ≤ 1/8X ≤ 1/8X CD
	c a	©IF GLASS CH TERMINAL, REMAIN AN ACCORDING TERMINAL ©IF THE PROD SEALED BY	b ≤ 1/8X WIDTH HICKNESS DE PAD LENGT HIPPING THE I' OVER 2/3 OF ' ND BE, INSPEC G TO ELECTRO SPECIFICATIO DUCT WILL BE THE CUSTOM MENT MARK N	TO THE ITO MU TED DDE DNS THEAT ER,	

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13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +60°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -10°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C , 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 25 CYCLES OF OPERATION: -20°C FOR 60 MINUTES ~ +70°C FOR 60 MINUTES
7	(EEEE TROSTATIC	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

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13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

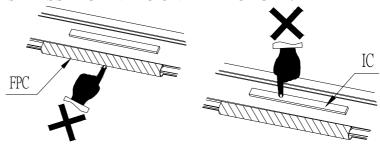
FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 12.5, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	DEEED TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE. IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!



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13.7 NOTICE

- 13.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC -PROTECTED MATERIAL.
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW. WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.